



SINGLE 2 INPUT EXCLUSIVE OR GATE

Description

The 74LVC1G86 is a single 2-input EXCLUSIVE OR gate with a standard push-pull output. The device is designed for operation with a power supply range of 1.65V to 5.5V. The inputs are tolerant to 5.5V allowing this device to be used in a mixed voltage environment. The device is fully specified for partial power down applications using I_{OFF}. The I_{OFF} circuitry disables the output preventing damaging current backflow when the device is powered down.

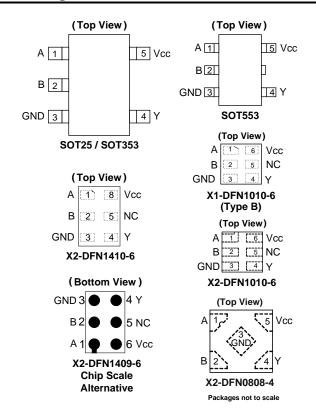
The gate performs the positive Boolean function:

$$Y = A \oplus B$$
 or $Y = \overline{A}B + A\overline{B}$

Features

- Wide Supply Voltage Range from 1.65 to 5.5V
- ± 24mA Output Drive at 3.3V
- **CMOS Low Power Consumption**
- I_{OFF} Supports Partial-Power-Down Mode Operation
- Inputs Accept Up to 5.5V
- ESD Protection Tested per JESD 22
 - Exceeds 200-V Machine Model (A115)
 - Exceeds 2000-V Human Body Model (A114)
 - Exceeds 1000-V Charged Device Model (C101)
- Latch-Up Exceeds 100mA per JESD 78, Class I
- Range of Package Options
- Direct Interface with TTL Levels
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Pin Assignments



Applications

- Voltage Level Shifting
- General Purpose Logic
- Power Down Signal Isolation
- Wide Array of Products Such as:
 - PCs, Networking, Notebooks, Netbooks, PDAs
 - Tablet Computers. E-readers
 - Computer Peripherals, Hard Drives, CD/DVD ROM

April 2016

- TV, DVD, DVR, Set Top Box
- Cell Phones, Personal Navigation / GPS
- MP3 Players, Cameras, Video Recorders

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green"
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

-7: 7" Tape & Reel



Ordering Information (Note 4)

Logic Device Function Package Packing

74 : Logic Prefix LVC : 1.65 to 5.5 V Logic Family 1G : One Gate 86 : 2-Input
Exclusive OR
Gate

W5 : SOT25 SE : SOT353 Z : SOT553

Z : SOT553 FS3 : X2-DFN0808-4

FW5 :X1-DFN1010-6 (Type B) FW4 :X2-DFN1010-6

FX4: X2- DFN1409-6 FZ4: X2- DFN1410-6

Part Number	Package	Package	Package	7" Tape a	and Reel
Part Number	Code	(Notes 5 & 6)	Size	Quantity	Part Number Suffix
74LVC1G86W5-7	W5	SOT25	3.0mm x 2.8mm x 1.2mm 0.95mm lead pitch	3,000/Tape & Reel	-7
74LVC1G86SE-7	SE	SOT353	2.0mm x 2.0mm x 1.1mm 0.65mm lead pitch	3,000/Tape & Reel	-7
74LVC1G86Z-7	Z	SOT553	1.6mm x 1.6 mm x 0.62mm 0.5mm lead pitch	4,000/Tape & Reel	-7
74LVC1G86FS3-7	FS3	X2-DFN0808-4	0.8mm x 0.8 mm x 0.35mm 0.5mm pad pitch (diamond)	5,000/Tape & Reel	-7
74LVC1G86FW5-7	FW5	X1-DFN1010-6 (Type B)	1.0mm x 1.0mm x 0.5mm 0.35mm pad pitch	5,000/Tape & Reel	-7
74LVC1G86FW4-7	FW4	X2-DFN1010-6	1.0mm x 1.0mm x 0.4mm 0.35mm pad pitch	5,000/Tape & Reel	-7
74LVC1G86FX4-7	FX4	X2-DFN1409-6 (Chip scale alternative)	1.4mm x 0.9mm x 0.4mm 0.5mm pad pitch	5,000/Tape & Reel	-7
74LVC1G86FZ4-7	FZ4	X2-DFN1410-6	1.4mm x 1.0mm x 0.4mm 0.5mm pad pitch	5,000/Tape & Reel	-7

Notes:

- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.
- 5. Pad layout as shown in Diodes Inc. suggested pad layouts, which can be found on our website at see http://www.diodes.com/package-outlines.html.
- 6. The taping orientation is located on our website at http://www.diodes.com/datasheets/ap02007.pdf.

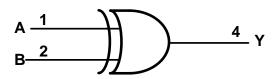
Pin Descriptions

Pin Name	Description
Α	Data Input
В	Data Input
GND	Ground
Υ	Data Output
Vcc	Supply Voltage
NC	No Connection

Function Table

Inp	Output	
Α	В	Y
Н	Н	L
L	Н	Н
Н	L	Н
L	L	L

Logic Diagram





Absolute Maximum Ratings (Notes 7 & 8) (@T_A = +25°C, unless otherwise specified.)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	kV
ESD CDM	Charged Device Model ESD Protection	1	kV
ESD MM	Machine Model ESD Protection	200	V
V _{CC}	Supply Voltage Range	-0.5 to 6.5	V
VI	Input Voltage Range	-0.5 to 6.5	V
Vo	Voltage Applied to Output in High Impedance or IOFF state	-0.5 to 6.5	V
Vo	Voltage applied to Output in High or Low state.	-0.5 to V _{CC} +0.5	V
l _{IK}	Input Clamp Current V _I < 0	-50	mA
lok	Output Clamp Current	-50	mA
lo	Continuous Output Current	±50	mA
Icc, Ignd	Continuous Current Through V _{CC} or GND	±100	mA
TJ	Operating Junction Temperature	-40 to +150	°C
T _{STG}	Storage Temperature	-65 to +150	°C

Notes:

Recommended Operating Conditions (Note 9) (@T_A = +25°C, unless otherwise specified.)

Symbol		Parameter	Min	Max	Unit
V	Operating Voltage	Operating	1.65	5.5	V
V _{CC}	Operating voltage	Data Retention Only	1.5	_	V
		V _{CC} = 1.65V to 1.95V	0.65 x V _{CC}		
\ /	High-Level Input Voltage	V _{CC} = 2.3V to 2.7V	1.7	_	V
V _{IH}	High-Level Input Voltage	V _{CC} = 3V to 3.6V	2	_	V
		V _{CC} = 4.5V to 5.5V	0.7 x V _{CC}	_	
		V _{CC} = 1.65V to 1.95V	_	0.35 x V _{CC}	
	Law Law Law Law Mark College	V _{CC} = 2.3V to 2.7V	_	0.7	V
V_{IL}	Low-Level Input voltage	V _{CC} = 3V to 3.6V	_	0.8	V
		V _{CC} = 4.5V to 5.5V	_	0.3 x V _{CC}	
VI	Input Voltage		0	5.5	V
Vo	Output Voltage		0	V _{CC}	V
		V _{CC} = 1.65V	_	-4	
		V _{CC} = 2.3V	_	-8	
1	High-Level Output Current	V _{CC} = 2.7V	_	-12	mA
I _{OH}	I light-Level Output Outrett	V 2V	_	-16	ША
		V _{CC} = 3V	_	-24	
		V _{CC} = 4.5V	_	-32	
		V _{CC} = 1.65V	_	4	
		$V_{CC} = 2.3V$	_	8	
loL	Low-Level Output Current	$V_{CC} = 2.7V$	_	12	mA
IOL	Low Level Output Outlent	Vcc = 3V	_	16	IIIA
		VCC = 3V	_	24	
		V _{CC} = 4.5V	_	32	
	lanut Transition Disc on Fall	$V_{CC} = 1.8V \pm 0.15V, 2.5V \pm 0.2V$	_	20	
$\Delta t/\Delta V$	Input Transition Rise or Fall Rate	$V_{CC} = 3.3V \pm 0.3V$	_	10	ns/V
		$V_{CC} = 5V \pm 0.5V$	_	5	
TA	Operating Free-Air Temperature	_	-40	+125	°C

Note: 9. Unused inputs should be held at V_{CC} or Ground.

^{7.} Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device

operation should be within recommend values.

8. Forcing the maximum allowed voltage could cause a condition exceeding the maximum current or conversely forcing the maximum current could cause a condition exceeding the maximum voltage. The ratings of both current and voltage must be maintained within the controlled range.



Electrical Characteristics (All typical values are at $V_{CC} = 3.3V$, $T_A = +25$ °C)

Symbol	Parameter	Test Conditions	V	-4	0°C to +85°	С	-40°C to	+125°C	Unit
Symbol	Parameter	rest Conditions	V _{CC}	Min	Тур	Max	Min	Max	Unit
		$I_{OH} = -100 \mu A$	1.65V to 5.5V	V _{CC} – 0.1	1	_	V _{CC} – 0.1	1	
		$I_{OH} = -4mA$	1.65V	1.2		_	0.95		
	l	$I_{OH} = -8mA$	2.3V	1.9	_	_	1.7	_	
VoH	High Level Output Voltage	I _{OH} = -12mA	2.7V	2.2	_	_	1.9	_	V
	Output Voltago	I _{OH} = -16mA	3V	2.4	_	_	2.2	_	
		I _{OH} = -24mA	30	2.3	_	_	2.0	_	
		I _{OH} = -32mA	4.5V	3.8	_	_	3.4	_	
		I _{OL} = 100μA	1.65V to 5.5V		1	0.1	_	0.1	
		$I_{OL} = 4mA$	1.65V		1	0.45	_	0.7	V
	l	$I_{OL} = 8mA$	2.3V		_	0.3	_	0.45	
V_{OL}	Low Level Output Voltage	I _{OL} = 12mA	2.7V	_	_	0.4	_	0.6	
	Output Voltago	I _{OL} = 16mA	3V	_	_	0.4	_	0.6	
		$I_{OL} = 24mA$	3V	_	_	0.55	_	0.8	
		$I_{OL} = 32mA$	4.5V	_	_	0.55	_	.8	
II	Input Current	V _I = 5.5 V or GND	0 to 5.5V	_	± 0.1	±5	_	± 100	μA
I _{OFF}	Power Down Leakage Current	V_I or $V_O = 5.5V$	0V		_	±10	_	±200	μA
Icc	Supply Current	V _I = 5.5V or GND I _O = 0	5.5V	_	0.1	10	_	200	μA
Δl _{CC}	Additional Supply Current	One input at V _{CC} –0.6V Other inputs at V _{CC} or GND	3V to 5.5V	_	_	500	_	5,000	μA
C _i	Input Capacitance	$V_I = V_{CC} - \text{or GND}$	3.3V	_	5	_	_	_	pF

Package Characteristics (All typical values are at $V_{CC} = 3.3V$, $T_A = +25$ °C)

Symbol	Parameter	Test Conditions	V _{CC}	Min	Тур	Max	Unit
		SOT25		_	204	_	
		SOT353		_	371	_	
		SOT553		_	231	_	
0	Thermal Resistance	X2-DFN0808-4	(Note 10)	_	400	_	°C/W
θ_{JA}	Junction-to-Ambient	X1-DFN1010-6 (Type B)	(Note 10)	_	435	_	*C/VV
		X2-DFN1010-6		_	445	_	
		X2-DFN1409-6		_	470	_	
		X2-DFN1410-6		_	460	_	
		SOT25		_	52	_	
		SOT353		_	143	_	
		SOT553		_	105	_	
0	Thermal Resistance	X2-DFN0808-4	(Note 10)	_	225	_	°C/W
θЈС	Junction-to-Case	X1-DFN1010-6 (Type B)	(Note 10)	_	250	_	*C/VV
		X2-DFN1010-6		_	250	_	
		X2-DFN1409-6			275	_	
		X2-DFN1410-6		_	265	_	

Note: 10. Test condition for each of the 8 package types: Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.



Switching Characteristics

Figure 1 Typical Values at $T_A = +25$ °C and nominal voltages 1.8V, 2.5V, 2.7V, 3.3V, and 5.0V.

Parameter	From	То	V	T,	$T_A = -40$ °C to $+85$ °C		T _A = -40°C to +125°C		Unit
Farameter	Input	Output	Vcc	Min	Тур	Max	Min	Max	Unit
			1.8V ± 0.15V	1.0	3.7	9.9	1.0	13.0	
			2.5V ± 0.2V	0.5	2.5	5.5	0.5	7.0	
t _{pd}	A or B	Υ	2.7V	0.5	2.8	5.8	0.5	7.5	ns
			3.3V ± 0.3V	0.5	2.3	5.0	0.5	6.5	
			5.0V ± 0.5V	0.5	1.9	4.0	0.5	5.5	

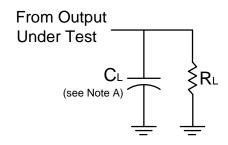
Operating Characteristics

 $T_{\Delta} = +25^{\circ}C$

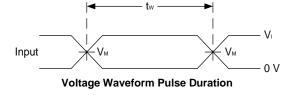
Parameter		Test Conditions	V _{CC} = 1.8V Typ	V _{CC} = 2.5V Typ	V _{CC} = 3.3V Typ	V _{CC} = 5V Typ	Unit
C _{pd}	Power Dissipation Capacitance	f = 10 MHz	14	14	14	14	pF

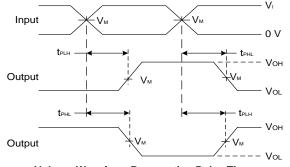


Parameter Measurement Information



v	Inputs			В	
V _{CC}	VI	t _r /t _f	- V _M	C _L	R_L
1.8V ± 0.15V	Vcc	≤2ns	V _{CC} /2	30pF	1kΩ
2.5V ± 0.2V	Vcc	≤2ns	V _{CC} /2	30pF	500Ω
2.7V	Vcc	≤2.5ns	1.5V	50pF	500Ω
3.3V ± 0.3V	3.0V	≤2.5ns	1.5V	50pF	500Ω
5.0V ± 0.5V	Vcc	≤2.5ns	V _{CC} /2	50pF	500Ω





Voltage Waveform Propagation Delay Times Inverting and Non Inverting Outputs

Figure 1. Load Circuit and Voltage Waveforms

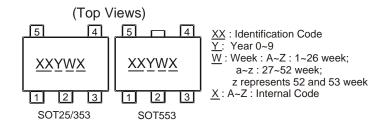
Notes:

- A. Includes test lead and test apparatus capacitance.
 B. All pulses are supplied at pulse repetition rate ≤ 10MHz.
 C. Inputs are measured separately one transition per measurement.
- D. t_{PLH} and t_{PHL} are the same as t_{PD}.



Marking Information

(1) SOT25, SOT353 and SOT553



Part Number	Package	Identification Code
74LVC1G86W5-7	SOT25	UX
74LVC1G86SE-7	SOT353	UX
74LVC1G86Z-7	SOT553	UX

(2) DFN Packages

(Top View)

<u>XX</u> <u>Y W X</u> $\begin{array}{l} \underline{XX} : \text{Identification Code} \\ \underline{Y} : \text{ Year 0~9} \\ \underline{W} : \text{Week : A~Z : 1~26 week;} \end{array}$

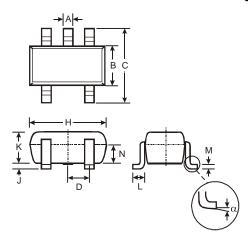
a~z: 27~52 week; z represents 52 and 53 week X: A~Z: Internal Code

Part Number Package Identification Code 74LVC1G86FS3-7 X2-DFN0808-4 WX74LVC1G86FW5-7 X1-DFN1010-6 (Type B) VX 74LVC1G86FW4-7 X2-DFN1010-6 UX 74LVC1G86FX4-7 X2-DFN1409-6 MV 74LVC1G86FZ4-7 X2-DFN1410-6 UX



Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT25

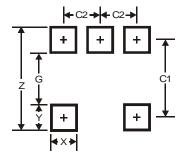


	SOT25							
Dim	Min	Max	Тур					
Α	0.35	0.50	0.38					
В	1.50	1.70	1.60					
O	2.70	3.00	2.80					
ם	-	-	0.95					
Ξ	2.90	3.10	3.00					
J	0.013	0.10	0.05					
K	1.00	1.30	1.10					
L	0.35	0.55	0.40					
M	0.10	0.20	0.15					
N	0.70	0.80	0.75					
α	0°	8°	-					
All D	imensi	ons in	mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT25

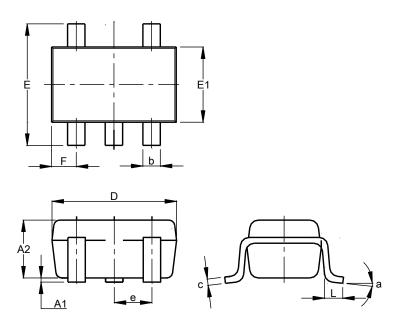


Dimensions	Value
Z	3.20
G	1.60
Х	0.55
Y	0.80
C1	2.40
C2	0.95



Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT353

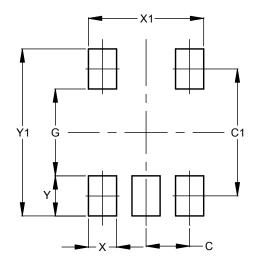


	SOT353			
Dim	Min	Max	Тур	
A1	0.00	0.10	0.05	
A2	0.90	1.00	1.00	
b	0.10	0.30	0.25	
С	0.10	0.22	0.11	
D	1.80	2.20	2.15	
Е	2.00	2.20	2.10	
E1	1.15	1.35	1.30	
е	C).650 B	SC	
F	0.40	0.45	0.425	
L	0.25	0.40	0.30	
а	0°	8°		
All Dimensions in mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT353

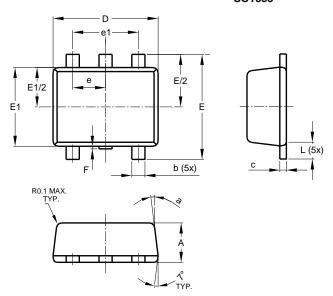


Dimensions	Value (in mm)
С	0.650
C1	1.900
G	1.300
X	0.420
X1	1.720
Y	0.600
V1	2 500



Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT553

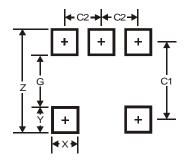


SOT553			
Dim	Min	Max	Тур
Α	0.55	0.62	0.60
b	0.15	0.30	0.20
С	0.10	0.18	0.15
D	1.50	1.70	1.60
Е	1.55	1.70	1.60
E1	1.10	1.25	1.20
е	0.50 BSC		
e1	e1 1.00 BSC		
F	0.00	0.10	_
L	0.10	0.30	0.20
а	6°	8°	7°
All Dimensions in mm			

Suggested Pad Layout

 $Please \ see \ http://www.diodes.com/package-outlines.html \ for \ the \ latest \ version.$

SOT553

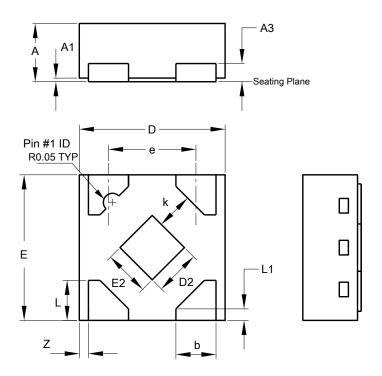


Dimensions	Value
Z	2.2
G	1.2
Х	0.375
Y	0.5
C1	1.7
C2	0.5



Please see http://www.diodes.com/package-outlines.html for the latest version.

X2-DFN0808-4

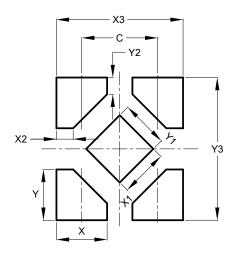


X2-DFN0808-4			
Dim	Min	Max	Тур
Α	0.25	0.35	0.30
A1	0	0.04	0.02
А3	-	-	0.13
b	0.17	0.27	0.22
D	0.75	0.85	0.80
D2	0.15	0.35	0.25
Е	0.75	0.85	0.80
E2	0.15	0.35	0.25
е		-	0.48
k	0.20	-	-
Г	0.17	0.27	0.22
L1	0.02	0.12	0.07
z	-	-	0.05
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

X2-DFN0808-4

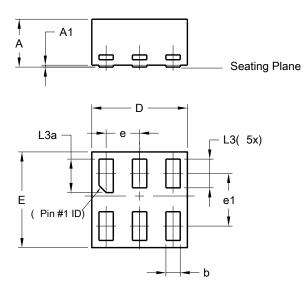


Dimensions	Value
С	0.480
X	0.320
X1	0.300
X2	0.106
Х3	0.800
Y	0.320
Y1	0.300
Y2	0.106
Y3	0.900



Please see http://www.diodes.com/package-outlines.html for the latest version.

X1-DFN1010-6 (Type B)

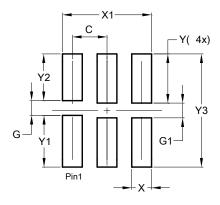


	X1-DFN1010-6 (Type B)				
Dim					
Α	-	0.50	0.39		
A1	-	0.04	-		
b	0.12	0.20	0.15		
D	0.95	1.050	1.00		
Е	0.95	1.050	1.00		
e 0.35 BSC					
e1	e1 0.55 BSC				
L3	0.27	0.30	0.30		
L3a	0.32	0.40	0.35		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

X1-DFN1010-6 (Type B)

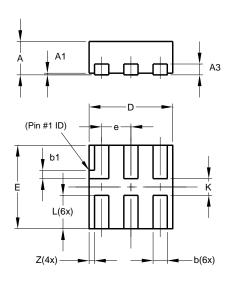


Dimensions	Value	
פווטופווסוווט	(in mm)	
С	0.350	
G	0.150	
G1	0.150	
Х	0.200	
X1	0.900	
Y	0.500	
Y1	0.525	
Y2	0.475	
Y3	1.150	



Please see http://www.diodes.com/package-outlines.html for the latest version.

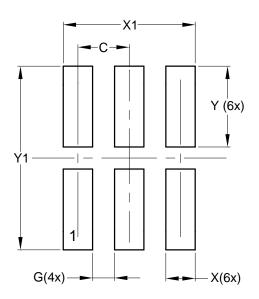
X2-DFN1010-6



X2-DFN1010-6			
Dim	Min	Max	Тур
Α		0.40	0.39
A1	0.00	0.05	0.02
A3			0.13
b	0.14	0.20	0.17
b1	0.05	0.15	0.10
D	0.95	1.05	1.00
E	0.95	1.05	1.00
е			0.35
L	0.35	0.45	0.40
K	0.15		
Z			0.065
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



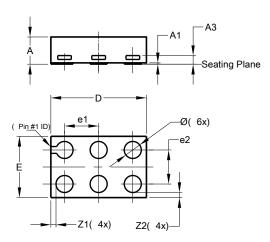
X2-DFN1010-6

Dimensions	Value (in mm)
С	0.350
G	0.150
Х	0.200
X1	0.900
Y	0.550
Y1	1.250



Please see http://www.diodes.com/package-outlines.html for the latest version.

X2-DFN1409-6 CHIP SCALE ALTERNATIVE

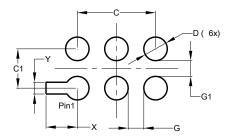


	X2-DFN1409-6					
Dim	Dim Min Max Typ					
Α	-	0.40	0.39			
A1	0	0.05	0.02			
A3	-	-	0.13			
Ø	0.20	0.30	0.25			
D	1.35	1.45	1.40			
Е	0.85	0.95	0.90			
e1	-	-	0.50			
e2	-	-	0.50			
Z 1	-	-	0.075			
Z2	-	-	0.075			
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

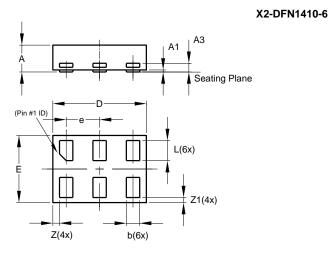
X2-DFN1409-6 CHIP SCALE ALTERNATIVE



Dimensions	Value	
Dilliensions	(in mm)	
С	1.000	
C1	0.500	
D	0.300	
G	0.200	
G1	0.200	
X	0.400	
Υ	0.150	



Please see http://www.diodes.com/package-outlines.html for the latest version.

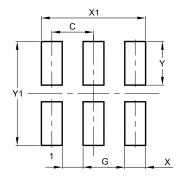


X2-DFN1410-6			
Dim	Min	Max	Тур
Α		0.40	0.39
A1	0.00	0.05	0.02
А3	_	_	0.13
b	0.15	0.25	0.20
D	1.35	1.45	1.40
Е	0.95	1.05	1.00
е			0.50
L	0.25	0.35	0.30
Z		_	0.10
Z 1	0.045	0.105	0.075
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

X2-DFN1410-6



Dimensions	(in mm)
С	0.500
G	0.250
Х	0.250
X1	1.250
Y	0.525
Y1	1.250



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