



1. Description

1.1. Project

| | |
|-----------------|--------------------------------|
| Project Name | motorc_board2_mitsuba_emulator |
| Board Name | custom |
| Generated with: | STM32CubeMX 6.4.0 |
| Date | 02/15/2022 |

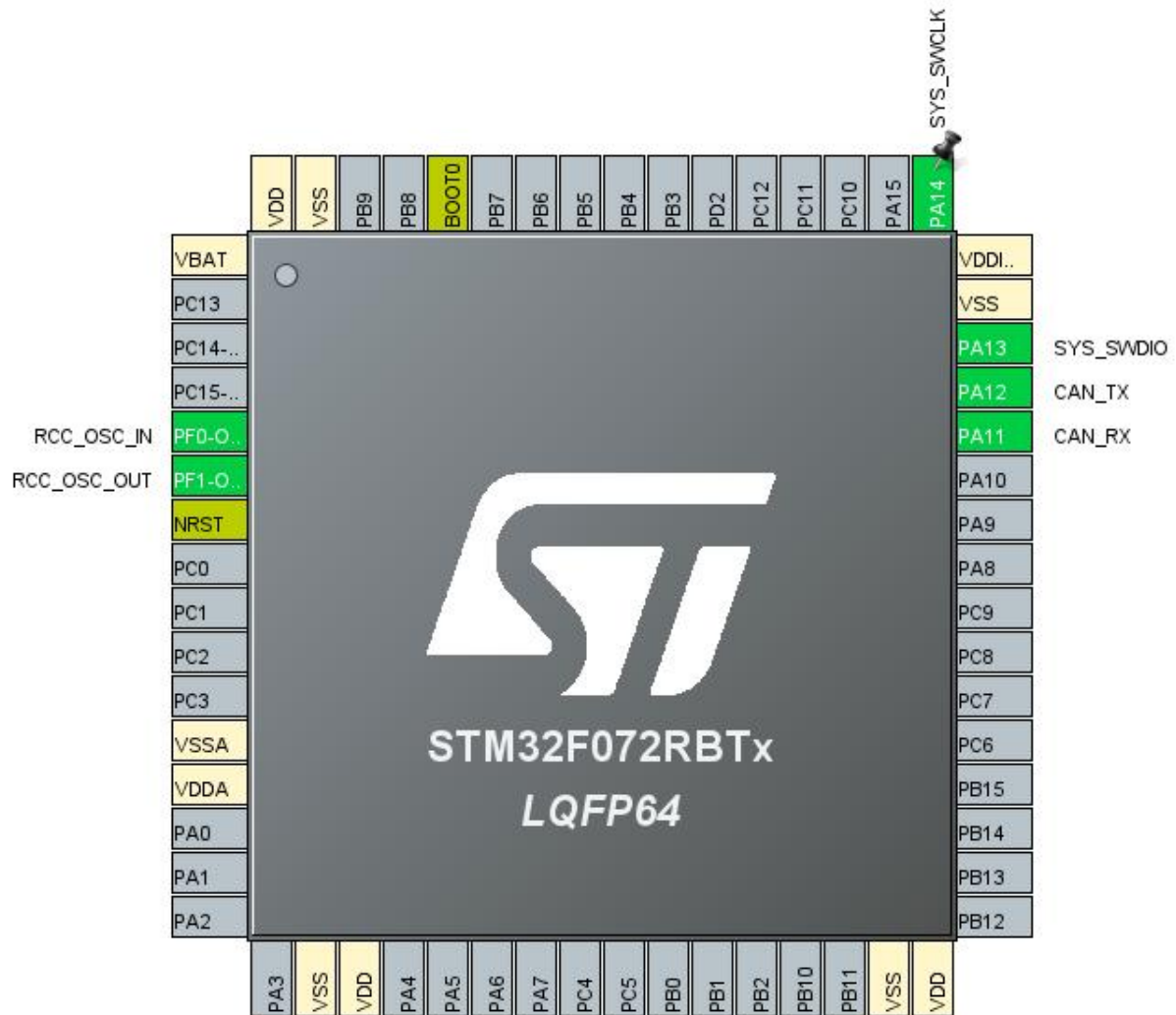
1.2. MCU

| | |
|----------------|---------------|
| MCU Series | STM32F0 |
| MCU Line | STM32F0x2 |
| MCU name | STM32F072RBTx |
| MCU Package | LQFP64 |
| MCU Pin number | 64 |

1.3. Core(s) information

| | |
|---------|---------------|
| Core(s) | Arm Cortex-M0 |
|---------|---------------|

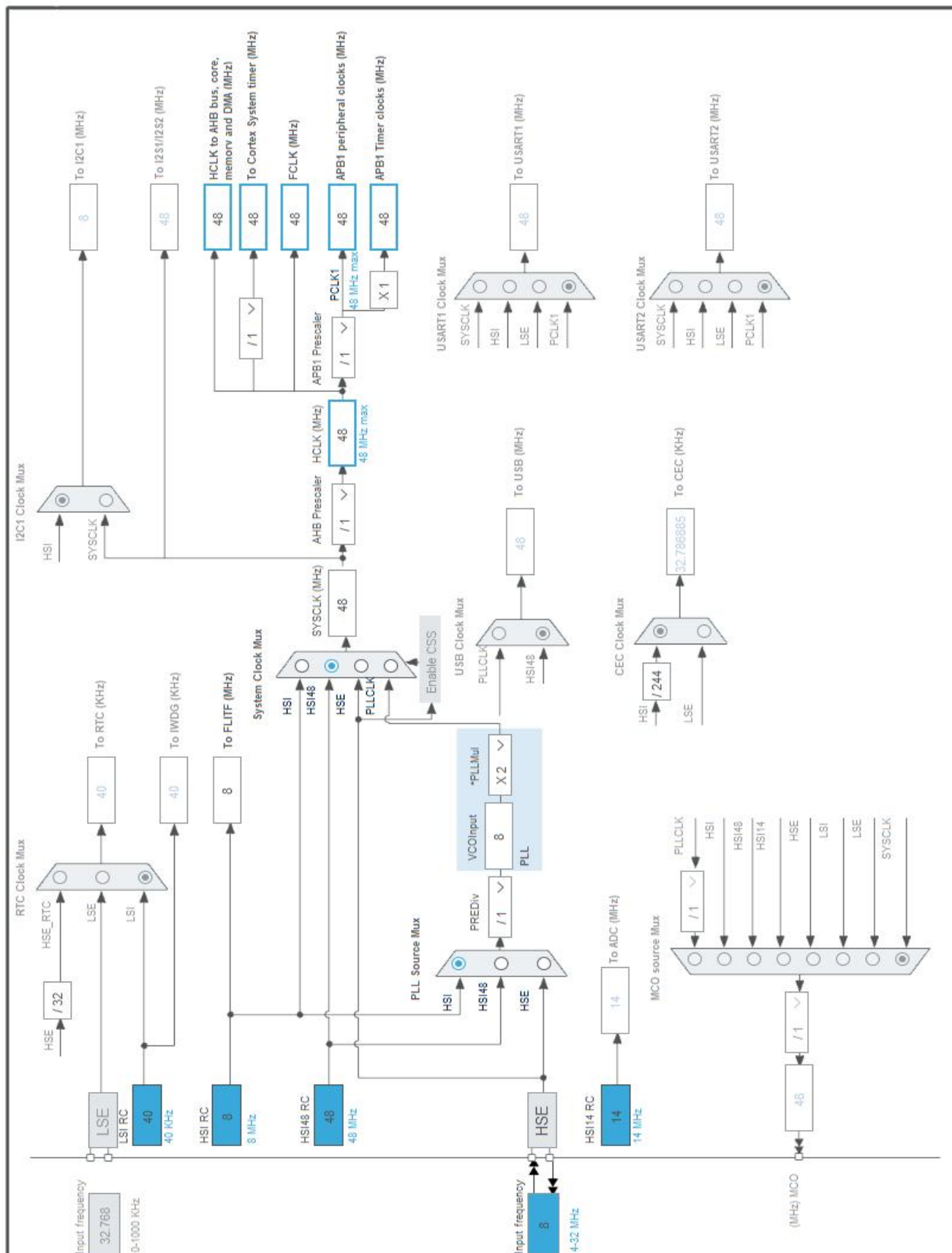
2. Pinout Configuration



3. Pins Configuration

| Pin Number LQFP64 | Pin Name (function after reset) | Pin Type | Alternate Function(s) | Label |
|----------------------|---------------------------------------|----------|--------------------------|-------|
| 1 | VBAT | Power | | |
| 5 | PF0-OSC_IN | I/O | RCC_OSC_IN | |
| 6 | PF1-OSC_OUT | I/O | RCC_OSC_OUT | |
| 7 | NRST | Reset | | |
| 12 | VSSA | Power | | |
| 13 | VDDA | Power | | |
| 18 | VSS | Power | | |
| 19 | VDD | Power | | |
| 31 | VSS | Power | | |
| 32 | VDD | Power | | |
| 44 | PA11 | I/O | CAN_RX | |
| 45 | PA12 | I/O | CAN_TX | |
| 46 | PA13 | I/O | SYS_SWDIO | |
| 47 | VSS | Power | | |
| 48 | VDDIO2 | Power | | |
| 49 | PA14 | I/O | SYS_SWCLK | |
| 60 | BOOT0 | Boot | | |
| 63 | VSS | Power | | |
| 64 | VDD | Power | | |

4. Clock Tree Configuration



5. Software Project

5.1. Project Settings

| Name | Value |
|-----------------------------------|--|
| Project Name | motorc_board2_mitsuba_emulator |
| Project Folder | C:\Users\ajp47\source\repos\AuxCAN_Example\Auxiliary_2019- |
| Toolchain / IDE | STM32CubeIDE |
| Firmware Package Name and Version | STM32Cube FW_F0 V1.11.3 |
| Application Structure | Advanced |
| Generate Under Root | Yes |
| Do not generate the main() | No |
| Minimum Heap Size | 0x200 |
| Minimum Stack Size | 0x400 |

5.2. Code Generation Settings

| Name | Value |
|---|---------------------------------------|
| STM32Cube MCU packages and embedded software | Copy only the necessary library files |
| Generate peripheral initialization as a pair of '.c/.h' files | No |
| Backup previously generated files when re-generating | No |
| Keep User Code when re-generating | Yes |
| Delete previously generated files when not re-generated | Yes |
| Set all free pins as analog (to optimize the power consumption) | No |
| Enable Full Assert | No |

5.3. Advanced Settings - Generated Function Calls

| Rank | Function Name | Peripheral Instance Name |
|------|--------------------|--------------------------|
| 1 | MX_GPIO_Init | GPIO |
| 2 | SystemClock_Config | RCC |
| 3 | MX_CAN_Init | CAN |

6. Power Consumption Calculator report

6.1. Microcontroller Selection

| | |
|-----------|---------------|
| Series | STM32F0 |
| Line | STM32F0x2 |
| MCU | STM32F072RBTx |
| Datasheet | DS9826_Rev5 |

6.2. Parameter Selection

| | |
|-------------|-----|
| Temperature | 25 |
| Vdd | 3.6 |

6.3. Battery Selection

| | |
|-------------------|-----------------|
| Battery | Li-SOCL2(A3400) |
| Capacity | 3400.0 mAh |
| Self Discharge | 0.08 %/month |
| Nominal Voltage | 3.6 V |
| Max Cont Current | 100.0 mA |
| Max Pulse Current | 200.0 mA |
| Cells in series | 1 |
| Cells in parallel | 1 |

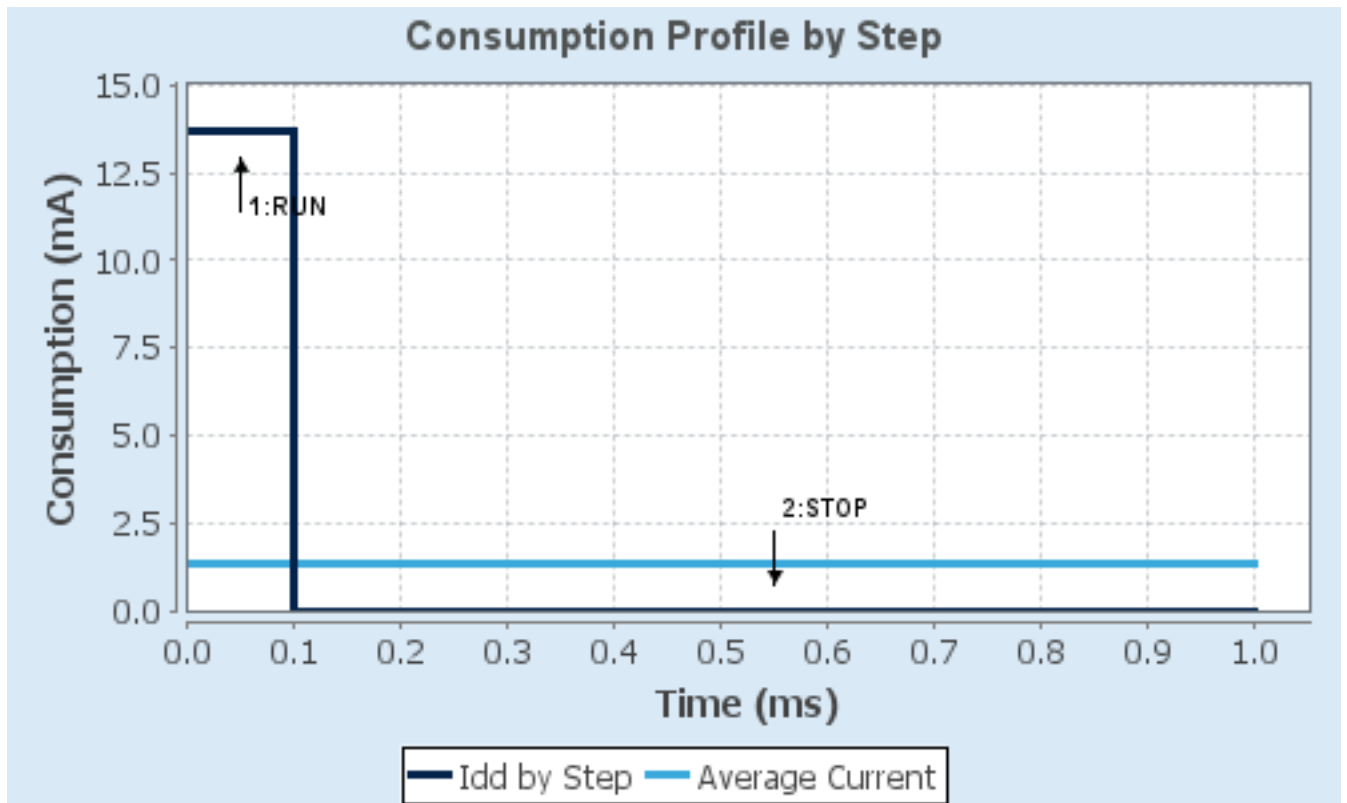
6.4. Sequence

| | | |
|-------------------------------|-------------|--------------|
| Step | Step1 | Step2 |
| Mode | RUN | STOP |
| Vdd | 3.6 | 3.6 |
| Voltage Source | Battery | Battery |
| Range | No Scale | No Scale |
| Fetch Type | FLASH | n/a |
| CPU Frequency | 48 MHz | 0 Hz |
| Clock Configuration | HSE PLL | Regulator LP |
| Clock Source Frequency | 8 MHz | 0 Hz |
| Peripherals | | |
| Additional Cons. | 0 mA | 0 mA |
| Average Current | 13.66 mA | 6.5 μ A |
| Duration | 0.1 ms | 0.9 ms |
| DMIPS | 0.0 | 0.0 |
| Ta Max | 102.84 | 105 |
| Category | In DS Table | In DS Table |

6.5. Results

| | | | |
|---------------|-----------------------------|-----------------|-----------|
| Sequence Time | 1 ms | Average Current | 1.37 mA |
| Battery Life | 3 months, 11 days, 17 hours | Average DMIPS | 0.0 DMIPS |

6.6. Chart



7. Peripherals and Middlewares Configuration

7.1. CAN

mode: Activated

7.1.1. Parameter Settings:

Bit Timings Parameters:

| | |
|------------------------------|----------------------------|
| Prescaler (for Time Quantum) | 16 |
| Time Quantum | 333.3333333333333 * |
| Time Quanta in Bit Segment 1 | 1 Time |
| Time Quanta in Bit Segment 2 | 1 Time |
| Time for one Bit | 1000 |
| Baud Rate | 1000000 * |
| ReSynchronization Jump Width | 1 Time |

Basic Parameters:

| | |
|-----------------------------------|---------|
| Time Triggered Communication Mode | Disable |
| Automatic Bus-Off Management | Disable |
| Automatic Wake-Up Mode | Disable |
| Automatic Retransmission | Disable |
| Receive Fifo Locked Mode | Disable |
| Transmit Fifo Priority | Disable |

Advanced Parameters:

| | |
|----------------|--------|
| Operating Mode | Normal |
|----------------|--------|

7.2. RCC

High Speed Clock (HSE): Crystal/Ceramic Resonator

7.2.1. Parameter Settings:

System Parameters:

| | |
|-------------------|--------------------|
| VDD voltage (V) | 3.3 |
| Prefetch Buffer | Enabled |
| Flash Latency(WS) | 1 WS (2 CPU cycle) |

RCC Parameters:

| | |
|--------------------------------|------|
| HSE Startup Timeout Value (ms) | 100 |
| LSE Startup Timeout Value (ms) | 5000 |

7.3. SYS

mode: Debug Serial Wire

Timebase Source: SysTick

*** User modified value**

8. System Configuration

8.1. GPIO configuration

| IP | Pin | Signal | GPIO mode | GPIO pull/up pull down | Max Speed | User Label |
|-----|-------------|-------------|------------------------------|-----------------------------|---------------|------------|
| CAN | PA11 | CAN_RX | Alternate Function Push Pull | No pull-up and no pull-down | High * | |
| | PA12 | CAN_TX | Alternate Function Push Pull | No pull-up and no pull-down | High * | |
| RCC | PF0-OSC_IN | RCC_OSC_IN | n/a | n/a | n/a | |
| | PF1-OSC_OUT | RCC_OSC_OUT | n/a | n/a | n/a | |
| SYS | PA13 | SYS_SWDIO | n/a | n/a | n/a | |
| | PA14 | SYS_SWCLK | n/a | n/a | n/a | |

8.2. DMA configuration

nothing configured in DMA service

8.3. NVIC configuration

8.3.1. NVIC

| Interrupt Table | Enable | Preenmption Priority | SubPriority |
|---|--------|----------------------|-------------|
| Non maskable interrupt | true | 0 | 0 |
| Hard fault interrupt | true | 0 | 0 |
| System service call via SWI instruction | true | 0 | 0 |
| Pendable request for system service | true | 0 | 0 |
| System tick timer | true | 0 | 0 |
| HDMI-CEC and CAN interrupts / HDMI-CEC wake-up interrupt through EXTI line 27 | true | 0 | 0 |
| PVD and VDDIO2 supply comparator interrupts through EXTI lines 16 and 31 | unused | | |
| Flash global interrupt | unused | | |
| RCC and CRS global interrupts | unused | | |

8.3.2. NVIC Code generation

| Enabled interrupt Table | Select for init sequence ordering | Generate IRQ handler | Call HAL handler |
|---|-----------------------------------|----------------------|------------------|
| Non maskable interrupt | false | true | false |
| Hard fault interrupt | false | true | false |
| System service call via SWI instruction | false | true | false |
| Pendable request for system service | false | true | false |
| System tick timer | false | true | true |
| HDMI-CEC and CAN interrupts / HDMI-CEC wake-up interrupt through EXTI line 27 | false | true | true |

* User modified value

9. System Views

9.1. Category view

9.1.1. Current

Middleware

| System Core | Analog | Timers | Connectivity | Multimedia | Computing |
|-------------|--------|--------|--------------|------------|-----------|
| DMA | | | CAN | | |
| GPIO | | | | | |
| IIVIC | | | | | |
| RCC | | | | | |
| SYS | | | | | |

10. Docs & Resources

| Type | Link |
|--------------------|---|
| Datasheet | http://www.st.com/resource/en/datasheet/DM00090510.pdf |
| Reference manual | http://www.st.com/resource/en/reference_manual/DM00031936.pdf |
| Programming manual | http://www.st.com/resource/en/programming_manual/DM00051352.pdf |
| Errata sheet | http://www.st.com/resource/en/errata_sheet/DM00096495.pdf |
| Application note | http://www.st.com/resource/en/application_note/CD00160362.pdf |
| Application note | http://www.st.com/resource/en/application_note/CD00167594.pdf |
| Application note | http://www.st.com/resource/en/application_note/CD00211314.pdf |
| Application note | http://www.st.com/resource/en/application_note/CD00249778.pdf |
| Application note | http://www.st.com/resource/en/application_note/CD00259245.pdf |
| Application note | http://www.st.com/resource/en/application_note/CD00264342.pdf |
| Application note | http://www.st.com/resource/en/application_note/CD00264379.pdf |
| Application note | http://www.st.com/resource/en/application_note/DM00024853.pdf |
| Application note | http://www.st.com/resource/en/application_note/DM00025071.pdf |
| Application note | http://www.st.com/resource/en/application_note/DM00042534.pdf |
| Application note | http://www.st.com/resource/en/application_note/DM00051986.pdf |
| Application note | http://www.st.com/resource/en/application_note/DM00052530.pdf |
| Application note | http://www.st.com/resource/en/application_note/DM00053084.pdf |
| Application note | http://www.st.com/resource/en/application_note/DM00072315.pdf |
| Application note | http://www.st.com/resource/en/application_note/DM00073742.pdf |
| Application note | http://www.st.com/resource/en/application_note/DM00080497.pdf |
| Application note | http://www.st.com/resource/en/application_note/DM00085385.pdf |
| Application note | http://www.st.com/resource/en/application_note/DM00087593.pdf |
| Application note | http://www.st.com/resource/en/application_note/DM00129215.pdf |
| Application note | http://www.st.com/resource/en/application_note/DM00145318.pdf |
| Application note | http://www.st.com/resource/en/application_note/DM00160482.pdf |

Application note http://www.st.com/resource/en/application_note/DM00188145.pdf
Application note http://www.st.com/resource/en/application_note/DM00189562.pdf
Application note http://www.st.com/resource/en/application_note/DM00210690.pdf
Application note http://www.st.com/resource/en/application_note/DM00220769.pdf
Application note http://www.st.com/resource/en/application_note/DM00226326.pdf
Application note http://www.st.com/resource/en/application_note/DM00236305.pdf
Application note http://www.st.com/resource/en/application_note/DM00257177.pdf
Application note http://www.st.com/resource/en/application_note/DM00296349.pdf
Application note http://www.st.com/resource/en/application_note/DM00315319.pdf
Application note http://www.st.com/resource/en/application_note/DM00327191.pdf
Application note http://www.st.com/resource/en/application_note/DM00354244.pdf
Application note http://www.st.com/resource/en/application_note/DM00355687.pdf
Application note http://www.st.com/resource/en/application_note/DM00380469.pdf
Application note http://www.st.com/resource/en/application_note/DM00395696.pdf
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Application note http://www.st.com/resource/en/application_note/DM00725181.pdf