

F2802x Burn In Unit

Code Reference Manual

Fri Aug 2 2013



# Contents

<b>1</b>	<b>Introduction</b>	<b>1</b>
1.1	General Operation . . . . .	1
1.2	Digital Power Control . . . . .	2
<b>2</b>	<b>Data Structure Documentation</b>	<b>5</b>
2.1	channelParameters Struct Reference . . . . .	5
2.2	i2cMsg Struct Reference . . . . .	7
<b>3</b>	<b>File Documentation</b>	<b>9</b>
3.1	Adc.h File Reference . . . . .	9
3.2	BstEn.h File Reference . . . . .	15
3.3	Cntl.h File Reference . . . . .	18
3.4	Common.h File Reference . . . . .	23
3.5	FanEn.h File Reference . . . . .	24
3.6	I2c.h File Reference . . . . .	27
3.7	MacroNets.h File Reference . . . . .	32
3.8	PhaseCtrl.h File Reference . . . . .	36
3.9	Pwm.h File Reference . . . . .	37
3.10	Sci.h File Reference . . . . .	39
3.11	SCPI_specificCmds.h File Reference . . . . .	41
3.12	Settings.h File Reference . . . . .	42
3.13	SineGen.h File Reference . . . . .	45
3.14	SlewControl.h File Reference . . . . .	51
3.15	StateMachine.h File Reference . . . . .	54
3.16	Timers.h File Reference . . . . .	55
3.17	Tmp.h File Reference . . . . .	56
	<b>Index</b>	<b>60</b>



# Chapter 1

## Introduction

This document forms the reference manual for the Burn In Unit based on the Texas Instruments F2802x.

This system has been implemented using several Texas Instruments libraries; digital power v3.1, SGen V1.01, IQ-Math v1.5c and SQMath. Please refer to the TI documentation pertaining to those libraries and to the C28x device to understand their operation.

This system also uses the System for Standard Commands for Programmable Instruments for C28x, again, refer to the documentation pertaining to that system to understand its use.

### 1.1 General Operation

This system operates using the concept of a state machine with multiplexed threading. This allows important tasks, such as signal generation and the monitoring of critical parameters to take precedence while still allowing other less critical tasks to run with acceptable frequency. This is allowed by creating several threads DPL, A, B and C and iterating them at different interval periods, such that every time the thread is iterated the next single task in that thread is run.

#### 1.1.1 Fast (DPL) Thread

The DPL thread is the fastest thread, is coded in assembly and consists of only two tasks. It is the only thread that uses an interrupt to iterate. Thus the tasks are formed by an interrupt service routine (ISR). This allows the DPL thread to interrupt the ongoing action of any of the other threads. This means that as long as the length of time each of the tasks take to execute is less than the interval time, then the threads operation will be deterministic. It is within this thread's two tasks that the critical and time-sensitive actions of the program are carried out. The thread ISR is triggered by a start of conversion (SOC) signal from the master enhanced pulse width modulation (ePWM) peripheral. This occurs at a frequency of 33 kHz. As there are two tasks on the thread, the frequency of each task is 16.5 kHz resulting in a period of 60us.

#### 1.1.2 Sequential Iteration Threads

The remaining threads (A, B, C) iterate by checking the rollover state of different CPU timers. Their operation and iteration is sequential and so less deterministic. The outline of the handling sequential iteration threads by the state machine is illustrated by the following flow diagram, where N and n rollover to A and 0, respectively whenever they reach their maximum value.

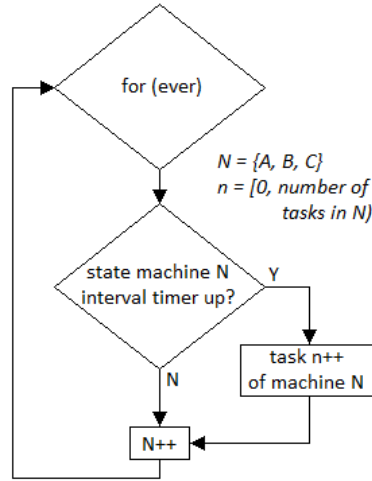


Figure 1.1: Sequential iteration thread state machine

The frequency of tasks can be determined from the period of the interval timers using the following equation, where  $n$  is the number of tasks in the thread and  $T_{in}$  is the interval timer period.

$$f_{ta} = \frac{1}{T_{in} \times n}$$

With interval periods such that  $T_A < T_B < T_C$  this results in the flow of execution illustrated by the following figure.

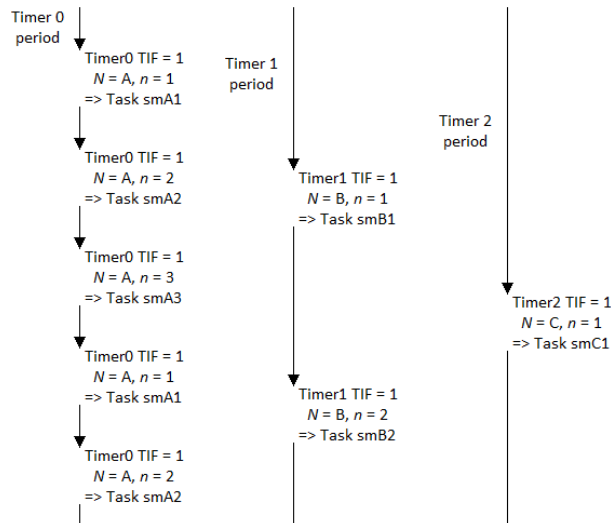


Figure 1.2: Sequence of thread tasks

## 1.2 Digital Power Control

The control of the power, voltage and current is managed using control loop logic, normally one loop for each stage (Transformer, AC, each of the four loads). These control loops are formed from a set of macros and helper functions.

Each of these macros acts as a block of functionality, much like a black-box electronic component, allowing their terminals to be wired together in the required order with nets. Once initialised and configured, or connected, correctly the macro blocks are executed, sequentially, by calling the appropriate code from an assembly ISR. In this case, this is what forms the DPL ISR tasks. Each block performs a precisely defined computation and delivers the

result to the appropriate net-list variables. Each control loop's net variables and operation parameter variables are grouped together and stored in a structure ([channelParameters](#)), with one such structure for each control loop.

The basic use of macros that are available within the program can be broken down into three parts:

1. Initialisation: the macro blocks are initialised from the program by using a C callable function, `DPL_Init()`, which is contained within the assembly file `DPL_ISR.asm`.
2. Configuration: C pointers of the macro block terminals are assigned to net nodes to form the desired control structure, i.e., the control loop.
3. Execution: Macro block code is executed in the assembly ISR, `DPL_ISR()`.

### 1.2.1 Boost Load Control

The following figure shows a diagram that illustrates the macro and net view of the control loop for one of the load boost converter stages. The loop depicted is current controlled. It uses a macro to retrieve a reading from an ADC peripheral to obtain a value that indicates the level of the current at the low side of the load input,  $\text{CHANn } I_{\text{SNS}}$ . This value is used as the feedback into a 2-pole 2-zero filter macro where it is compared to the user-set reference value and the output value is varied up or down, using the filter coefficients, as needed to move it towards matching the reference. This output value is then used by another macro to set the duty of the ePWM peripheral that controls the switching of the converter,  $\text{CHANn PWM}$ .

Another macro retrieves a value from a second ADC that represents the voltage level across the load input,  $\text{CHANn } V_{\text{SNS}}$ . A task in one of the sequential iteration threads compares this value to the user-set allowable voltage limit, the OVP limit. If the value is above this limit the control loop output is disabled and an over-current protection alert is raised.

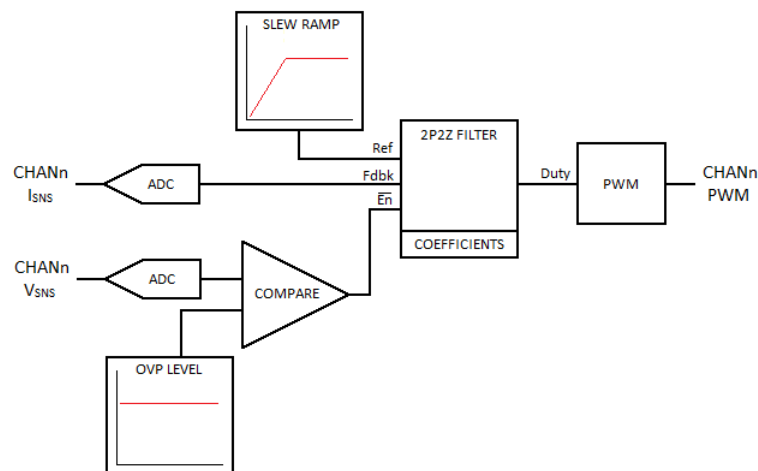


Figure 1.3: Macro view of control loop for single boost load

### 1.2.2 Inter-Boost Control

The following figure shows a diagram that illustrates the macro and net view of the control loop for the inter-boost, or "middle", boost converter stage. The loop depicted is voltage controlled. It uses a macro to retrieve a reading from an ADC peripheral to obtain a value that indicates the level of the voltage across the stage output, DC HV  $V_{\text{SNS}}$ . This value is used as the feedback into a 3-pole 3-zero filter macro where it is compared to the user-set reference value and the output value is varied up or down, using the filter coefficients, as needed to move towards matching the reference. This output value is then used by another macro to set the duty of the ePWM peripheral that controls the switching of the converter, `INTBST PWM`.

Another macro retrieves a value from a second ADC that represents the current level at the low side of the input, DC MID  $I_{\text{SNS}}$ . An on-board analogue comparator compares this value against the user-set allowable current limit,

the OCP limit. If the value is above this limit the comparator output sets the related trip-zone, quickly disabling the control loop output in software and hardware, HV EN.

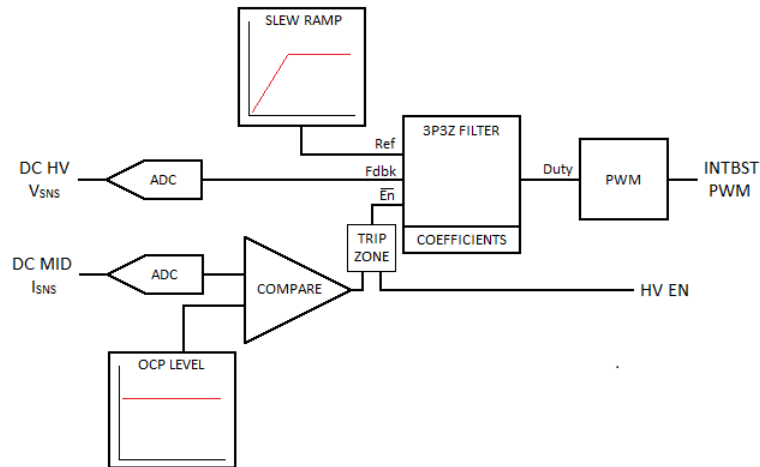


Figure 1.4: Macro view of control loop for inter-boost

### 1.2.3 AC Bridge Control

The following figure shows a diagram that illustrates the macro and a net view of the control loop for the AC bridge converter stage. The loop depicted is both voltage *and* current controlled. It uses a macro to retrieve a reading from an ADC peripheral to obtain a value that indicates the voltage level across the stage output, AC  $V_{SNS}$ . This value is used as the feedback into a 3-pole 3-zero filter macro where it is compared to the generated sine wave reference and the output value is varied up or down, using the filter coefficients, as needed to move towards matching the reference.

Another macro retrieves a value from a second ADC that represents the current level at the low side of the stage input, AC  $I_{SNS}$ . This value is used as the feedback into a 2-pole 2-zero filter macro where it is compared to the output of the 3-pole 3-zero filter macro and the output is varied up or down, using the filter coefficients, as needed to move towards matching the reference.

An on board analogue comparator also compares this AC  $I_{SNS}$  value against the user-set allowable current limit, the OCP limit. If the value is above the limit the comparator output sets the related trip zone, quickly disabling the control loop output and all other stages output, STOP ALL.

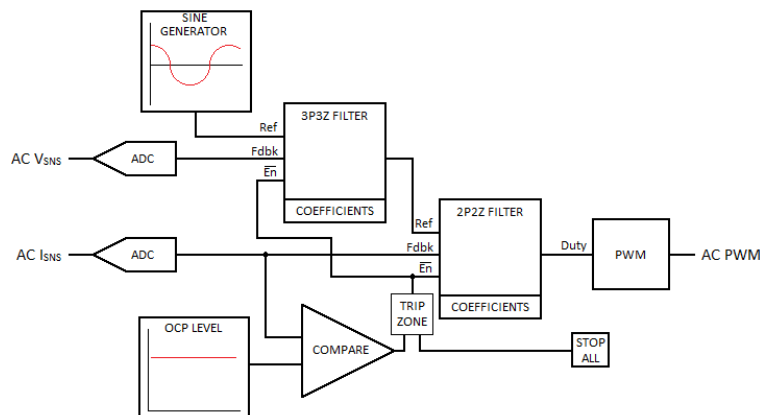


Figure 1.5: Macro view of control loop for AC bridge



## Chapter 2

# Data Structure Documentation

### 2.1 channelParameters Struct Reference

```
#include <MacroNets.h>
```

#### Data Fields

- volatile int32 [refNet](#)
- volatile int32 [iFdbkNet](#)
- volatile int32 [vFdbkNet](#)
- volatile int32 [outNet](#)
- int32 [ocp](#)
- int32 [ovp](#)
- int32 [target](#)
- int32 [slewRate](#)
- int16 [otp](#)
- int16 [iMaxRms](#)
- int16 [iMinRms](#)
- int16 [vMaxRms](#)
- int16 [vMinRms](#)
- int16 [iScale](#)
- int16 [vScale](#)
- int16 [vGainLmt](#)
- [opType](#) [opMode](#)
- [ctlType](#) [ctlMode](#)
- Uint16 [acFrequency](#)
- Uint16 [chEnable](#)

#### 2.1.1 Detailed Description

A structure used to represent the collection of settings pertaining to a particular channel or stage. Note that DPLib CNTL coefficient structures are handled separately to reduce complexity as DPLib expects them to be arranged in a certain manner in memory.

#### 2.1.2 Field Documentation

##### 2.1.2.1 Uint16 channelParameters::acFrequency

Sine signal generator frequency setting (Hz).

**2.1.2.2    Uint16 channelParameters::chEnable**

Channel enable status {FALSE, TRUE}.

**2.1.2.3    ctlType channelParameters::ctlMode**

Control mode setting {iCtrl, vCtrl}.

**2.1.2.4    volatile int32 channelParameters::iFdbkNet**

Current feednack net (IQ24).

**2.1.2.5    int16 channelParameters::iMaxRms**

Maximum RMS current setting limit (SQ10).

**2.1.2.6    int16 channelParameters::iMinRms**

Minimum RMS current setting limit (SQ10).

**2.1.2.7    int16 channelParameters::iScale**

Current scaling setting in volts-per-amp for scaling between a voltage level measured by an ADC to a real current value (SQ14).

**2.1.2.8    int32 channelParameters::ocp**

Normalised OCP limit (IQ24).

**2.1.2.9    opType channelParameters::opMode**

Output mode setting {dc, ac}.

**2.1.2.10    int16 channelParameters::otp**

OTP limit in ° C (SQ7).

**2.1.2.11    volatile int32 channelParameters::outNet**

IIR filter control law output net (IQ24).

**2.1.2.12    int32 channelParameters::ovp**

Normalised OVP limit (IQ24).

**2.1.2.13    volatile int32 channelParameters::refNet**

Net for CNTL reference (IQ24).

**2.1.2.14 int32 channelParameters::slewRate**

IIR filter control law reference slew rate (IQ24).

**2.1.2.15 int32 channelParameters::target**

IIR filter control law reference slew target (IQ24).

**2.1.2.16 volatile int32 channelParameters::vFdbkNet**

Voltage feedback net (IQ24).

**2.1.2.17 int16 channelParameters::vGainLmt**

Sine signal generator voltage gain limit (SQ14).

**2.1.2.18 int16 channelParameters::vMaxRms**

Maximum RMS voltage setting limit (SQ10).

**2.1.2.19 int16 channelParameters::vMinRms**

Minimum RMS voltage setting limit (SQ10).

**2.1.2.20 int16 channelParameters::vScale**

Voltage scaling setting in volts-per-volts for scaling between a voltage level measured by an ADC to a real voltage value (SQ14).

The documentation for this struct was generated from the following file:

- [MacroNets.h](#)

**2.2 i2cMsg Struct Reference**

```
#include <I2c.h>
```

**Data Fields**

- volatile Uint16 [msgStatus](#)
- Uint16 [slaveAddress](#)
- Uint16 [numOfBytes](#)
- Uint16 [numSlavePtrBytes](#)
- Uint16 [slavePtrAddrHigh](#)
- Uint16 [slavePtrAddrLow](#)
- Uint16 [msgBuffer](#) [I2C\_MAX\_BUFFER\_SIZE]

**2.2.1 Detailed Description**

The structure used to contain all settings and values relevant to a particular I2C message.

## 2.2.2 Field Documentation

### 2.2.2.1 `Uint16 i2cMsg::msgBuffer[I2C_MAX_BUFFER_SIZE]`

A buffer array for message data. The maximum buffer size, `MAX_BUFFER_SIZE`, is 4 due to the FIFO's size.

### 2.2.2.2 `volatile Uint16 i2cMsg::msgStatus`

Indicates which state the message is in.

### 2.2.2.3 `Uint16 i2cMsg::numOfBytes`

The number of valid bytes in (or to be put in `msgBuffer`).

### 2.2.2.4 `Uint16 i2cMsg::numSlavePtrBytes`

The number of slave register pointer address bytes.

### 2.2.2.5 `Uint16 i2cMsg::slaveAddress`

The slave device I2C address this message is intended for.

### 2.2.2.6 `Uint16 i2cMsg::slavePtrAddrHigh`

The slave register pointer high byte.

### 2.2.2.7 `Uint16 i2cMsg::slavePtrAddrLow`

The slave register pointer low byte.

The documentation for this struct was generated from the following file:

- [i2c.h](#)

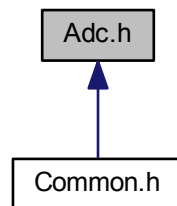
## Chapter 3

# File Documentation

### 3.1 Adc.h File Reference

ADC, DAC, comparator and related functions.

This graph shows which files directly or indirectly include this file:



#### Functions

- void [adcSocCnf](#) (void)
- void [adcMacroConfigure](#) (void)
- void [adcCompConfigure](#) (void)
- Uint16 [adcCheckOcp](#) (void)
- Uint16 [adcCheckOvp](#) (void)
- Uint16 [adcSetDac](#) (Uint16 chnl, float32 dacLvl)
- Uint16 [adcSetIScale](#) (Uint16 chnl, float32 scaleSetting)
- Uint16 [adcSetVScale](#) (Uint16 chnl, float32 scaleSetting)
- Uint16 [adcSetOcp](#) (Uint16 chnl, float32 ocpSetting)
- Uint16 [adcSetOvp](#) (Uint16 chnl, float32 ovpSetting)
- Uint16 [adcGetDac](#) (Uint16 chnl, float32 \*dacDest)
- Uint16 [adcGetIScale](#) (Uint16 chnl, float32 \*sclDest)
- Uint16 [adcGetVScale](#) (Uint16 chnl, float32 \*sclDest)
- Uint16 [adcGetVoltage](#) (Uint16 chnl, float32 \*vDest)
- Uint16 [adcGetCurrent](#) (Uint16 chnl, float32 \*iDest)
- Uint16 [adcGetOcp](#) (Uint16 chnl, float32 \*ocpDest)
- Uint16 [adcGetOvp](#) (Uint16 chnl, float32 \*ovpDest)

## Variables

- volatile int32 \* [ADCDRV\\_1ch\\_Rlt1](#)
- volatile int32 \* [ADCDRV\\_1ch\\_Rlt2](#)
- volatile int32 \* [ADCDRV\\_1ch\\_Rlt3](#)
- volatile int32 \* [ADCDRV\\_1ch\\_Rlt4](#)
- volatile int32 \* [ADCDRV\\_1ch\\_Rlt5](#)
- volatile int32 \* [ADCDRV\\_1ch\\_Rlt6](#)
- volatile int32 \* [ADCDRV\\_1ch\\_Rlt7](#)
- volatile int32 \* [ADCDRV\\_1ch\\_Rlt8](#)
- volatile int32 \* [ADCDRV\\_1ch\\_Rlt9](#)
- volatile int32 \* [ADCDRV\\_1ch\\_Rlt10](#)
- volatile int32 \* [ADCDRV\\_1ch\\_Rlt11](#)
- volatile int32 \* [ADCDRV\\_1ch\\_Rlt12](#)
- volatile int32 \* [ADCDRV\\_1ch\\_Rlt13](#)

### 3.1.1 Detailed Description

ADC, DAC, comparator and related functions. Requires the modification of the COMP\_REGS struct of the DS-P2802x\_GlobalVariableDefs in the file DSP2802x\_Comp.h, to allow use of the DACCTL and ramp-related register unions. Use the equivalent file from the f2903x includes for reference.

### 3.1.2 Function Documentation

#### 3.1.2.1 Uint16 adcCheckOcp ( void )

Checks the current current sense ADC readings against the OCP limits.

##### Returns

Error status

#### 3.1.2.2 Uint16 adcCheckOvp ( void )

Checks the current voltage sense ADC readings against the OVP limits.

##### Returns

Error status

#### 3.1.2.3 void adcCompConfigure ( void )

Configures the COMP 1 & 2 comparators using the internal DACs at inverting inputs.

- SHOULD be called AFTER [adcSocCnf\(\)](#).
- SHOULD be called BEFORE PWMS (SYNC) are started.
- SHOULD be called BEFORE pwmTZConfigure().

#### 3.1.2.4 Uint16 adcGetCurrent ( Uint16 *chnl*, float32 \* *iDest* )

Queries the most recent current reading from the specified channel's associated ADC.

**Parameters**

in	<i>chnl</i>	Specifies the channel number on which the reading is to be queried.
out	<i>iDest</i>	Address of the memory location at which to place the query result (amps).

**Returns**

Error status.

**3.1.2.5 Uint16 adcGetDac ( Uint16 *chnl*, float32 \* *dacDest* )**

Queries the output level setting of the DAC on the inverting input of the comparators.

**Parameters**

in	<i>chnl</i>	Specifies the channel number on which the setting is to be queried.
out	<i>dacDest</i>	Address of the memory location at which to place the query result (volts or amps).

**Returns**

Error status.

**3.1.2.6 Uint16 adcGetIScale ( Uint16 *chnl*, float32 \* *sclDest* )**

Queries the current current scaling setting of the specified channel.

**Parameters**

in	<i>chnl</i>	Specifies the channel number on which the setting is to be queried.
out	<i>sclDest</i>	Address of the memory location at which to place the query result (amps).

**Returns**

Error status.

**3.1.2.7 Uint16 adcGetOcp ( Uint16 *chnl*, float32 \* *ocpDest* )**

Queries the over current protection setting for the specified channel.

**Parameters**

in	<i>chnl</i>	Specifies the channel number on which the setting is to be queried.
out	<i>ocpDest</i>	Address of the memory location at which to place the query result (amps).

**Returns**

Error status.

**3.1.2.8 Uint16 adcGetOvp ( Uint16 *chnl*, float32 \* *ovpDest* )**

Queries the over current protection setting for the specified channel.

**Parameters**

in	<i>chnl</i>	Specifies the channel number on which the setting is to be queried.
out	<i>ovpDest</i>	Address of the memory location at which to place the query result (volts).

**Returns**

Error status.

**3.1.2.9 Uint16 adcGetVoltage ( Uint16 *chnl*, float32 \* *vDest* )**

Queries the most recent voltage reading from the specified channel's associated ADC.

**Parameters**

in	<i>chnl</i>	Specifies the channel number on which the reading is to be queried.
out	<i>vDest</i>	Address of the memory location at which to place the query result (volts).

**Returns**

Error status.

**3.1.2.10 Uint16 adcGetVScale ( Uint16 *chnl*, float32 \* *sclDest* )**

Queries the current voltage scaling setting of the specified channel.

**Parameters**

in	<i>chnl</i>	Specifies the channel number on which the setting is to be queried.
out	<i>sclDest</i>	Address of the memory location at which to place the query result (volts).

**Returns**

Error status.

**3.1.2.11 void adcMacroConfigure ( void )**

Configures the ADC's SOC's then calls [pwmSocConfigure\(\)](#).

- SHOULD be run after [pwmMacroConfigure\(\)](#).
- SHOULD be run before `DPL_INIT()`.

**3.1.2.12 Uint16 adcSetDac ( Uint16 *chnl*, float32 *dacLvl* )**

Sets the output levels of the DACs on the inverting input of the comparators. The function will determine the scaling to be applied by testing the `ctrlMode` setting of the channel specified. The respective channel's current or voltage MUST be set previously.

**Parameters**



in	<i>chnl</i>	Specifies the channel number the setting is to be applied to.
in	<i>dacLvl</i>	Specifies the value of the level setting to be applied (volts or amps).

**Returns**

Error status.

**3.1.2.13 Uint16 adcSetIScale ( Uint16 *chnl*, float32 *scaleSetting* )**

Sets the current scaling for the specified channel.

**Parameters**

in	<i>chnl</i>	Specifies the channel number the setting is to be applied to.
in	<i>scaleSetting</i>	Specifies the value of the scaling setting to be applied (amps/volts).

**Returns**

Error status.

**3.1.2.14 Uint16 adcSetOcp ( Uint16 *chnl*, float32 *ocpSetting* )**

Sets the over current protection limit for the specified channel.

**Parameters**

in	<i>chnl</i>	Specifies the channel number the setting is to be applied to.
in	<i>ocpSetting</i>	Specifies the value of the limit to be applied (Amps).

**Returns**

Error status.

**3.1.2.15 Uint16 adcSetOvp ( Uint16 *chnl*, float32 *ovpSetting* )**

Sets the over voltage protection limit for the specified channel.

**Parameters**

in	<i>chnl</i>	Specifies the channel number the setting is to be applied to.
in	<i>ovpSetting</i>	Specifies the value of the limit to be applied (volts).

**Returns**

Error status.

**3.1.2.16 Uint16 adcSetVScale ( Uint16 *chnl*, float32 *scaleSetting* )**

Sets the voltage scaling for the specified channel.

**Parameters**

<i>in</i>	<i>chnl</i>	Specifies the channel number the setting is to be applied to.
<i>in</i>	<i>scaleSetting</i>	Specifies the value of the scaling setting to be applied (volts/volts).

**Returns**

Error status.

**3.1.2.17 void adcSocCnf ( void )**

Configures ADC SOC for ADC macro

**3.1.3 Variable Documentation****3.1.3.1 volatile int32\* ADCDRV\_1ch\_Rlt1**

Channel 0 current sense ADC terminal pointer.

**3.1.3.2 volatile int32\* ADCDRV\_1ch\_Rlt10**

Channel 3 voltage sense ADC terminal pointer.

**3.1.3.3 volatile int32\* ADCDRV\_1ch\_Rlt11**

Interboost voltage sense ADC terminal pointer.

**3.1.3.4 volatile int32\* ADCDRV\_1ch\_Rlt12**

AC stage voltage sense ADC terminal pointer.

**3.1.3.5 volatile int32\* ADCDRV\_1ch\_Rlt13**

VMid voltage sense ADC terminal pointer.

**3.1.3.6 volatile int32\* ADCDRV\_1ch\_Rlt2**

Channel 1 current sense ADC terminal pointer.

**3.1.3.7 volatile int32\* ADCDRV\_1ch\_Rlt3**

Channel 2 current sense ADC terminal pointer.

**3.1.3.8 volatile int32\* ADCDRV\_1ch\_Rlt4**

Channel 3 current sense ADC terminal pointer.

**3.1.3.9 volatile int32\* ADCDRV\_1ch\_Rlt5**

Interboost current sense ADC terminal pointer.

## 3.1.3.10 volatile int32\* ADCDRV\_1ch\_Rlt6

AC stage current sense ADC terminal pointer.

## 3.1.3.11 volatile int32\* ADCDRV\_1ch\_Rlt7

Channel 0 voltage sense ADC terminal pointer.

## 3.1.3.12 volatile int32\* ADCDRV\_1ch\_Rlt8

Channel 1 voltage sense ADC terminal pointer.

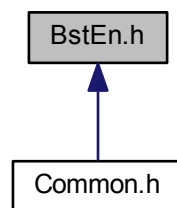
## 3.1.3.13 volatile int32\* ADCDRV\_1ch\_Rlt9

Channel 2 voltage sense ADC terminal pointer.

## 3.2 BstEn.h File Reference

Functions for enabling and disabling the boost converter stages via I2C.

This graph shows which files directly or indirectly include this file:



### Macros

- #define IOE\_I2C\_ADDR 0x20
- #define IOE\_IODIR\_ADDR 0x00
- #define IOE\_IPOL\_ADDR 0x01
- #define IOE\_GPINTEN\_ADDR 0x02
- #define IOE\_DEFVAL\_ADDR 0x03
- #define IOE\_INTCON\_ADDR 0x04
- #define IOE\_IOCON\_ADDR 0x05
- #define IOE\_GPPU\_ADDR 0x06
- #define IOE\_INTF\_ADDR 0x07
- #define IOE\_INTCAP\_ADDR 0x08
- #define IOE\_GPIO\_ADDR 0x09
- #define IOE\_OLAT\_ADDR 0x0A
- #define BST\_NUM\_CHNL 0x04
- #define IOE\_NUM\_CHNL BST\_NUM\_CHNL

## Functions

- Uint16 [bcInit](#) (void)
- Uint16 [bcEnable](#) (Uint16 chnl)
- Uint16 [bcDisable](#) (Uint16 chnl)

### 3.2.1 Detailed Description

Functions for enabling and disabling the boost converter stages via I2C. The converters are controlled via an external I/O expander (MCP23008) that is connected to the I2C bus at address 0100x-x-x where 'x-x-x' is dependent upon the configuration of resistors R60 - 61 & R70 - R74.

After [bcInit\(\)](#) all converters default to disabled.

#### Warning

Before any converter control functions can be used the I2C peripheral MUST be initialised and EITHER [bcInit\(\)](#) or [fcInit\(\)](#) MUST be run - [bcInit\(\)](#) will require the interrupts to be enabled globally.

#### See Also

[i2cInit\(\)](#)  
[fcInit\(\)](#)

### 3.2.2 Macro Definition Documentation

#### 3.2.2.1 #define BST\_NUM\_CHNL 0x04

Number of boost converter channels.

#### 3.2.2.2 #define IOE\_DEFVAL\_ADDR 0x03

MCP23008 I/O expander default value register address.

#### 3.2.2.3 #define IOE\_GPINTEN\_ADDR 0x02

MCP23008 I/O expander interrupt on change enable register address.

#### 3.2.2.4 #define IOE\_GPIO\_ADDR 0x09

MCP23008 I/O expander GPIO port register address.

#### 3.2.2.5 #define IOE\_GPPU\_ADDR 0x06

MCP23008 I/O expander pull-up resistor configuration register address.

#### 3.2.2.6 #define IOE\_I2C\_ADDR 0x20

MCP23008 I/O expander I2C address (slave, 32d, 8-bit I/O expander).

#### 3.2.2.7 #define IOE\_INTCAP\_ADDR 0x08

MCP23008 I/O expander interrupt capture register address.

#### 3.2.2.8 #define IOE\_INTCON\_ADDR 0x04

MCP23008 I/O expander interrupt on change control register address.

#### 3.2.2.9 #define IOE\_INTF\_ADDR 0x07

MCP23008 I/O expander interrupt flag register address.

#### 3.2.2.10 #define IOE\_IOCON\_ADDR 0x05

MCP23008 I/O expander configuration register address.

#### 3.2.2.11 #define IOE\_IODIR\_ADDR 0x00

MCP23008 I/O expander I/O direction register address.

#### 3.2.2.12 #define IOE\_IPOL\_ADDR 0x01

MCP23008 I/O expander input polarity register address.

#### 3.2.2.13 #define IOE\_NUM\_CHNL BST\_NUM\_CHNL

Total number of MCP I/O expander channels.

#### 3.2.2.14 #define IOE\_OLAT\_ADDR 0x0A

MCP23008 I/O expander output latch register address.

### 3.2.3 Function Documentation

#### 3.2.3.1 Uint16 bcDisable ( Uint16 *chnl* )

Disables the specified channel's boost converter. The I2C peripheral and the boost converter enable controller interface MUST be initialised before this function is used.

##### See Also

[i2cInit\(\)](#)  
[bcInit\(\)](#)

##### Parameters

<i>in</i>	<i>chnl</i>	Specifies the channel boost that is to be disabled.
-----------	-------------	---

##### Returns

Error status.

### 3.2.3.2 Uint16 bcEnable ( Uint16 *chnl* )

Enables the specified channel's boost converter. The I2C peripheral and the boost converter enable controller interface MUST be initialised before this function is used.

#### See Also

[i2cInit\(\)](#)  
[bcInit\(\)](#)

#### Parameters

<i>in</i>	<i>chnl</i>	Specifies the channel boost that is to be enabled.
-----------	-------------	--

#### Returns

Error status.

### 3.2.3.3 Uint16 bcInit ( void )

Initialises the boost converter enable control interface. The I2C peripheral MUST be initialised before this function is used.

#### See Also

[i2cInit\(\)](#)

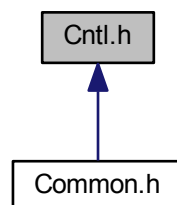
#### Returns

Error status.

## 3.3 Cntl.h File Reference

DPLib CNTL Macro related helper functions.

This graph shows which files directly or indirectly include this file:



## Macros

- `#define SATMAX_MAX` 0.9f

## Typedefs

- typedef enum [coefNum](#) [cfType](#)

## Enumerations

- enum [coefNum](#) { ,  
[cMin](#) = firstCoef, [cMax](#), [cB0](#), [cB1](#),  
[cA1](#), [cB2](#), [cA2](#), [cB3](#),  
[cA3](#) }

## Functions

- void [cntlUpdateCoefs](#) (void)
- Uint16 [cntlGetCoef](#) (Uint16 chnl, [cfType](#) coef, float32 \*valDest)
- Uint16 [cntlSetCoef](#) (Uint16 chnl, [cfType](#) coef, float32 val)

## Variables

- volatile int32 \* [CNTL\\_2P2Z\\_Coef1](#)
- volatile int32 \* [CNTL\\_2P2Z\\_Coef2](#)
- volatile int32 \* [CNTL\\_2P2Z\\_Coef3](#)
- volatile int32 \* [CNTL\\_2P2Z\\_Coef4](#)
- volatile int32 \* [CNTL\\_2P2Z\\_Coef5](#)
- volatile int32 \* [CNTL\\_2P2Z\\_Fdbk1](#)
- volatile int32 \* [CNTL\\_2P2Z\\_Fdbk2](#)
- volatile int32 \* [CNTL\\_2P2Z\\_Fdbk3](#)
- volatile int32 \* [CNTL\\_2P2Z\\_Fdbk4](#)
- volatile int32 \* [CNTL\\_2P2Z\\_Fdbk5](#)
- volatile int32 \* [CNTL\\_2P2Z\\_Out1](#)
- volatile int32 \* [CNTL\\_2P2Z\\_Out2](#)
- volatile int32 \* [CNTL\\_2P2Z\\_Out3](#)
- volatile int32 \* [CNTL\\_2P2Z\\_Out4](#)
- volatile int32 \* [CNTL\\_2P2Z\\_Out5](#)
- volatile int32 \* [CNTL\\_2P2Z\\_Ref1](#)
- volatile int32 \* [CNTL\\_2P2Z\\_Ref2](#)
- volatile int32 \* [CNTL\\_2P2Z\\_Ref3](#)
- volatile int32 \* [CNTL\\_2P2Z\\_Ref4](#)
- volatile int32 \* [CNTL\\_2P2Z\\_Ref5](#)
- volatile int32 \* [CNTL\\_3P3Z\\_Coef1](#)
- volatile int32 \* [CNTL\\_3P3Z\\_Coef2](#)
- volatile int32 \* [CNTL\\_3P3Z\\_Fdbk1](#)
- volatile int32 \* [CNTL\\_3P3Z\\_Fdbk2](#)
- volatile int32 \* [CNTL\\_3P3Z\\_Out1](#)
- volatile int32 \* [CNTL\\_3P3Z\\_Out2](#)
- volatile int32 \* [CNTL\\_3P3Z\\_Ref1](#)
- volatile int32 \* [CNTL\\_3P3Z\\_Ref2](#)
- struct CNTL\_2P2Z\_CoefStruct [coefs2](#) [[NUM\\_ICTRL\\_CHNLS](#)]
- struct CNTL\_3P3Z\_CoefStruct [coefs3](#) [[NUM\\_VCTRL\\_CHNLS](#)]

### 3.3.1 Detailed Description

DPLib CNTL Macro related helper functions.

### 3.3.2 Macro Definition Documentation

#### 3.3.2.1 `#define SATMAX_MAX 0.9f`

The maximum allowable value for the IIR filter control law's maximum saturation.

### 3.3.3 Typedef Documentation

#### 3.3.3.1 `typedef enum coefNum cfType`

A type that allows a reference to a CNTL coefficient.

### 3.3.4 Enumeration Type Documentation

#### 3.3.4.1 `enum coefNum`

CNTL Coefficient references

Enumerator

***cMin*** Saturation minimum reference.

***cMax*** Saturation maximum reference.

***cB0*** B0 coefficient reference.

***cB1*** B1 coefficient reference.

***cA1*** A1 coefficient reference.

***cB2*** B2 coefficient reference.

***cA2*** A2 coefficient reference.

***cB3*** B3 coefficient reference.

***cA3*** A3 coefficient reference.

### 3.3.5 Function Documentation

#### 3.3.5.1 `Uint16 cntlGetCoef ( Uint16 chnl, cfType coef, float32 * valDest )`

Queries the specified IIR filter control law coefficient for the specified channel.

Parameters

in	<i>chnl</i>	Specifies the channel number on which the setting is to be queried.
in	<i>coef</i>	Specifies the coefficient to be queried.
out	<i>valDest</i>	Address of the memory location at which to place the query result.

Returns

Error status.

#### 3.3.5.2 `Uint16 cntlSetCoef ( Uint16 chnl, cfType coef, float32 val )`

Sets the specified IIR filter control law coefficient for the specified channel.

- The actual setting in use is not updated until AFTER [cntlUpdateCoefs\(\)](#) has been called.



**Parameters**

in	<i>chnl</i>	Specifies the channel number the setting is to be applied to [0, NUM_CHNLS).
in	<i>coef</i>	Specifies the coefficient to be set [cMin, cA3].
in	<i>val</i>	Specifies the coefficient value to be applied. Should be between the minimum and maximum values for the specific coefficient as defined by cfLmts[coef] and cfLmts[coef + cA3].

**Returns**

Error status.

**3.3.5.3 void cntlUpdateCoefs ( void )**

Updates the IIR filter control law's coefficients that are being used to those values set by the use of the other functions within this file.

**3.3.6 Variable Documentation****3.3.6.1 volatile int32\* CNTL\_2P2Z\_Coef1**

Channel 0 IIR filter control law coefficient terminal pointer.

**3.3.6.2 volatile int32\* CNTL\_2P2Z\_Coef2**

Channel 1 IIR filter control law coefficient terminal pointer.

**3.3.6.3 volatile int32\* CNTL\_2P2Z\_Coef3**

Channel 2 IIR filter control law coefficient terminal pointer.

**3.3.6.4 volatile int32\* CNTL\_2P2Z\_Coef4**

Channel 3 IIR filter control law coefficient terminal pointer.

**3.3.6.5 volatile int32\* CNTL\_2P2Z\_Coef5**

Channel 4 IIR filter control law coefficient terminal pointer.

**3.3.6.6 volatile int32\* CNTL\_2P2Z\_Fdbk1**

Channel 0 IIR filter control law feedback terminal pointer.

**3.3.6.7 volatile int32\* CNTL\_2P2Z\_Fdbk2**

Channel 1 IIR filter control law feedback terminal pointer.

**3.3.6.8 volatile int32\* CNTL\_2P2Z\_Fdbk3**

Channel 2 IIR filter control law feedback terminal pointer.

### 3.3.6.9 volatile int32\* CNTL\_2P2Z\_Fdbk4

Channel 3 IIR filter control law feedback terminal pointer.

### 3.3.6.10 volatile int32\* CNTL\_2P2Z\_Fdbk5

Channel 4 IIR filter control law feedback terminal pointer.

### 3.3.6.11 volatile int32\* CNTL\_2P2Z\_Out1

Channel 0 IIR filter control law output terminal pointer.

### 3.3.6.12 volatile int32\* CNTL\_2P2Z\_Out2

Channel 1 IIR filter control law output terminal pointer.

### 3.3.6.13 volatile int32\* CNTL\_2P2Z\_Out3

Channel 2 IIR filter control law output terminal pointer.

### 3.3.6.14 volatile int32\* CNTL\_2P2Z\_Out4

Channel 3 IIR filter control law output terminal pointer.

### 3.3.6.15 volatile int32\* CNTL\_2P2Z\_Out5

Channel 4 IIR filter control law output terminal pointer.

### 3.3.6.16 volatile int32\* CNTL\_2P2Z\_Ref1

Channel 0 IIR filter control law reference terminal pointer.

### 3.3.6.17 volatile int32\* CNTL\_2P2Z\_Ref2

Channel 1 IIR filter control law reference terminal pointer.

### 3.3.6.18 volatile int32\* CNTL\_2P2Z\_Ref3

Channel 2 IIR filter control law reference terminal pointer.

### 3.3.6.19 volatile int32\* CNTL\_2P2Z\_Ref4

Channel 3 IIR filter control law reference terminal pointer.

### 3.3.6.20 volatile int32\* CNTL\_2P2Z\_Ref5

Channel 4 IIR filter control law reference terminal pointer.

**3.3.6.21 volatile int32\* CNTL\_3P3Z\_Coef1**

Interboost IIR filter control law coefficient terminal pointer.

**3.3.6.22 volatile int32\* CNTL\_3P3Z\_Coef2**

AC stage IIR filter control law coefficient terminal pointer.

**3.3.6.23 volatile int32\* CNTL\_3P3Z\_Fdbk1**

Interboost IIR filter control law feedback terminal pointer.

**3.3.6.24 volatile int32\* CNTL\_3P3Z\_Fdbk2**

AC stage IIR filter control law feedback terminal pointer.

**3.3.6.25 volatile int32\* CNTL\_3P3Z\_Out1**

Interboost IIR filter control law output terminal pointer.

**3.3.6.26 volatile int32\* CNTL\_3P3Z\_Out2**

AC stage IIR filter control law output terminal pointer.

**3.3.6.27 volatile int32\* CNTL\_3P3Z\_Ref1**

Interboost IIR filter control law reference terminal pointer.

**3.3.6.28 volatile int32\* CNTL\_3P3Z\_Ref2**

AC stage IIR filter control law reference terminal pointer.

**3.3.6.29 struct CNTL\_2P2Z\_CoefStruct coefs2[NUM\_ICTRL\_CHNLS]**

Array of structures that hold the 2-pole 2-zero IIR filter control law coefficient currently in use.

**3.3.6.30 struct CNTL\_3P3Z\_CoefStruct coefs3[NUM\_VCTRL\_CHNLS]**

Array of structures that hold the 3-pole 3-zero IIR filter control law coefficient currently in use.

## 3.4 Common.h File Reference

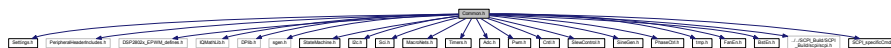
Common include file for the project.

```

#include "Settings.h"
#include "PeripheralHeaderIncludes.h"
#include "DSP2802x_EPWM_defines.h"
#include "IQMathLib.h"
#include "SQMath.h"
#include "DPLib.h"
#include "sgen.h"
#include "StateMachine.h"
#include "I2c.h"
#include "Sci.h"
#include "MacroNets.h"
#include "Timers.h"
#include "Adc.h"
#include "Pwm.h"
#include "Cntl.h"
#include "SlewControl.h"
#include "SineGen.h"
#include "PhaseCtrl.h"
#include "tmp.h"
#include "FanEn.h"
#include "BstEn.h"
#include "../SCPI_Build/SCPI_Build/scpi/scpi.h"
#include "SCPI_specificCmds.h"

```

Include dependency graph for Common.h:



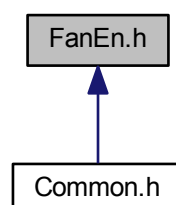
### 3.4.1 Detailed Description

Common include file for the project. All other header files used should be included within this file and this file should then be used to include them in the required source files.

## 3.5 FanEn.h File Reference

Functions for enabling and disabling the external fans via I2C.

This graph shows which files directly or indirectly include this file:



## Macros

- `#define IOE_I2C_ADDR 0x20`
- `#define IOE_IODIR_ADDR 0x00`
- `#define IOE_IPOL_ADDR 0x01`
- `#define IOE_GPINTEN_ADDR 0x02`
- `#define IOE_DEFVAL_ADDR 0x03`
- `#define IOE_INTCON_ADDR 0x04`
- `#define IOE_IOCON_ADDR 0x05`
- `#define IOE_GPPU_ADDR 0x06`
- `#define IOE_INTF_ADDR 0x07`
- `#define IOE_INTCAP_ADDR 0x08`
- `#define IOE_GPIO_ADDR 0x09`
- `#define IOE_OLAT_ADDR 0x0A`
- `#define FAN_NUM_CHNL 0x04`
- `#define FAN_CHNL_OFST 0x04`

## Functions

- `Uint16 fclnit (void)`
- `Uint16 fcEnable (Uint16 chnl)`
- `Uint16 fcDisable (Uint16 chnl)`

### 3.5.1 Detailed Description

Functions for enabling and disabling the external fans via I2C. The fans are controlled via an external I/O expander (MCP23008) that is connected to the I2C bus at address 0100x-x-x where 'x-x-x' is dependent upon the configuration of resistors R60 - 61 & R70 - R74.

After `fclnit()` all fans default to disabled.

#### Warning

Before any fan control functions can be used the I2C peripheral MUST be initialised and EITHER `fclnit()` or `bclnit()` must be run - `fclnit()` will require the interrupts to be enabled globally.

#### See Also

`i2cInlt()`  
`bclnit()`

### 3.5.2 Macro Definition Documentation

#### 3.5.2.1 `#define FAN_CHNL_OFST 0x04`

Fan channel numbering offset

#### 3.5.2.2 `#define FAN_NUM_CHNL 0x04`

Number of fan channels

#### 3.5.2.3 `#define IOE_DEFVAL_ADDR 0x03`

MCP23008 I/O expander default value register address

#### 3.5.2.4 `#define IOE_GPINTEN_ADDR 0x02`

MCP23008 I/O expander interrupt on change enable register address

#### 3.5.2.5 `#define IOE_GPIO_ADDR 0x09`

MCP23008 I/O expander GPIO port register address

#### 3.5.2.6 `#define IOE_GPPU_ADDR 0x06`

MCP23008 I/O expander pull-up resistor configuration register address

#### 3.5.2.7 `#define IOE_I2C_ADDR 0x20`

MCP23008 I/O expander I2C address (slave, 32d, 8-bit I/O expander)

#### 3.5.2.8 `#define IOE_INTCAP_ADDR 0x08`

MCP23008 I/O expander interrupt capture register address

#### 3.5.2.9 `#define IOE_INTCON_ADDR 0x04`

MCP23008 I/O expander interrupt on change control register address

#### 3.5.2.10 `#define IOE_INTF_ADDR 0x07`

MCP23008 I/O expander interrupt flag register address

#### 3.5.2.11 `#define IOE_IOCON_ADDR 0x05`

MCP23008 I/O expander configuration register address

#### 3.5.2.12 `#define IOE_IODIR_ADDR 0x00`

MCP23008 I/O expander I/O direction register address

#### 3.5.2.13 `#define IOE_IPOL_ADDR 0x01`

MCP23008 I/O expander input polarity register address

#### 3.5.2.14 `#define IOE_OLAT_ADDR 0x0A`

MCP23008 I/O expander output latch register address

### 3.5.3 Function Documentation

#### 3.5.3.1 `Uint16 fcDisable ( Uint16 chnl )`

Disables the specified channel's fan The I2C peripheral and the fan enable controller interface MUST be initialised before this function is used.

## See Also

[i2cInit\(\)](#)  
[fclnit\(\)](#)

## Parameters

<i>in</i>	<i>chnl</i>	Specifies the channel fan that is to be disabled
-----------	-------------	--

## Returns

Error status

3.5.3.2 Uint16 fcEnable ( Uint16 *chnl* )

Enables the specified channel's fan The I2C peripheral and the fan enable controller interface MUST be initialised before this function is used.

## See Also

[i2cInit\(\)](#)  
[fclnit\(\)](#)

## Parameters

<i>in</i>	<i>chnl</i>	Specifies the channel fan that is to be enabled
-----------	-------------	---

## Returns

Error status

## 3.5.3.3 Uint16 fclnit ( void )

Initialises the fan enable control interface. The I2C peripheral must be initialised before this function is used.

## See Also

[i2cInit\(\)](#)

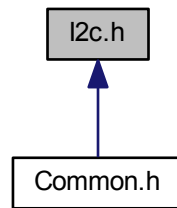
## Returns

Error status

## 3.6 I2c.h File Reference

I2C communication functions.

This graph shows which files directly or indirectly include this file:



## Data Structures

- struct [i2cMsg](#)

## Macros

- #define [I2C\\_MAX\\_BUFFER\\_SIZE](#) 0x04
- #define [I2C\\_MAX\\_PTR\\_SIZE](#) 0x02
- #define [I2C\\_CLR\\_AL\\_BIT](#) 0x0001
- #define [I2C\\_CLR\\_NACK\\_BIT](#) 0x0002
- #define [I2C\\_CLR\\_ARDY\\_BIT](#) 0x0004
- #define [I2C\\_CLR\\_RRDY\\_BIT](#) 0x0008
- #define [I2C\\_CLR\\_SCD\\_BIT](#) 0x0020
- #define [I2C\\_ARDY\\_ISRC](#) 0x0003
- #define [I2C\\_SCD\\_ISRC](#) 0x0006
- #define [I2C\\_MSGSTAT\\_INACTIVE](#) 0x0000
- #define [I2C\\_MSGSTAT\\_SEND\\_WITHSTOP](#) 0x0010
- #define [I2C\\_MSGSTAT\\_WRITE\\_BUSY](#) 0x0011
- #define [I2C\\_MSGSTAT\\_SEND\\_NOSTOP](#) 0x0020
- #define [I2C\\_MSGSTAT\\_SEND\\_NOSTOP\\_BUSY](#) 0x0021
- #define [I2C\\_MSGSTAT\\_RESTART](#) 0x0022
- #define [I2C\\_MSGSTAT\\_READ\\_BUSY](#) 0x0023

## Functions

- void [i2cInit](#) (void)
- void [i2cPopMsg](#) ([i2cMsg](#) \*msg, Uint16 msgStatus, Uint16 slaveAddr, Uint16 numDataBytes, Uint16 numSlavePtrBytes, Uint16 slavePtrAddrHi, Uint16 slavePtrAddrLo)
- Uint16 [i2cWrite](#) ([i2cMsg](#) \*msg)
- Uint16 [i2cRead](#) ([i2cMsg](#) \*msg)



### 3.6.1 Detailed Description

I2C communication functions.

#### Warning

The following bridge tracks should be cut on the TI C2000 LaunchPad XL PCB before I2C (or SPI) may be used:

Bridge to Cut	GPIO	PCB Pin
JP5	GPIO32	J2-6
JP7	GPIO33	J2-7
JP8	GPIO16	J6-7
JP10	GPIO17	J6-8

This should result in the following functionality:

Function	GPIO	PCB Pin
I2C_SDAA	GPIO32	J6-7
I2C_SCLA	GPIO33	J6-8
SPI_MOSI	GPIO16	J2-6
SPI_MISO	GPIO17	J2-7

The function `i2cInit()` MUST be called before any other public I2C function is used. This will clear any values already in the I2C registers.

Interrupts MUST be globally enabled for the functions `i2cWrite()` and `i2cRead()` to operate correctly.

#### See Also

[BstEn.h](#)  
[FanEn.h](#)  
[Tmp.h](#)

### 3.6.2 Macro Definition Documentation

#### 3.6.2.1 `#define I2C_ARDY_ISRC 0x0003`

I2C Interrupt Sources Register access ready condition I2C interrupt source.

#### 3.6.2.2 `#define I2C_CLR_AL_BIT 0x0001`

I2C Status Clear Bits Arbitration lost status clear bit.

#### 3.6.2.3 `#define I2C_CLR_ARDY_BIT 0x0004`

Register access ready status clear bit.

#### 3.6.2.4 `#define I2C_CLR_NACK_BIT 0x0002`

NACK status clear bit.

#### 3.6.2.5 `#define I2C_CLR_RRDY_BIT 0x0008`

Receive data ready status clear bit.

#### 3.6.2.6 `#define I2C_CLR_SCD_BIT 0x0020`

Stop detected status clear bit.

#### 3.6.2.7 `#define I2C_MAX_BUFFER_SIZE 0x04`

Maximum I2C message buffer size in bytes, including slave register pointer bytes.

#### 3.6.2.8 `#define I2C_MAX_PTR_SIZE 0x02`

Maximum number of slave register pointer bytes.

#### 3.6.2.9 `#define I2C_MSGSTAT_INACTIVE 0x0000`

I2C Message States Inactive I2C message state.

#### 3.6.2.10 `#define I2C_MSGSTAT_READ_BUSY 0x0023`

State indicating the I2C is busy with a read.

#### 3.6.2.11 `#define I2C_MSGSTAT_RESTART 0x0022`

Transmit a master read with a restart.

#### 3.6.2.12 `#define I2C_MSGSTAT_SEND_NOSTOP 0x0020`

Transmit a write with no stop.

#### 3.6.2.13 `#define I2C_MSGSTAT_SEND_NOSTOP_BUSY 0x0021`

State indicating the I2C is busy with a write with no stop.

#### 3.6.2.14 `#define I2C_MSGSTAT_SEND_WITHSTOP 0x0010`

Transmit a write with stop I2C message state.

#### 3.6.2.15 `#define I2C_MSGSTAT_WRITE_BUSY 0x0011`

State indicating the I2C is busy with a write with a stop.

#### 3.6.2.16 `#define I2C_SCD_ISRC 0x0006`

Stop detected condition I2C interrupt source.

### 3.6.3 Function Documentation

#### 3.6.3.1 `void i2cinit ( void )`

Initialises the I2C-A peripheral and relevant interrupts. This function will clear any values already in the I2C peripheral registers. This function **MUST** be called before any other public I2C function.

3.6.3.2 void i2cPopMsg ( i2cMsg \* *msg*, Uint16 *msgStatus*, Uint16 *slaveAddr*, Uint16 *numDataBytes*, Uint16 *numSlavePtrBytes*, Uint16 *slavePtrAddrHi*, Uint16 *slavePtrAddrLo* )

This function can be used to validate and populate the specified settings and values into the specified I2C message structure.

**Parameters**

out	<i>msg</i>	The I2C message structure.
in	<i>msgStatus</i>	The initial I2C message status.
in	<i>slaveAddr</i>	The slave address.
in	<i>numDataBytes</i>	The number, if any, of data bytes, above any slave register pointer bytes, in the message.
in	<i>numSlavePtr-Bytes</i>	The number, if any, of slave register pointer bytes.
in	<i>slavePtrAddrHi</i>	The slave register pointer high byte. If only one byte, or none, (as indicated by numSlavePtrbytes) is to be used leave this at zero.
in	<i>slavePtrAddrLo</i>	The slave register pointer low byte. If no pointer bytes (as indicated by num-SlavePtrbytes) are used leave this at zero.

**3.6.3.3 Uint16 i2cRead ( i2cMsg \* msg )**

Starts an I2C-A read using the settings specified. Read bytes are saved to the buffer msg.msgBuffer[].

**Parameters**

in	<i>msg</i>	The I2C message struct.
----	------------	-------------------------

**Returns**

Error status.

**3.6.3.4 Uint16 i2cWrite ( i2cMsg \* msg )**

Starts an I2C-A write using the settings and values specified.

**Parameters**

in	<i>msg</i>	The I2C message structure.
----	------------	----------------------------

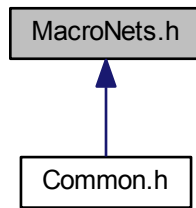
**Returns**

Error Status.

**3.7 MacroNets.h File Reference**

DPLib macro net and value control functions.

This graph shows which files directly or indirectly include this file:



### Data Structures

- struct [channelParameters](#)

### Macros

- #define [LOAD\\_0](#) 0
- #define [LOAD\\_1](#) 1
- #define [LOAD\\_2](#) 2
- #define [LOAD\\_3](#) 3
- #define [AC\\_I\\_CNTL](#) 4
- #define [DC\\_STAGE](#) 5
- #define [AC\\_STAGE](#) 6
- #define [V\\_MID\\_CH](#) 7

### Typedefs

- typedef enum [acOrDc opType](#)
- typedef enum [iOrVCtl ctlType](#)

### Enumerations

- enum [acOrDc](#) { [dc](#) = 0, [ac](#) = 1 }
- enum [iOrVCtl](#) { [iCtrl](#) = 0, [vCtrl](#) = 1 }

### Functions

- void [mnSetupChannels](#) (void)
- void [mnConnectNets](#) (void)
- void [mnStopAll](#) (void)
- void [mnRunAll](#) (void)

### Variables

- Uint16 [stopAll](#)
- Uint16 [enableAll](#)
- [channelParameters](#) [channel](#) [NUM\_CHNLS+1]

### 3.7.1 Detailed Description

DPLib macro net and value control functions.

### 3.7.2 Macro Definition Documentation

#### 3.7.2.1 `#define AC_I_CNTL 4`

The index position for AC I control settings.

#### 3.7.2.2 `#define AC_STAGE 6`

The index position for AC stage settings.

#### 3.7.2.3 `#define DC_STAGE 5`

The index position for DC stage settings.

#### 3.7.2.4 `#define LOAD_0 0`

The index position for Load 0 settings.

#### 3.7.2.5 `#define LOAD_1 1`

The index position for Load 1 settings.

#### 3.7.2.6 `#define LOAD_2 2`

The index position for Load 2 settings.

#### 3.7.2.7 `#define LOAD_3 3`

The index position for Load 3 settings.

#### 3.7.2.8 `#define V_MID_CH 7`

The index position for VMid settings.

### 3.7.3 Typedef Documentation

#### 3.7.3.1 `typedef enum iOrVCtl ctlType`

A type that allow specification of a channel's control mode setting.

#### 3.7.3.2 `typedef enum acOrDc opType`

A type that allow specification of a channel's output mode setting.

### 3.7.4 Enumeration Type Documentation

#### 3.7.4.1 enum acOrDc

The possible settings for channel output settings.

Enumerator

**dc** DC channel setting (0).

**ac** AC channel setting (1 or not-zero).

#### 3.7.4.2 enum iOrVctl

The possible settings for channel control setting.

Enumerator

**iCtrl** Current control setting (0).

**vCtrl** Voltage control setting (1 or not-zero).

### 3.7.5 Function Documentation

#### 3.7.5.1 void mnConnectNets ( void )

Connects the macro terminals to the relevant nets. This SHOULD be called AFTER DPL\_Init()

#### 3.7.5.2 void mnRunAll ( void )

Enables all IIR filter control law reference inputs.

#### 3.7.5.3 void mnSetupChannels ( void )

Initialises all channel settings structures with their default values.

Warning

This MUST be called AFTER [pwmMacroConfigure\(\)](#)

#### 3.7.5.4 void mnStopAll ( void )

Disables and zeros all IIR filter control law reference inputs, thus causing their outputs to ramp down to zero.

### 3.7.6 Variable Documentation

#### 3.7.6.1 channelParameters channel[NUM\_CHNLS+1]

A collection of the individual channel structures.

#### 3.7.6.2 Uint16 enableAll

Enable-all condition flag that allows status communication between the state machine tasks.

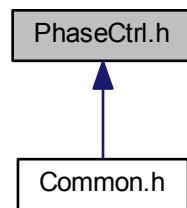
### 3.7.6.3 Uint16 stopAll

Stop-all condition flag that allows status communication between the state machine tasks.

## 3.8 PhaseCtrl.h File Reference

Signal generator phase (ACFBPHASE) control function.

This graph shows which files directly or indirectly include this file:



### Functions

- void [pcUpdate](#) (void)

### Variables

- volatile int32 \* [PHASE\\_CTRL\\_In](#)

### 3.8.1 Detailed Description

Signal generator phase (ACFBPHASE) control function.

#### Warning

This file is included by the file ISR.asm and thus any dependencies this file has should also be included there (e.g. PeripheralHeaderIncludes.h).

### 3.8.2 Function Documentation

#### 3.8.2.1 void pcUpdate ( void )

Updates GPIO19 based on state of \*PHASE\_CTRL\_In terminal. Expects 0 (GPIO19 set) or non-zero (GPIO19 cleared). This is generally called by the DPL\_ISR.asm

### 3.8.3 Variable Documentation

#### 3.8.3.1 volatile int32\* PHASE\_CTRL\_In

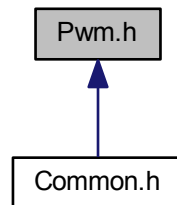
Phase control module signal input terminal.



## 3.9 Pwm.h File Reference

PWM and related functions.

This graph shows which files directly or indirectly include this file:



### Macros

- `#define PERIOD 600`

### Functions

- void `pwmTzConfigure` (void)
- void `pwmRstTz` (void)
- void `pwmMacroConfigure` (void)
- void `pwmSocConfigure` (void)
- void `pwmDPLTrigInit` (void)
- Uint16 `pwmSetFreq` (Uint32 freq)
- Uint16 `pwmGetFreq` (Uint32 \*freqDest)
- interrupt void `DPL_ISR` (void)

### Variables

- volatile int32 \* `PWMDRV_2ch_UpCnt_Duty1A`
- volatile int32 \* `PWMDRV_2ch_UpCnt_Duty1B`
- volatile int32 \* `PWMDRV_2ch_UpCnt_Duty2A`
- volatile int32 \* `PWMDRV_2ch_UpCnt_Duty2B`
- volatile int32 \* `PWMDRV_2ch_UpCnt_Duty3A`
- volatile int32 \* `PWMDRV_2ch_UpCnt_Duty3B`

### 3.9.1 Detailed Description

PWM and related functions.

### 3.9.2 Macro Definition Documentation

#### 3.9.2.1 `#define PERIOD 600`

Defines the initial PWM period setting = 60MHz / 600 = 100.

### 3.9.3 Function Documentation

#### 3.9.3.1 interrupt void DPL\_ISR ( void )

Digital power control loop interrupt service routine Located in the assembly file BurnInUnit\_ISR.asm

#### 3.9.3.2 void pwmDPLTrigInit ( void )

Initialises and enables PWM1 (master) to trigger the DPL ISR.

#### 3.9.3.3 Uint16 pwmGetFreq ( Uint32 \* *freqDest* )

Queries the current PWM frequency setting.

##### Parameters

out	<i>freqDest</i>	Address of the memory location at which to place the query result (hertz).
-----	-----------------	--

##### Returns

Error status.

#### 3.9.3.4 void pwmMacroConfigure ( void )

Configures each of the PWM macros for use.

#### 3.9.3.5 void pwmRstTz ( void )

Resets the trip zone after a comparator event.

#### 3.9.3.6 Uint16 pwmSetFreq ( Uint32 *freq* )

Sets the frequency of the PWMs.

##### Parameters

in	<i>freq</i>	Specifies the required frequency (hertz).
----	-------------	---

##### Returns

Error status.

#### 3.9.3.7 void pwmSocConfigure ( void )

Configures PWM1 (master) to generate ADC SOC start for ADC macro - configure before initialisation.

#### 3.9.3.8 void pwmTzConfigure ( void )

Configures PWM trip zones for use. Requires the comparator and DAC to be configured

##### See Also

[adc.h](#)

### 3.9.4 Variable Documentation

#### 3.9.4.1 volatile int32\* PWMDRV\_2ch\_UpCnt\_Duty1A

Channel 0 PWM terminal pointer.

#### 3.9.4.2 volatile int32\* PWMDRV\_2ch\_UpCnt\_Duty1B

Channel 1 PWM terminal pointers.

#### 3.9.4.3 volatile int32\* PWMDRV\_2ch\_UpCnt\_Duty2A

Channel 2 PWM terminal pointer.

#### 3.9.4.4 volatile int32\* PWMDRV\_2ch\_UpCnt\_Duty2B

Channel 3 PWM terminal pointer.

#### 3.9.4.5 volatile int32\* PWMDRV\_2ch\_UpCnt\_Duty3A

Interboost PWM terminal pointer.

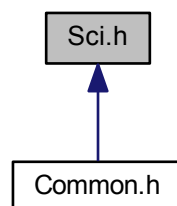
#### 3.9.4.6 volatile int32\* PWMDRV\_2ch\_UpCnt\_Duty3B

AC stage PWM terminal pointer.

## 3.10 Sci.h File Reference

SCI communications functions.

This graph shows which files directly or indirectly include this file:



### Macros

- #define SCIBAUD\_MIN 29
- #define SCIFFRX\_INT\_LVL 1
- #define SCIFFTX\_INT\_LVL 0
- #define SCIFFTX\_FILL\_LVL 4

## Functions

- Uint16 `scilnit` (Uint32 baud)
- void `sciTx` (void)

### 3.10.1 Detailed Description

SCI communications functions.

On the TI C2000 LaunchPad XL:

Function	GPIO	PCB Pin
SCI_RX	GPIO28	J1-3
SCI_TX	GPIO29	J1-4

#### Warning

On the TI C2000 LaunchPad XL the switch 4 (S4) should be in the OFF position to communicate with devices other than the on-board USB controller.

### 3.10.2 Macro Definition Documentation

#### 3.10.2.1 `#define SCIBAUD_MIN 29`

Minimum allowable value of SCI Baud.

#### 3.10.2.2 `#define SCIFFRX_INT_LVL 1`

Interrupt level for receiving FIFO.

#### 3.10.2.3 `#define SCIFFTX_FILL_LVL 4`

Fill level for transmission FIFO.

#### 3.10.2.4 `#define SCIFFTX_INT_LVL 0`

Interrupt level for transmission FIFO.

### 3.10.3 Function Documentation

#### 3.10.3.1 Uint16 `scilnit` ( Uint32 *baud* )

Initialises the SCI(A) peripheral and relate interrupts.

##### Parameters

<i>in</i>	<i>baud</i>	The baud rate that the SCI should use minimum value is set by SCIBAUD_MIN.
-----------	-------------	--

##### Returns

Error status.

**Warning**

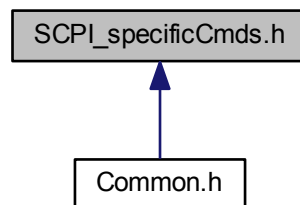
This function **MUST** be called before any other SCI(A) function.  
 This function will clear any values placed in the associated SCI(A) registers prior to calling.

**3.10.3.2 void sciTx ( void )**

Transmits whatever data is on the SCPI output queue.

**3.11 SCPI\_specificCmds.h File Reference**

This graph shows which files directly or indirectly include this file:

**3.11.1 Detailed Description**

This file includes the functions for registering the application specific commands and the related callback functions

These commands and their associated callback functions can be edited to suit the application in use, though they should follow the guidelines and conventions laid out in IEEE 488.2-1992 and SCPI-1999

Please follow the information in the parser documentation and the file SCPI\_specificCmds.c on how to add or change message headers, commands and parameters

To register a new command device specific tree node, add a registerChild() call to the function registerSpecificCommands() with the form "err += registerChild(CHILD\_LONG\_NAME, PARENT\_SHORT\_NAME, BOOL\_IS\_HEADER\_ONLY, BOOL\_IS\_QUERY, CALLBACK\_HANDLE);", where:

- CHILD\_LONG\_NAME is the long form of the name of the child node to be registered.
- PARENT\_SHORT\_NAME is the short form of the name of the parent of the child node to be registered. This must be an already existing node.
- BOOL\_IS\_HEADER\_ONLY is a boolean value that indicates if the node being registered is a header only (i.e. is not a command) or not.
- BOOL\_IS\_QUERY is a boolean value that indicates if the node being registered may be queried or not.
- CALLBACK\_HANDLE is a pointer to the function to be associated with this child if it is a command, or NULL if it is a header only.

For further information on the operation of registerChild() please see the function description.

All string literals **MUST** be in upper case and enclosed in quotation marks.

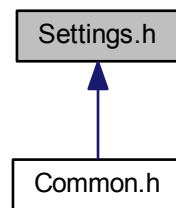
Node long and short names must conform to the standard as outlined by SCPI-99 in 6.2.1

All tree nodes are children of at least "ROOT". Changes to the number of node registered should be reflected by the user in the number defined for TREE\_CHILD\_LIMIT in scpi.h

### 3.12 Settings.h File Reference

Major build definitions and settings for the project.

This graph shows which files directly or indirectly include this file:



#### Macros

- #define INCR\_BUILD 2
- #define DEBUG
- #define DUAL\_CNTL\_AC
- #define VSSA 0l
- #define VMID\_R1 540.0
- #define VMID\_R2 4.3
- #define VAC\_R1 540.0
- #define VAC\_R2 4.3
- #define NUM\_ICTRL\_CHNLS 5
- #define NUM\_VCTRL\_CHNLS 2
- #define NUM\_CHNLS NUM\_ICTRL\_CHNLS + NUM\_VCTRL\_CHNLS
- #define SQRT\_2 1.41429
- #define RECP\_SQRT\_2 0.70711
- #define VDDA 3300l
- #define uSec100 6000
  
- #define CHANNEL\_OOB 0x10
- #define VALUE\_OOB 0x11
- #define OCP\_TRIP 0x12
- #define OVP\_TRIP 0x13
- #define OTP\_TRIP 0x14
- #define I2C\_READ\_WRONG\_MSG 0x20
- #define I2C\_WRITE\_WRONG\_MSG 0x21
- #define I2C\_STP\_NOT\_READY 0x22
- #define I2C\_BUS\_BUSY 0x23
- #define I2C\_INVALID\_ISRC 0x24

### 3.12.1 Detailed Description

Major build definitions and settings for the project.

#### Warning

This file is included and referenced by ISR.asm, main() and [mnConnectNets\(\)](#).  
When changes are made to this file please use rebuild all.

### 3.12.2 Macro Definition Documentation

#### 3.12.2.1 #define CHANNEL\_OOB 0x10

Channel out of bounds error code.

#### 3.12.2.2 #define DEBUG

Includes and makes functions and variables public that are used only for debugging purposes.

#### 3.12.2.3 #define DUAL\_CNTL\_AC

Uses the dual CNTL AC control instead of single VCtrl. Cannot be used if PID is still in use.

#### 3.12.2.4 #define I2C\_BUS\_BUSY 0x23

I2C bus already busy error code.

#### 3.12.2.5 #define I2C\_INVALID\_ISRC 0x24

Invalid I2C interrupt source error code.

#### 3.12.2.6 #define I2C\_READ\_WRONG\_MSG 0x20

Incorrect type I2C message read error code.

#### 3.12.2.7 #define I2C\_STP\_NOT\_READY 0x22

I2C stop bit was not yet received error code.

#### 3.12.2.8 #define I2C\_WRITE\_WRONG\_MSG 0x21

Incorrect type I2C write message error code.

#### 3.12.2.9 #define INCR\_BUILD 2

Alters the digital power control loop between closed or open. Open-Loop: 1. Closed-loop: 2.

#### 3.12.2.10 #define NUM\_CHNLS NUM\_ICTRL\_CHNLS + NUM\_VCTRL\_CHNLS

Total number of IIR filter control law macros used (doesn't include VMID semi-channel).

**3.12.2.11 #define NUM\_ICTRL\_CHNLS 5**

The number of current, or 2-pole 2-zero, IIR filter control law macros used.

**3.12.2.12 #define NUM\_VCTRL\_CHNLS 2**

The number of voltage, or 3-pole 3-zero, IIR filter control law macros used.

**3.12.2.13 #define OCP\_TRIP 0x12**

Over-current protection trip error code.

**3.12.2.14 #define OTP\_TRIP 0x14**

Over-temperature protection trip error code.

**3.12.2.15 #define OVP\_TRIP 0x13**

Over-voltage protection trip error code.

**3.12.2.16 #define RECP\_SQRT\_2 0.70711**

1/sqrt(2) constant used for RMS calculations.

**3.12.2.17 #define SQRT\_2 1.41429**

Sqrt(2) constant used for RMS calculations.

**3.12.2.18 #define uSec100 6000**

100us - System define.

**3.12.2.19 #define VAC\_R1 540.0**

Scaling voltage divider R1 resistor value for VAC ADC.

**3.12.2.20 #define VAC\_R2 4.3**

Scaling voltage divider R2 resistor value for VAC ADC.

**3.12.2.21 #define VALUE\_OOB 0x11**

Value out of bounds error code.

**3.12.2.22 #define VDDA 3300I**

System VMAXREF (millivolts).



## 3.12.2.23 #define VMID\_R1 540.0

Scaling voltage divider R1 resistor value for VMID ADC.

## 3.12.2.24 #define VMID\_R2 4.3

Scaling voltage divider R2 resistor value for VMID ADC.

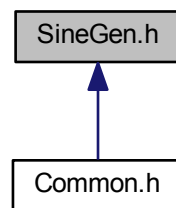
## 3.12.2.25 #define VSSA 0l

System VLOWREF (millivolts).

## 3.13 SineGen.h File Reference

Signal generator functions.

This graph shows which files directly or indirectly include this file:



### Macros

- #define [SIN\\_DFLT\\_RCTFY](#) TRUE
- #define [SIN\\_DFLT\\_OFST](#) 0
- #define [SIN\\_DFLT\\_PHSE](#) 0
- #define [SIN\\_DFLT\\_GAIN](#) 0.9
- #define [SIN\\_DFLT\\_F](#) 50.0
- #define [SIN\\_DFLT\\_F\\_MAX](#) 1000u
- #define [SIN\\_CHANNEL AC\\_STAGE](#)
- #define [SIN\\_F\\_SPL](#) 8250u

### Functions

- void [sgInit](#) (void)
- void [sgUpdate](#) (void)
- void [sgGainUpdate](#) (void)
- Uint16 [sgSetState](#) (Uint16 stt)
- Uint16 [sgSetRectify](#) (Uint16 rfy)
- Uint16 [sgSetOffset](#) (float32 ofst)
- Uint16 [sgSetInitialPhase](#) (float32 phs)

- Uint16 [sgSetGainTarget](#) (float32 gnt)
- Uint16 [sgSetFreq](#) (Uint16 frq)
- Uint16 [sgGetState](#) (Uint16 \*sttDest)
- Uint16 [sgGetRectify](#) (Uint16 \*rfyDest)
- Uint16 [sgGetOffset](#) (float32 \*oftDest)
- Uint16 [sgGetGainTarget](#) (float32 \*gntDest)
- Uint16 [sgGetFreq](#) (Uint16 \*frqDest)
- Uint16 [sgGetResolution](#) (float32 \*rslDest)

## Variables

- volatile int32 \* [SGENTI\\_1ch\\_VOut](#)
- volatile int32 \* [SGENTI\\_1ch\\_Sign](#)

### 3.13.1 Detailed Description

Signal generator functions. [sgInit\(\)](#) must be called before any other signal generator functions are used. Note that the frequency resolution is determined by the maximum frequency and the step max. For further details, see the signal generator library documentation (Texas Instruments Signal Generator Library Module user's Guide).

#### Warning

This file is included by the file ISR.asm.

### 3.13.2 Macro Definition Documentation

#### 3.13.2.1 `#define SIN_CHANNEL AC_STAGE`

Defines which channel enable controls the generator output.

#### 3.13.2.2 `#define SIN_DFLT_F 50.0`

Initial frequency setting (hertz).

#### 3.13.2.3 `#define SIN_DFLT_F_MAX 1000u`

Initial maximum frequency setting (hertz).

#### 3.13.2.4 `#define SIN_DFLT_GAIN 0.9`

Initial gain setting [0.0, 1.0].

#### 3.13.2.5 `#define SIN_DFLT_OFST 0`

Initial offset setting [-0.5, +0.5], IQ15.

#### 3.13.2.6 `#define SIN_DFLT_PHSE 0`

Initial initial phase setting [0, 360), IQ16.

3.13.2.7 `#define SIN_DFLT_RCTFY TRUE`

Initial rectification setting [TRUE | FALSE].

3.13.2.8 `#define SIN_F_SPL 8250u`

Signal sampling frequency, i.e. the frequency that `sgen.calc()` is called at. This is dependent on ISR frequency, currently 1/4 of `f_ISR`, full ISR speed is 33,000Hz.

## 3.13.3 Function Documentation

3.13.3.1 `void sgGainUpdate ( void )`

Updates the gain value to create a slow-start ramp. This should be called at the same time and similarly to the DC slew update.

See Also

[scSlewUpdate\(\)](#)

3.13.3.2 `Uint16 sgGetFreq ( Uint16 * freqDest )`

Queries the current frequency setting.

Parameters

<code>out</code>	<code><i>freqDest</i></code>	Address of the memory location at which to place the query result (hertz).
------------------	------------------------------	--

Returns

Error status.

3.13.3.3 `Uint16 sgGetGainTarget ( float32 * gntDest )`

Queries the current target gain setting.

Parameters

<code>out</code>	<code><i>gntDest</i></code>	Address of the memory location at which to place the query result.
------------------	-----------------------------	--

Returns

Error status.

3.13.3.4 `Uint16 sgGetOffset ( float32 * oftDest )`

Queries the current signal DC offset setting.

Parameters

out	<i>offDest</i>	Address of the memory location at which to place the query result.
-----	----------------	--

**Returns**

Error status.

**3.13.3.5 Uint16 sgGetRectify ( Uint16 \* rfyDest )**

Queries the current state of the signal generator rectification enable.

**Parameters**

out	<i>rfyDest</i>	Address of the memory location at which to place the query result {1:ON   0:OF}.
-----	----------------	--

**Returns**

Error status.

**3.13.3.6 Uint16 sgGetResolution ( float32 \* rslDest )**

Queries the current frequency resolution. This is equal to  $f_{max} / \text{step\_max}$ .

**Parameters**

out	<i>rslDest</i>	Address of the memory location at which to place the query result.
-----	----------------	--

**Returns**

Error status.

**3.13.3.7 Uint16 sgGetState ( Uint16 \* sttDest )**

Queries the current state of the generator output.

**Parameters**

out	<i>sttDest</i>	Address of the memory location at which to place the query result {1:ON   0:OFF}.
-----	----------------	---

**Returns**

Error status.

**3.13.3.8 void sgInit ( void )**

Sets the initial generator values and disables the output. This function MUST be called before any other signal generator function.

**3.13.3.9 Uint16 sgSetFreq ( Uint16 frq )**

Sets the signal frequency.

## Parameters

<i>in</i>	<i>frq</i>	Frequency value [0, $f_{max}$ ] (hertz).
-----------	------------	--

## Returns

Error status.

3.13.3.10 Uint16 sgSetGainTarget ( float32 *gnt* )

Sets the target gain of the signal.

## Parameters

<i>in</i>	<i>gnt</i>	Gain target value [0.0, 1.0).
-----------	------------	-------------------------------

## Returns

Error status.

3.13.3.11 Uint16 sgSetInitialPhase ( float32 *phs* )

Sets the signal initial phase value

## Parameters

<i>in</i>	<i>phs</i>	Initial phase value [0, 360) (degrees).
-----------	------------	---

## Returns

Error status

3.13.3.12 Uint16 sgSetOffset ( float32 *ofst* )

Sets the signal DC offset

## Parameters

<i>in</i>	<i>ofst</i>	DC offset value [-0.5, +0.5].
-----------	-------------	-------------------------------

## Returns

Error status.

3.13.3.13 Uint16 sgSetRectify ( Uint16 *rfy* )

Enables or disables the rectification of the generator output

## Parameters

<i>in</i>	<i>rfy</i>	Rectification enable state {1:ON   0:OFF}.
-----------	------------	--

## Returns

Error status.

#### 3.13.3.14 Uint16 sgSetState ( Uint16 *stt* )

Enables or disables the output of the generator onto the connected net

## Parameters

<i>in</i>	<i>stt</i>	Output enable state {1:ON   0:OFF}.
-----------	------------	-------------------------------------

## Returns

Error status.

## 3.13.3.15 void sgUpdate ( void )

Generates the next signal data point and loads it onto the VOut terminal. If the point is positive the sign terminal is set, otherwise it is cleared. If rectify is enabled, the value produced will be an absolute value.

## 3.13.4 Variable Documentation

## 3.13.4.1 volatile int32\* SGENTI\_1ch\_Sign

Voltage sign (pre-rectification) output terminal.

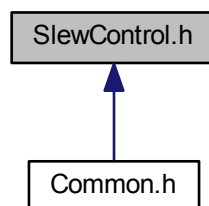
## 3.13.4.2 volatile int32\* SGENTI\_1ch\_VOut

Voltage output terminal.

## 3.14 SlewControl.h File Reference

Slew control functions.

This graph shows which files directly or indirectly include this file:



## Functions

- void [scSlewUpdate](#) (void)
- Uint16 [scSetTarget](#) (Uint16 chnl, float32 trgt)
- Uint16 [scSetStep](#) (Uint16 chnl, float32 stp)
- Uint16 [scSetState](#) (Uint16 chnl, Uint16 stt)
- Uint16 [scSetTargetAll](#) (float32 trgt)
- Uint16 [scSetStepAll](#) (float32 stp)
- Uint16 [scSetStateAll](#) (Uint16 stt)

- Uint16 [scGetTarget](#) (Uint16 *chnl*, float32 \**trgtDest*)
- Uint16 [scGetStep](#) (Uint16 *chnl*, float32 \**stpDest*)
- Uint16 [scGetState](#) (Uint16 *chnl*, Uint16 \**sttDest*)

### 3.14.1 Detailed Description

Slew control functions.

### 3.14.2 Function Documentation

#### 3.14.2.1 Uint16 [scGetState](#) ( Uint16 *chnl*, Uint16 \* *sttDest* )

Queries the current reference net enable state for the specified channel.

##### Parameters

in	<i>chnl</i>	Specifies the channel the setting is to be read from.
out	<i>sttDest</i>	Address of the memory location at which to place the query result (0:OFF   non-zero:ON).

##### Returns

Error status.

#### 3.14.2.2 Uint16 [scGetStep](#) ( Uint16 *chnl*, float32 \* *stpDest* )

Queries the current slew step size of the specified channel.

##### Parameters

in	<i>chnl</i>	Specifies the channel the setting is to be read from.
out	<i>stpDest</i>	Address of the memory location at which to place the query result (amps or volts).

##### Returns

Error status.

#### 3.14.2.3 Uint16 [scGetTarget](#) ( Uint16 *chnl*, float32 \* *trgtDest* )

Queries the current slew target setting for the specified channel.

##### Parameters

in	<i>chnl</i>	Specifies the channel the setting is to be read from.
out	<i>trgtDest</i>	Address of the memory location at which to place the query result (amps or volts).

##### Returns

Error status.

#### 3.14.2.4 Uint16 [scSetState](#) ( Uint16 *chnl*, Uint16 *stt* )

Sets the reference net enable state for the specified channel.



**Parameters**

in	<i>chnl</i>	Specifies the channel the setting is to be applied to.
in	<i>stt</i>	Specifies the reference net state to be applied (0:OFF   non-zero:ON).

**Returns**

Error Status.

**3.14.2.5 Uint16 scSetStateAll ( Uint16 *stt* )**

Sets all channels' reference net enable state.

**Parameters**

in	<i>stt</i>	Specifies the reference net state to be applied (0:OFF   non-zero:ON).
----	------------	--

**Returns**

Error status.

**3.14.2.6 Uint16 scSetStep ( Uint16 *chnl*, float32 *stp* )**

Sets the slew step size for the specified channel.

**Parameters**

in	<i>chnl</i>	Specifies the channel the setting is to be applied to.
in	<i>stp</i>	Specifies the value of the slew step size to be applied (amps or volts).

**Returns**

Error status.

**3.14.2.7 Uint16 scSetStepAll ( float32 *stp* )**

Sets all channels' slew step size.

**Parameters**

in	<i>stp</i>	Specifies the value of the slew step size to be applied (amps or volts).
----	------------	--

**Returns**

Error status.

**3.14.2.8 Uint16 scSetTarget ( Uint16 *chnl*, float32 *trgt* )**

Sets the slew target for the specified channel.

**Parameters**


---

in	<i>chnl</i>	Specifies the channel the setting is to be applied to.
in	<i>trgt</i>	Specifies the value of the slew target to be applied (amps or volts).

**Returns**

Error status.

**3.14.2.9 Uint16 scSetTargetAll ( float32 *trgt* )**

Sets all channels' slew target

**Parameters**

in	<i>trgt</i>	Specifies the value of the slew target to be applied (amps or volts).
----	-------------	---

**Returns**

Error status.

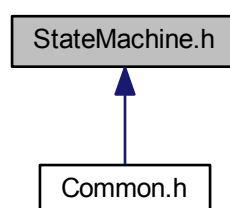
**3.14.2.10 void scSlewUpdate ( void )**

Advances the slew ramps for all relevant channels. Does not apply to channels that use sine references

**3.15 StateMachine.h File Reference**

State machine functions.

This graph shows which files directly or indirectly include this file:

**Functions**

- void [smlnit](#) (void)

**Variables**

- void(\* [Alpha\\_State\\_Ptr](#) )(void)

### 3.15.1 Detailed Description

State machine functions.

### 3.15.2 Function Documentation

#### 3.15.2.1 void smlInit ( void )

Sets up the state machine (incl. timers) ready for use.

### 3.15.3 Variable Documentation

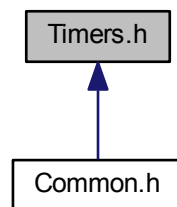
#### 3.15.3.1 void(\* Alpha\_State\_Ptr)(void)

Runs the next iteration of the state machine. Should be called from the main super-loop.

## 3.16 Timers.h File Reference

Real and virtual timer functions.

This graph shows which files directly or indirectly include this file:



### Functions

- void [timersSetupReal](#) (void)

### 3.16.1 Detailed Description

Real and virtual timer functions. These functions should be run as part of the state machine setup.

See Also

[StateMachine.h](#)

### 3.16.2 Function Documentation

### 3.16.2.1 void timersSetupReal ( void )

Sets up the real timers that run the state machine This should be called as part of the state machine initialisation.

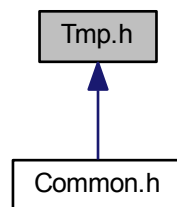
See Also

[smInit\(\)](#)

## 3.17 Tmp.h File Reference

Temperature sensor functions.

This graph shows which files directly or indirectly include this file:



### Macros

- #define [ADC\\_I2C\\_ADDR](#) 0x48
- #define [ADC\\_NUM\\_CHNL](#) 0x08
- #define [ADC\\_VREF](#) 5.0
- #define [ADC\\_STPS](#) 256
- #define [TMP\\_V0C\\_OFST](#) 0.4
- #define [TMP\\_SCL\\_OFST](#)  $\text{TMP\_V0C\_OFST} * \text{ADC\_STPS} / \text{ADC\_VREF}$
- #define [TMP\\_E\\_T\\_COLD](#) 1.5

### Functions

- Uint16 [tmpInit](#) (void)
- Uint16 [tmpSetOtp](#) (Uint16 chnl, float32 tmp)
- Uint16 [tmpGetOtp](#) (Uint16 chnl, float32 \*tmpDest)
- Uint16 [tmpCheckOtp](#) (void)
- Uint16 [tmpRead](#) (Uint16 chnl, float32 \*tmpDest)

### 3.17.1 Detailed Description

Temperature sensor functions. The temperature sensor (MCP9701) output is read via an external ADC (ADS7830) that is connected to the I2C bus at address 10010xx where 'xx' is dependent upon the configuration of resistors R75 - R78. All temperatures are in degrees Celcius.

### Warning

Before any temperature functions can be used the I2C peripheral MUST be initialised and `tmpInit()` must be run - `tmpInit()` will require the interrupts to be enabled globally.

### See Also

`i2cInit()`

## 3.17.2 Macro Definition Documentation

### 3.17.2.1 `#define ADC_I2C_ADDR 0x48`

Slave I2C address (ADS7830 8-channel ADC - A0 = 0, A1 = 0).

### 3.17.2.2 `#define ADC_NUM_CHNL 0x08`

Number of temperature channels. The program expects a 50/50 split with first half for the on-board temperature channels.

### 3.17.2.3 `#define ADC_STPS 256`

Number of ADS7830 ADC steps.

### 3.17.2.4 `#define ADC_VREF 5.0`

ADS7830 ADC reference voltage (volts).

### 3.17.2.5 `#define TMP_E_T_COLD 1.5`

MCP9701 Error at lowest operating temperature ( ° C), calculated as shown in Microchip AN1001.

### 3.17.2.6 `#define TMP_SCL_OFST TMP_V0C_OFST * ADC_STPS / ADC_VREF`

Scaled temperature offset.

### 3.17.2.7 `#define TMP_V0C_OFST 0.4`

MCP9701 Temperature sensor  $V_{0^{\circ}C}$  (volts).

## 3.17.3 Function Documentation

### 3.17.3.1 `Uint16 tmpCheckOtp ( void )`

Tests the current on-board temperature sensor readings against the OTP limits.

### Returns

Error status.

### 3.17.3.2 `Uint16 tmpGetOtp ( Uint16 chnl, float32 * tmpDest )`

Queries the on-board over temperature limit for the specified channel. The I2C peripheral and temperature reading interface MUST be initialised before this function is used.

**Parameters**

in	<i>chnl</i>	Specifies the channel the setting is to be read from.
out	<i>tmpDest</i>	Address of the memory location at which to place the query result ( ° C).

**Returns**

Error status.

**3.17.3.3 Uint16 tmpInit ( void )**

Initialises the system for temperature readings. The I2C peripheral must be initialised before this function is used

**See Also**

[i2cInit\(\)](#).

**Returns**

Error status.

**3.17.3.4 Uint16 tmpRead ( Uint16 chnl, float32 \* tmpDest )**

Queries the current on-board temperature of the specified channel.

**Parameters**

in	<i>chnl</i>	Specifies the channel the temperature is to be read from.
out	<i>tmpDest</i>	Address of the memory location at which to place the query result ( ° C).

**Returns**

Error status.

**3.17.3.5 Uint16 tmpSetOtp ( Uint16 chnl, float32 tmp )**

Sets the on-board over temperature limit for the specified channel. The I2C peripheral and temperature reading interface MUST be initialised before this function is used.

**Parameters**

in	<i>chnl</i>	Specifies the channel the setting is to be applied to.
in	<i>tmp</i>	Specifies the value of the limit to be applied ( ° C).

**Returns**

Error status.

# Index

AC\_I\_CNTL  
    MacroNets.h, [34](#)  
AC\_STAGE  
    MacroNets.h, [34](#)  
ADC\_I2C\_ADDR  
    Tmp.h, [57](#)  
ADC\_NUM\_CHNL  
    Tmp.h, [57](#)  
ADC\_STPS  
    Tmp.h, [57](#)  
ADC\_VREF  
    Tmp.h, [57](#)  
ADCDRV\_1ch\_Rlt1  
    Adc.h, [14](#)  
ADCDRV\_1ch\_Rlt10  
    Adc.h, [14](#)  
ADCDRV\_1ch\_Rlt11  
    Adc.h, [14](#)  
ADCDRV\_1ch\_Rlt12  
    Adc.h, [14](#)  
ADCDRV\_1ch\_Rlt13  
    Adc.h, [14](#)  
ADCDRV\_1ch\_Rlt2  
    Adc.h, [14](#)  
ADCDRV\_1ch\_Rlt3  
    Adc.h, [14](#)  
ADCDRV\_1ch\_Rlt4  
    Adc.h, [14](#)  
ADCDRV\_1ch\_Rlt5  
    Adc.h, [14](#)  
ADCDRV\_1ch\_Rlt6  
    Adc.h, [14](#)  
ADCDRV\_1ch\_Rlt7  
    Adc.h, [15](#)  
ADCDRV\_1ch\_Rlt8  
    Adc.h, [15](#)  
ADCDRV\_1ch\_Rlt9  
    Adc.h, [15](#)  
ac  
    MacroNets.h, [35](#)  
acFrequency  
    channelParameters, [5](#)  
acOrDc  
    MacroNets.h, [35](#)  
Adc.h, [9](#)  
    ADCDRV\_1ch\_Rlt1, [14](#)  
    ADCDRV\_1ch\_Rlt10, [14](#)  
    ADCDRV\_1ch\_Rlt11, [14](#)  
    ADCDRV\_1ch\_Rlt12, [14](#)  
    ADCDRV\_1ch\_Rlt13, [14](#)  
    ADCDRV\_1ch\_Rlt2, [14](#)  
    ADCDRV\_1ch\_Rlt3, [14](#)  
    ADCDRV\_1ch\_Rlt4, [14](#)  
    ADCDRV\_1ch\_Rlt5, [14](#)  
    ADCDRV\_1ch\_Rlt6, [14](#)  
    ADCDRV\_1ch\_Rlt7, [15](#)  
    ADCDRV\_1ch\_Rlt8, [15](#)  
    ADCDRV\_1ch\_Rlt9, [15](#)  
    adcCheckOcp, [10](#)  
    adcCheckOvp, [10](#)  
    adcCompConfigure, [10](#)  
    adcGetCurrent, [10](#)  
    adcGetDac, [11](#)  
    adcGetIScale, [11](#)  
    adcGetOcp, [11](#)  
    adcGetOvp, [11](#)  
    adcGetVScale, [12](#)  
    adcGetVoltage, [12](#)  
    adcMacroConfigure, [12](#)  
    adcSetDac, [12](#)  
    adcSetIScale, [13](#)  
    adcSetOcp, [13](#)  
    adcSetOvp, [13](#)  
    adcSetVScale, [13](#)  
    adcSocCnf, [14](#)  
    adcCheckOcp  
        Adc.h, [10](#)  
    adcCheckOvp  
        Adc.h, [10](#)  
    adcCompConfigure  
        Adc.h, [10](#)  
    adcGetCurrent  
        Adc.h, [10](#)  
    adcGetDac  
        Adc.h, [11](#)  
    adcGetIScale  
        Adc.h, [11](#)  
    adcGetOcp  
        Adc.h, [11](#)  
    adcGetOvp  
        Adc.h, [11](#)  
    adcGetVScale  
        Adc.h, [12](#)  
    adcGetVoltage  
        Adc.h, [12](#)  
    adcMacroConfigure  
        Adc.h, [12](#)  
    adcSetDac



Adc.h, [12](#)  
 adcSetIScale  
     Adc.h, [13](#)  
 adcSetOcp  
     Adc.h, [13](#)  
 adcSetOvp  
     Adc.h, [13](#)  
 adcSetVScale  
     Adc.h, [13](#)  
 adcSocCnf  
     Adc.h, [14](#)  
 Alpha\_State\_Ptr  
     StateMachine.h, [55](#)  
  
 BST\_NUM\_CHNL  
     BstEn.h, [16](#)  
 bcDisable  
     BstEn.h, [17](#)  
 bcEnable  
     BstEn.h, [17](#)  
 bcInit  
     BstEn.h, [18](#)  
 BstEn.h, [15](#)  
     BST\_NUM\_CHNL, [16](#)  
     bcDisable, [17](#)  
     bcEnable, [17](#)  
     bcInit, [18](#)  
     IOE\_DEFVAL\_ADDR, [16](#)  
     IOE\_GPINTEN\_ADDR, [16](#)  
     IOE\_GPIO\_ADDR, [16](#)  
     IOE\_GPPU\_ADDR, [16](#)  
     IOE\_I2C\_ADDR, [16](#)  
     IOE\_INTCAP\_ADDR, [16](#)  
     IOE\_INTCON\_ADDR, [16](#)  
     IOE\_INTF\_ADDR, [17](#)  
     IOE\_IOCON\_ADDR, [17](#)  
     IOE\_IODIR\_ADDR, [17](#)  
     IOE\_IPOL\_ADDR, [17](#)  
     IOE\_NUM\_CHNL, [17](#)  
     IOE\_OLAT\_ADDR, [17](#)  
  
 cA1  
     Cntl.h, [20](#)  
 cA2  
     Cntl.h, [20](#)  
 cA3  
     Cntl.h, [20](#)  
 cB0  
     Cntl.h, [20](#)  
 cB1  
     Cntl.h, [20](#)  
 cB2  
     Cntl.h, [20](#)  
 cB3  
     Cntl.h, [20](#)  
 cMax  
     Cntl.h, [20](#)  
 cMin  
     Cntl.h, [20](#)  
  
 CHANNEL\_OOB  
     Settings.h, [43](#)  
 CNTL\_2P2Z\_Coef1  
     Cntl.h, [21](#)  
 CNTL\_2P2Z\_Coef2  
     Cntl.h, [21](#)  
 CNTL\_2P2Z\_Coef3  
     Cntl.h, [21](#)  
 CNTL\_2P2Z\_Coef4  
     Cntl.h, [21](#)  
 CNTL\_2P2Z\_Coef5  
     Cntl.h, [21](#)  
 CNTL\_2P2Z\_Fdbk1  
     Cntl.h, [21](#)  
 CNTL\_2P2Z\_Fdbk2  
     Cntl.h, [21](#)  
 CNTL\_2P2Z\_Fdbk3  
     Cntl.h, [21](#)  
 CNTL\_2P2Z\_Fdbk4  
     Cntl.h, [21](#)  
 CNTL\_2P2Z\_Fdbk5  
     Cntl.h, [22](#)  
 CNTL\_2P2Z\_Out1  
     Cntl.h, [22](#)  
 CNTL\_2P2Z\_Out2  
     Cntl.h, [22](#)  
 CNTL\_2P2Z\_Out3  
     Cntl.h, [22](#)  
 CNTL\_2P2Z\_Out4  
     Cntl.h, [22](#)  
 CNTL\_2P2Z\_Out5  
     Cntl.h, [22](#)  
 CNTL\_2P2Z\_Ref1  
     Cntl.h, [22](#)  
 CNTL\_2P2Z\_Ref2  
     Cntl.h, [22](#)  
 CNTL\_2P2Z\_Ref3  
     Cntl.h, [22](#)  
 CNTL\_2P2Z\_Ref4  
     Cntl.h, [22](#)  
 CNTL\_2P2Z\_Ref5  
     Cntl.h, [22](#)  
 CNTL\_3P3Z\_Coef1  
     Cntl.h, [22](#)  
 CNTL\_3P3Z\_Coef2  
     Cntl.h, [23](#)  
 CNTL\_3P3Z\_Fdbk1  
     Cntl.h, [23](#)  
 CNTL\_3P3Z\_Fdbk2  
     Cntl.h, [23](#)  
 CNTL\_3P3Z\_Out1  
     Cntl.h, [23](#)  
 CNTL\_3P3Z\_Out2  
     Cntl.h, [23](#)  
 CNTL\_3P3Z\_Ref1  
     Cntl.h, [23](#)  
 CNTL\_3P3Z\_Ref2  
     Cntl.h, [23](#)

- cfType
  - Cntl.h, 20
- chEnable
  - channelParameters, 5
- channel
  - MacroNets.h, 35
- channelParameters, 5
  - acFrequency, 5
  - chEnable, 5
  - ctlMode, 6
  - iFdbkNet, 6
  - iMaxRms, 6
  - iMinRms, 6
  - iScale, 6
  - ocp, 6
  - opMode, 6
  - otp, 6
  - outNet, 6
  - ovp, 6
  - refNet, 6
  - slewRate, 6
  - target, 7
  - vFdbkNet, 7
  - vGainLmt, 7
  - vMaxRms, 7
  - vMinRms, 7
  - vScale, 7
- Cntl.h
  - cA1, 20
  - cA2, 20
  - cA3, 20
  - cB0, 20
  - cB1, 20
  - cB2, 20
  - cB3, 20
  - cMax, 20
  - cMin, 20
- Cntl.h, 18
  - CNTL\_2P2Z\_Coef1, 21
  - CNTL\_2P2Z\_Coef2, 21
  - CNTL\_2P2Z\_Coef3, 21
  - CNTL\_2P2Z\_Coef4, 21
  - CNTL\_2P2Z\_Coef5, 21
  - CNTL\_2P2Z\_Fdbk1, 21
  - CNTL\_2P2Z\_Fdbk2, 21
  - CNTL\_2P2Z\_Fdbk3, 21
  - CNTL\_2P2Z\_Fdbk4, 21
  - CNTL\_2P2Z\_Fdbk5, 22
  - CNTL\_2P2Z\_Out1, 22
  - CNTL\_2P2Z\_Out2, 22
  - CNTL\_2P2Z\_Out3, 22
  - CNTL\_2P2Z\_Out4, 22
  - CNTL\_2P2Z\_Out5, 22
  - CNTL\_2P2Z\_Ref1, 22
  - CNTL\_2P2Z\_Ref2, 22
  - CNTL\_2P2Z\_Ref3, 22
  - CNTL\_2P2Z\_Ref4, 22
  - CNTL\_2P2Z\_Ref5, 22
  - CNTL\_3P3Z\_Coef1, 22
  - CNTL\_3P3Z\_Coef2, 23
  - CNTL\_3P3Z\_Fdbk1, 23
  - CNTL\_3P3Z\_Fdbk2, 23
  - CNTL\_3P3Z\_Out1, 23
  - CNTL\_3P3Z\_Out2, 23
  - CNTL\_3P3Z\_Ref1, 23
  - CNTL\_3P3Z\_Ref2, 23
  - cfType, 20
  - cntlGetCoef, 20
  - cntlSetCoef, 20
  - cntlUpdateCoefs, 21
  - coefNum, 20
  - coefs2, 23
  - coefs3, 23
  - SATMAX\_MAX, 20
- cntlGetCoef
  - Cntl.h, 20
- cntlSetCoef
  - Cntl.h, 20
- cntlUpdateCoefs
  - Cntl.h, 21
- coefNum
  - Cntl.h, 20
- coefs2
  - Cntl.h, 23
- coefs3
  - Cntl.h, 23
- Common.h, 23
- ctlMode
  - channelParameters, 6
- ctlType
  - MacroNets.h, 34
- DC\_STAGE
  - MacroNets.h, 34
- DEBUG
  - Settings.h, 43
- DPL\_ISR
  - Pwm.h, 38
- DUAL\_CNTL\_AC
  - Settings.h, 43
- dc
  - MacroNets.h, 35
- enableAll
  - MacroNets.h, 35
- FAN\_CHNL\_OFST
  - FanEn.h, 25
- FAN\_NUM\_CHNL
  - FanEn.h, 25
- FanEn.h, 24
  - FAN\_CHNL\_OFST, 25
  - FAN\_NUM\_CHNL, 25
  - fcDisable, 26
  - fcEnable, 27
  - fcInit, 27
  - IOE\_DEFVAL\_ADDR, 25

- IOE\_GPINTEN\_ADDR, 25
- IOE\_GPIO\_ADDR, 26
- IOE\_GPPU\_ADDR, 26
- IOE\_I2C\_ADDR, 26
- IOE\_INTCAP\_ADDR, 26
- IOE\_INTCON\_ADDR, 26
- IOE\_INTF\_ADDR, 26
- IOE\_IOCON\_ADDR, 26
- IOE\_IODIR\_ADDR, 26
- IOE\_IPOL\_ADDR, 26
- IOE\_OLAT\_ADDR, 26
- fcDisable
  - FanEn.h, 26
- fcEnable
  - FanEn.h, 27
- fcInit
  - FanEn.h, 27
- I2C\_ARDY\_ISRC
  - I2c.h, 29
- I2C\_BUS\_BUSY
  - Settings.h, 43
- I2C\_CLR\_AL\_BIT
  - I2c.h, 29
- I2C\_CLR\_ARDY\_BIT
  - I2c.h, 29
- I2C\_CLR\_NACK\_BIT
  - I2c.h, 29
- I2C\_CLR\_RRDY\_BIT
  - I2c.h, 29
- I2C\_CLR\_SCD\_BIT
  - I2c.h, 29
- I2C\_INVALID\_ISRC
  - Settings.h, 43
- I2C\_MAX\_BUFFER\_SIZE
  - I2c.h, 29
- I2C\_MAX\_PTR\_SIZE
  - I2c.h, 30
- I2C\_MSGSTAT\_INACTIVE
  - I2c.h, 30
- I2C\_MSGSTAT\_RESTART
  - I2c.h, 30
- I2C\_READ\_WRONG\_MSG
  - Settings.h, 43
- I2C\_SCD\_ISRC
  - I2c.h, 30
- I2C\_STP\_NOT\_READY
  - Settings.h, 43
- I2C\_WRITE\_WRONG\_MSG
  - Settings.h, 43
- I2c.h, 27
  - I2C\_ARDY\_ISRC, 29
  - I2C\_CLR\_AL\_BIT, 29
  - I2C\_CLR\_ARDY\_BIT, 29
  - I2C\_CLR\_NACK\_BIT, 29
  - I2C\_CLR\_RRDY\_BIT, 29
  - I2C\_CLR\_SCD\_BIT, 29
  - I2C\_MAX\_BUFFER\_SIZE, 29
  - I2C\_MAX\_PTR\_SIZE, 30
  - I2C\_MSGSTAT\_INACTIVE, 30
  - I2C\_MSGSTAT\_RESTART, 30
  - I2C\_SCD\_ISRC, 30
  - i2cInit, 30
  - i2cPopMsg, 30
  - i2cRead, 32
  - i2cWrite, 32
- i2cInit
  - I2c.h, 30
- i2cMsg, 7
  - msgBuffer, 8
  - msgStatus, 8
  - numOfBytes, 8
  - numSlavePtrBytes, 8
  - slaveAddress, 8
  - slavePtrAddrHigh, 8
  - slavePtrAddrLow, 8
- i2cPopMsg
  - I2c.h, 30
- i2cRead
  - I2c.h, 32
- i2cWrite
  - I2c.h, 32
- iCtrl
  - MacroNets.h, 35
- iFdbkNet
  - channelParameters, 6
- iMaxRms
  - channelParameters, 6
- iMinRms
  - channelParameters, 6
- INCR\_BUILD
  - Settings.h, 43
- IOE\_DEFVAL\_ADDR
  - BstEn.h, 16
  - FanEn.h, 25
- IOE\_GPINTEN\_ADDR
  - BstEn.h, 16
  - FanEn.h, 25
- IOE\_GPIO\_ADDR
  - BstEn.h, 16
  - FanEn.h, 26
- IOE\_GPPU\_ADDR
  - BstEn.h, 16
  - FanEn.h, 26
- IOE\_I2C\_ADDR
  - BstEn.h, 16
  - FanEn.h, 26
- IOE\_INTCAP\_ADDR
  - BstEn.h, 16
  - FanEn.h, 26
- IOE\_INTCON\_ADDR
  - BstEn.h, 16
  - FanEn.h, 26
- IOE\_INTF\_ADDR
  - BstEn.h, 17
  - FanEn.h, 26
- IOE\_IOCON\_ADDR

- BstEn.h, 17
- FanEn.h, 26
- IOE\_IODIR\_ADDR
  - BstEn.h, 17
  - FanEn.h, 26
- IOE\_IPOL\_ADDR
  - BstEn.h, 17
  - FanEn.h, 26
- IOE\_NUM\_CHNL
  - BstEn.h, 17
- IOE\_OLAT\_ADDR
  - BstEn.h, 17
  - FanEn.h, 26
- iOrVCtl
  - MacroNets.h, 35
- iScale
  - channelParameters, 6
- LOAD\_0
  - MacroNets.h, 34
- LOAD\_1
  - MacroNets.h, 34
- LOAD\_2
  - MacroNets.h, 34
- LOAD\_3
  - MacroNets.h, 34
- MacroNets.h
  - ac, 35
  - dc, 35
  - iCtrl, 35
  - vCtrl, 35
- MacroNets.h, 32
  - AC\_I\_CNTL, 34
  - AC\_STAGE, 34
  - acOrDc, 35
  - channel, 35
  - ctlType, 34
  - DC\_STAGE, 34
  - enableAll, 35
  - iOrVCtl, 35
  - LOAD\_0, 34
  - LOAD\_1, 34
  - LOAD\_2, 34
  - LOAD\_3, 34
  - mnConnectNets, 35
  - mnRunAll, 35
  - mnSetupChannels, 35
  - mnStopAll, 35
  - opType, 34
  - stopAll, 35
  - V\_MID\_CH, 34
- mnConnectNets
  - MacroNets.h, 35
- mnRunAll
  - MacroNets.h, 35
- mnSetupChannels
  - MacroNets.h, 35
- mnStopAll
  - MacroNets.h, 35
- msgBuffer
  - i2cMsg, 8
- msgStatus
  - i2cMsg, 8
- NUM\_CHNLS
  - Settings.h, 43
- NUM\_ICTRL\_CHNLS
  - Settings.h, 43
- NUM\_VCTRL\_CHNLS
  - Settings.h, 44
- numOfBytes
  - i2cMsg, 8
- numSlavePtrBytes
  - i2cMsg, 8
- OCP\_TRIP
  - Settings.h, 44
- OTP\_TRIP
  - Settings.h, 44
- OVP\_TRIP
  - Settings.h, 44
- ocp
  - channelParameters, 6
- opMode
  - channelParameters, 6
- opType
  - MacroNets.h, 34
- otp
  - channelParameters, 6
- outNet
  - channelParameters, 6
- ovp
  - channelParameters, 6
- PERIOD
  - Pwm.h, 37
- PHASE\_CTRL\_In
  - PhaseCtrl.h, 36
- PWMDRV\_2ch\_UpCnt\_Duty1A
  - Pwm.h, 39
- PWMDRV\_2ch\_UpCnt\_Duty1B
  - Pwm.h, 39
- PWMDRV\_2ch\_UpCnt\_Duty2A
  - Pwm.h, 39
- PWMDRV\_2ch\_UpCnt\_Duty2B
  - Pwm.h, 39
- PWMDRV\_2ch\_UpCnt\_Duty3A
  - Pwm.h, 39
- PWMDRV\_2ch\_UpCnt\_Duty3B
  - Pwm.h, 39
- pcUpdate
  - PhaseCtrl.h, 36
- PhaseCtrl.h, 36
  - PHASE\_CTRL\_In, 36
  - pcUpdate, 36
- Pwm.h, 37
  - DPL\_ISR, 38

- PERIOD, [37](#)
- PWMDRV\_2ch\_UpCnt\_Duty1A, [39](#)
- PWMDRV\_2ch\_UpCnt\_Duty1B, [39](#)
- PWMDRV\_2ch\_UpCnt\_Duty2A, [39](#)
- PWMDRV\_2ch\_UpCnt\_Duty2B, [39](#)
- PWMDRV\_2ch\_UpCnt\_Duty3A, [39](#)
- PWMDRV\_2ch\_UpCnt\_Duty3B, [39](#)
- pwmDPLTrigInit, [38](#)
- pwmGetFreq, [38](#)
- pwmMacroConfigure, [38](#)
- pwmRstTz, [38](#)
- pwmSetFreq, [38](#)
- pwmSocConfigure, [38](#)
- pwmTzConfigure, [38](#)
- pwmDPLTrigInit
  - Pwm.h, [38](#)
- pwmGetFreq
  - Pwm.h, [38](#)
- pwmMacroConfigure
  - Pwm.h, [38](#)
- pwmRstTz
  - Pwm.h, [38](#)
- pwmSetFreq
  - Pwm.h, [38](#)
- pwmSocConfigure
  - Pwm.h, [38](#)
- pwmTzConfigure
  - Pwm.h, [38](#)
- RECP\_SQRT\_2
  - Settings.h, [44](#)
- refNet
  - channelParameters, [6](#)
- SATMAX\_MAX
  - Cntl.h, [20](#)
- SCIBAUD\_MIN
  - Sci.h, [40](#)
- SCIFFRX\_INT\_LVL
  - Sci.h, [40](#)
- SCIFFTX\_FILL\_LVL
  - Sci.h, [40](#)
- SCIFFTX\_INT\_LVL
  - Sci.h, [40](#)
- SCPI\_specificCmds.h, [41](#)
- SGENTI\_1ch\_Sign
  - SineGen.h, [51](#)
- SGENTI\_1ch\_VOut
  - SineGen.h, [51](#)
- SIN\_CHANNEL
  - SineGen.h, [46](#)
- SIN\_DFLT\_F
  - SineGen.h, [46](#)
- SIN\_DFLT\_F\_MAX
  - SineGen.h, [46](#)
- SIN\_DFLT\_GAIN
  - SineGen.h, [46](#)
- SIN\_DFLT\_OFST
  - SineGen.h, [46](#)
- SIN\_DFLT\_PHSE
  - SineGen.h, [46](#)
- SIN\_DFLT\_RCTFY
  - SineGen.h, [46](#)
- SIN\_F\_SPL
  - SineGen.h, [47](#)
- SQRT\_2
  - Settings.h, [44](#)
- scGetState
  - SlewControl.h, [52](#)
- scGetStep
  - SlewControl.h, [52](#)
- scGetTarget
  - SlewControl.h, [52](#)
- scSetState
  - SlewControl.h, [52](#)
- scSetStateAll
  - SlewControl.h, [53](#)
- scSetStep
  - SlewControl.h, [53](#)
- scSetStepAll
  - SlewControl.h, [53](#)
- scSetTarget
  - SlewControl.h, [53](#)
- scSetTargetAll
  - SlewControl.h, [54](#)
- scSlewUpdate
  - SlewControl.h, [54](#)
- Sci.h, [39](#)
  - SCIBAUD\_MIN, [40](#)
  - SCIFFRX\_INT\_LVL, [40](#)
  - SCIFFTX\_FILL\_LVL, [40](#)
  - SCIFFTX\_INT\_LVL, [40](#)
  - sciInit, [40](#)
  - sciTx, [41](#)
- sciInit
  - Sci.h, [40](#)
- sciTx
  - Sci.h, [41](#)
- Settings.h, [42](#)
  - CHANNEL\_OOB, [43](#)
  - DEBUG, [43](#)
  - DUAL\_CNTL\_AC, [43](#)
  - I2C\_BUS\_BUSY, [43](#)
  - I2C\_INVALID\_ISRC, [43](#)
  - I2C\_READ\_WRONG\_MSG, [43](#)
  - I2C\_STP\_NOT\_READY, [43](#)
  - I2C\_WRITE\_WRONG\_MSG, [43](#)
  - INCR\_BUILD, [43](#)
  - NUM\_CHNLS, [43](#)
  - NUM\_ICTRL\_CHNLS, [43](#)
  - NUM\_VCTRL\_CHNLS, [44](#)
  - OCP\_TRIP, [44](#)
  - OTP\_TRIP, [44](#)
  - OVP\_TRIP, [44](#)
  - RECP\_SQRT\_2, [44](#)
  - SQRT\_2, [44](#)
  - uSec100, [44](#)

- VAC\_R1, [44](#)
- VAC\_R2, [44](#)
- VALUE\_OOB, [44](#)
- VDDA, [44](#)
- VMID\_R1, [44](#)
- VMID\_R2, [45](#)
- VSSA, [45](#)
- sgGainUpdate
  - SineGen.h, [47](#)
- sgGetFreq
  - SineGen.h, [47](#)
- sgGetGainTarget
  - SineGen.h, [47](#)
- sgGetOffset
  - SineGen.h, [47](#)
- sgGetRectify
  - SineGen.h, [48](#)
- sgGetResolution
  - SineGen.h, [48](#)
- sgGetState
  - SineGen.h, [48](#)
- sgInit
  - SineGen.h, [48](#)
- sgSetFreq
  - SineGen.h, [48](#)
- sgSetGainTarget
  - SineGen.h, [49](#)
- sgSetInitialPhase
  - SineGen.h, [49](#)
- sgSetOffset
  - SineGen.h, [49](#)
- sgSetRectify
  - SineGen.h, [49](#)
- sgSetState
  - SineGen.h, [49](#)
- sgUpdate
  - SineGen.h, [51](#)
- SineGen.h, [45](#)
  - SGENTI\_1ch\_Sign, [51](#)
  - SGENTI\_1ch\_VOut, [51](#)
  - SIN\_CHANNEL, [46](#)
  - SIN\_DFLT\_F, [46](#)
  - SIN\_DFLT\_F\_MAX, [46](#)
  - SIN\_DFLT\_GAIN, [46](#)
  - SIN\_DFLT\_OFST, [46](#)
  - SIN\_DFLT\_PHSE, [46](#)
  - SIN\_DFLT\_RCTFY, [46](#)
  - SIN\_F\_SPL, [47](#)
  - sgGainUpdate, [47](#)
  - sgGetFreq, [47](#)
  - sgGetGainTarget, [47](#)
  - sgGetOffset, [47](#)
  - sgGetRectify, [48](#)
  - sgGetResolution, [48](#)
  - sgGetState, [48](#)
  - sgInit, [48](#)
  - sgSetFreq, [48](#)
  - sgSetGainTarget, [49](#)
  - sgSetInitialPhase, [49](#)
  - sgSetOffset, [49](#)
  - sgSetRectify, [49](#)
  - sgSetState, [49](#)
  - sgUpdate, [51](#)
- slaveAddress
  - i2cMsg, [8](#)
- slavePtrAddrHigh
  - i2cMsg, [8](#)
- slavePtrAddrLow
  - i2cMsg, [8](#)
- SlewControl.h, [51](#)
  - scGetState, [52](#)
  - scGetStep, [52](#)
  - scGetTarget, [52](#)
  - scSetState, [52](#)
  - scSetStateAll, [53](#)
  - scSetStep, [53](#)
  - scSetStepAll, [53](#)
  - scSetTarget, [53](#)
  - scSetTargetAll, [54](#)
  - scSlewUpdate, [54](#)
- slewRate
  - channelParameters, [6](#)
- smlInit
  - StateMachine.h, [55](#)
- StateMachine.h, [54](#)
  - Alpha\_State\_Ptr, [55](#)
  - smlInit, [55](#)
- stopAll
  - MacroNets.h, [35](#)
- TMP\_E\_T\_COLD
  - Tmp.h, [57](#)
- TMP\_SCL\_OFST
  - Tmp.h, [57](#)
- TMP\_V0C\_OFST
  - Tmp.h, [57](#)
- target
  - channelParameters, [7](#)
- Timers.h, [55](#)
  - timersSetupReal, [55](#)
- timersSetupReal
  - Timers.h, [55](#)
- Tmp.h, [56](#)
  - ADC\_I2C\_ADDR, [57](#)
  - ADC\_NUM\_CHNL, [57](#)
  - ADC\_STPS, [57](#)
  - ADC\_VREF, [57](#)
  - TMP\_E\_T\_COLD, [57](#)
  - TMP\_SCL\_OFST, [57](#)
  - TMP\_V0C\_OFST, [57](#)
  - tmpCheckOtp, [57](#)
  - tmpGetOtp, [57](#)
  - tmpInit, [59](#)
  - tmpRead, [59](#)
  - tmpSetOtp, [59](#)
- tmpCheckOtp
  - Tmp.h, [57](#)

tmpGetOtp  
    Tmp.h, [57](#)  
tmpInit  
    Tmp.h, [59](#)  
tmpRead  
    Tmp.h, [59](#)  
tmpSetOtp  
    Tmp.h, [59](#)  
  
uSec100  
    Settings.h, [44](#)  
  
vCtrl  
    MacroNets.h, [35](#)  
V\_MID\_CH  
    MacroNets.h, [34](#)  
VAC\_R1  
    Settings.h, [44](#)  
VAC\_R2  
    Settings.h, [44](#)  
VALUE\_OOB  
    Settings.h, [44](#)  
VDDA  
    Settings.h, [44](#)  
vFdbkNet  
    channelParameters, [7](#)  
vGainLmt  
    channelParameters, [7](#)  
VMID\_R1  
    Settings.h, [44](#)  
VMID\_R2  
    Settings.h, [45](#)  
vMaxRms  
    channelParameters, [7](#)  
vMinRms  
    channelParameters, [7](#)  
VSSA  
    Settings.h, [45](#)  
vScale  
    channelParameters, [7](#)