

bit

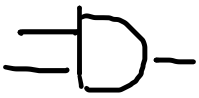
B 1100 1110



SHIFT

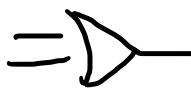
$\underbrace{\ll 1}_{\text{left shift}} \quad \underbrace{\gg 1}_{\text{right shift}}$

10011100

AND \Rightarrow 


A	B	O
0	0	0
0	1	0
1	0	0
1	1	1

&

OR \Rightarrow 

A	B	O
0	0	0
0	1	1
1	0	1
1	1	1

|

XOR \Rightarrow 

A	B	O
0	0	0
0	1	1
1	0	1
1	1	0

^

NOT \sim

I	O
0	1
1	0