cad16.ee.ntu.edu.tw (r392102)

Terminal Sessions View X server Tools Games Settings Macros Help

Session Servers Tools Games Sessions View Split MultiExec Tunneling Packages Settings Help X server Exit

Quick connect...

2. cad16.ee.ntu.edu.tw (r392102)

/home/raid7_2/user13/r3921072

Name

- nWaveLog
- csrc
- build
- ucli.key
- testbench.v
- simv
- novas_dump.log
- novas.rc
- novas.conf
- license.cshrc
- filenames.log
- default.svf
- command.log
- ALU.v
- alu.fsdb

Remote monitoring

Follow terminal folder

```
=============================================
|          Line          |  full/ parallel  |
=============================================
|           68           |    auto/auto     |
|          100           |    auto/auto     |
=============================================

Inferred memory devices in process
        in routine ALU line 56 in file
                '/home/raid7_2/user13/r3921072/hw2/ALU.v'.
===============================================================================
|    Register Name    |    Type    | Width | Bus | MB | AR | AS | SR | SS | ST |
===============================================================================
|    out_data_reg     | Flip-flop |  64   |  Y  | N  | Y  | N  | N  | N  | N  |
|      count_reg      | Flip-flop |   6   |  Y  | N  | Y  | N  | N  | N  | N  |
|     A_temp_reg      | Flip-flop |  32   |  Y  | N  | N  | N  | N  | N  | N  |
|     B_temp_reg      | Flip-flop |  32   |  Y  | N  | N  | N  | N  | N  | N  |
|    mode_temp_reg    | Flip-flop |   4   |  Y  | N  | N  | N  | N  | N  | N  |
|      ready_reg      | Flip-flop |   1   |  N  | N  | Y  | N  | N  | N  | N  |
|    prod_rem_reg     | Flip-flop |  65   |  Y  | N  | Y  | N  | N  | N  | N  |
|    temp_mul_reg     | Flip-flop |  32   |  Y  | N  | N  | N  | N  | N  | N  |
===============================================================================
Presto compilation completed successfully.
Current design is now '/home/raid7_2/user13/r3921072/hw2/ALU.db:ALU'
Loaded 1 design.
```