

COA Term-1, 25 Apr. 2021

Total points 26/30 ?

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Class

CSE-2

Class S.No.

52

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✓ 1.The length of Register is based on

1/1

- ☒ word size
- ☐ word limit
- ☐ register size



✓ 2. A group of bits that tell the computer to perform a specific operation is known as 1/1

- ☐ Register
- ☐ Micro-operation
- ☐ Accumulator
- ☒ instruction code



✗ 3. The internal hardware organization of a digital computer is defined by 0/1
(More than one option can be selected)

- ☐ The Set of registers and Function
- ☐ The sequence of Microoperation
- ☐ Conrol function
- ☐ Instruction Code
- ☒ All above



✓ 4. Choose the correct statement

1/1

- ☐ The operation executed on data stored in registers is called- Macrooperation
- ☒ A set of Microoperation is called Macrooperation
- ☐ Elementry operation performed on information stored in registers are Macrooperations



✓ 5. MRI indicates

1/1

- ☐ Memory Reference Information
- ☒ Memory Reference Instruction
- ☐ Memory Registers Instruction
- ☐ Memory Register information



✗ 6. Which operations are used for addition, subtraction, increment, decrement and complement function:

0/1

- ☐ Bus
- ☐ Memory transfer
- ☐ Arithmetic operation
- ☒ All of these



✓ 7. In which transfer the computer register are indicated in capital letters 1/1
for depicting its function:

- ☐ Memory transfer
- ☒ Register transfer
- ☐ Bus transfer
- ☐ None of these



✓ 8. Which are the operation that a computer performs on data that stored 1/1
in register:

- ☐ Register transfer
- ☐ Arithmetic
- ☐ Logical
- ☒ All of these
- ☐ None of these



✓ 9. In 3 state gate two states act as signals equal to: 1/1

- ☐ 1.Logic 0
- ☐ 2. Logic 1
- ☐ 3. None of these
- ☐ Both 2 & 3
- ☒ Both 1 & 2



✓ 10. Result of selective complement operation of 1010 and 1100 is

2/2

- ☐ 0101
- ☐ 1010
- ☒ 0110
- ☐ 1100



✓ 11. RST 7.5, INTR, TRAP are the

1/1

- ☐ Flags in 8085
- ☒ interrupts in 8085
- ☐ control in 8085



✓ 12. Determine the value of outputs s0, s1, s2, s3 and C4 in adder subtractor Circuit for inputs as if M=1, A= 0101 and B= 1010

2/2

- ☐ 1,1,0,1,1
- ☒ 1,0,1,1,0
- ☐ 0,1,0,1,1



✓ 13. A digital computer has a common bus system for 16 registers of 8 bit each , choose the correct statement 2/2

- ☐ 16 Mux of 8 X1, 4 Select lines
- ☐ 16 Mux of 8 X1, 3 Select lines
- ☒ 8 Mux of 16 X1 , 4 select lines



✓ 14. General purpose Register of 8085

1/1

- ☒ are of 8 bit
- ☐ are of 16 bit
- ☐ are of 12 bit



✓ 15. Total no. of addressing Modes in 8085 is

1/1

- ☐ 4
- ☒ 5
- ☐ 6
- ☐ 3



✓ 16. A computer has memory unit of 256K of 32 bit each, what is the length of IR, AR, DR , PC and AC registers 2/2

- ☐ 32, 8, 32, 8, 32
- ☒ 32, 18, 32, 18, 32
- ☐ 5, 8, 5, 8, 5
- ☐ Option 4



✗ 17. effective address is .../1

- ☐ 1. the address of the operand in a computation type instruction
- ☐ 2. target address in a branch type instruction
- ☒ 3. the address of instruction
- ☐ 1, 2 only
- ☐ 2, 3 only



✓ 18. state true or false- 5 Registers have three control inputs LD, INR, and CIR while Two have only load input in common Bus system 1/1

- ☒ TRUE
- ☐ False



✓ 19. How many types of instruction codes

1/1

☐ 2

☐ 4

☒ 3

☐ 5



✓ 20. In the fetch cycle, instructions are loaded into:

1/1

☐ the flash register.

☐ cache.

☐ ROM.

☒ the Instruction Register.



✓ 21. programs stored when they are NOT in the process of running?

1/1

☐ bus

☐ CPU

☐ RAM

☒ hard drive



✓ 22. Instruction cycle phases are

1/1

- ☐ fetch, Decode, Execute
- ☐ fetch, decode, Branching , Execute
- ☐ fetch, decode , Read effective Address
- ☒ fetch, decode , Read effective Address, Execute



✗ 23. 5XXX is the opcode for

0/1

- ☐ Register reference instruction
- ☐ Memory reference Instruction
- ☐ Direct reference instruction
- ☒ Input-output instruction



✓ 24. What is programmed control transfer?

1/1

- ☐ The process of transferring the control
- ☒ The process of transferring the information
- ☐ The process of transferring the interrupt control



✓ 25. state True or False- I/O instruction are for transferring the Information1/1
to and from DR register, and for controlling the inputs

☐ True

☒ False



☐ Option 3

✓ 26. IEN stands for

1/1

Interrupt Enable



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