

Compiler Construction

Chapter 14: Runtime Optimization

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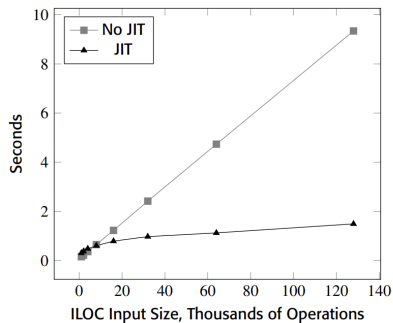
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Classic compilers

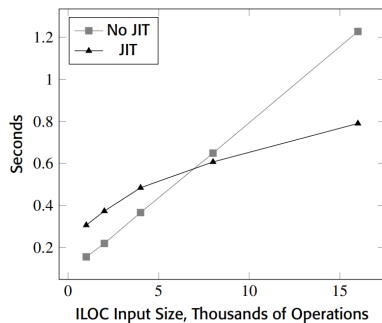
- Ahead-of-time compilation (AOT)
- Link time, loaded time knowledge
- No runtime knowledge
 - ▶ E.g. heavy used loops

Just-in-time compiler (JIT)

- Interpreter-like
- Compilation just before the execution
- Heavily used in scripting languages e.g JavaScript



(a) Full Data Set

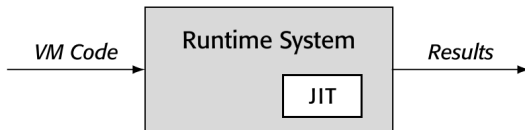


(b) Expanded View of Small Data Sets

■ **FIGURE 14.1** Scalability of a JAVA Application.

● Overhead cost in JIT

- VM-code execution



- ▶ More compact, and abstract than native codes
 - ▶ Requires emulation/translation
- Native-code execution
 - ▶ Faster than VM-code

Only compile (or optimize) **hot** codes

- Otherwise, it would be the same as AOT
- Requires a tracking/measurement of hot code

Finding hot code data profile

- Add codes to count the number of invocations
- Interrupts or special hardware to count the number of invocations

VM-code execution

- Mixed-mode environment: VM emulated + optimized translated native hot codes

Native-code execution

- Jump to optimized version when the code portion is hot

Hot traces

- A sequence of basic building blocks that are frequently invoked
- A trace can cross procedure-call boundaries
- Local or regional optimization e.g. code generations, allocation, scheduling

Hot methods

- A method that is frequently invoked
- Non-local optimization e.g. code motion, regional instruction scheduling, dead-code elimination, global redundancy elimination, strength reduction, inline substitution

Finding redundancy, constant folding, simplifying identities

- Trace optimizer: Local-Value-Numbering
- Method optimizer: global redundancy algorithm

(Register) Allocation

- Trace optimizer: local register allocator
- Method optimizer: global register allocator

Runtime fact

- Profile information
- Object type
- Data structure sizes
- Loop bounds
- Constant values

Possible improvement

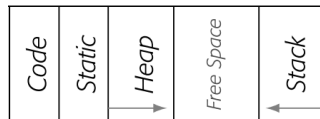
- Eliminating VM overhead: VM emulation → native codes
- Improve code layout: E.g. removing some branch/jumps
- Eliminate redundancy: Value numbering
- Reduce call overhead: inline substitution
- Tailor code to the system: machine-dependent optimization
- Capitalize on runtime information: E.g. utilizing frequently used data types

Hot-trace optimization



Processor

Normal Execution



Hot-Trace Optimizer

Processor

Execution Mediated by a
Runtime Optimizer

Candidate trace-entry blocks

- Loop header blocks
- Loop exit blocks

Counting the number of times the block is executed

- Set a threshold

L1: emulate code until a taken branch or jump

lookup target address in the entry table

if address is not in the table then

if address < emulated PC then

create table entry for address

set target address' counter to 1

else // address is in the entry table

if fragment has been compiled then

jump to the compiled fragment

bump the target address' counter

if counter > hot threshold then

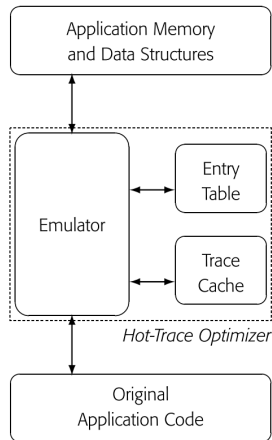
build, compile, & execute the trace

// if here, block runs in emulation mode

set emulator's PC to target address

jump back to L1

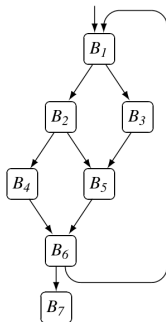
(a) High-level Algorithm



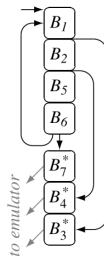
(b) Components of the Trace Optimizer

■ **FIGURE 14.2** Conceptual Structure of a Hot-Trace Optimizer.

Entry table and trace cache

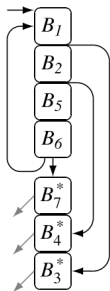


(a) Example CFG

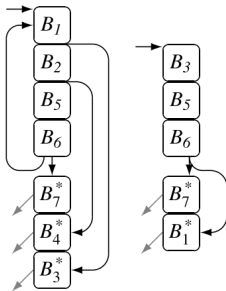


(b) Trace Buffer for $\langle B_1, B_2, B_5, B_6 \rangle$

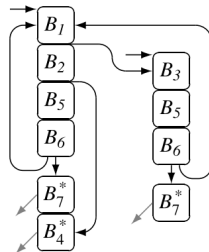
■ **FIGURE 14.3** Building a Trace.



(a) Original Trace



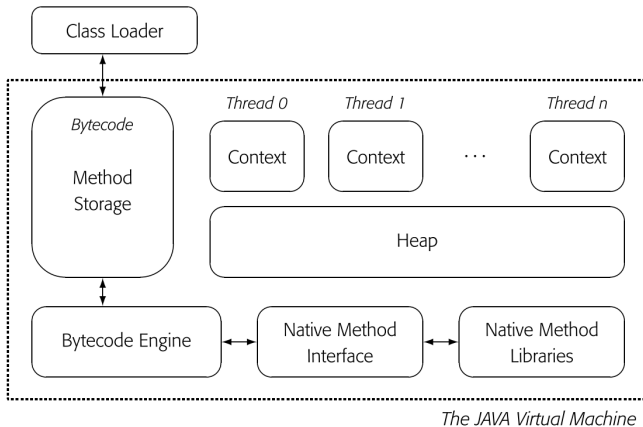
(b) Trace for $\langle B_3, B_5, B_6 \rangle$



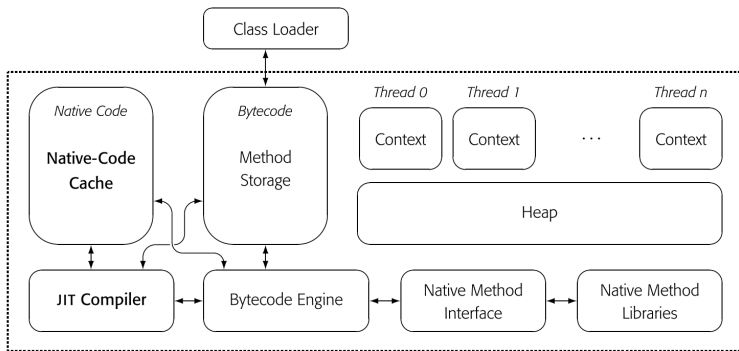
(c) After Trace Linking

■ **FIGURE 14.4** Adding a Second Trace.

Hot-method in mixed-mode environment



■ **FIGURE 14.5** The JAVA Runtime Environment.



The JAVA Virtual Machine, with a JIT

■ **FIGURE 14.6** Adding a JIT to the JAVA Runtime Environment.

Instrumented VM code

- Insert VM code for counters
- Count: method invocations, loop iterations

Instrumented engine

- The emulator contains the counters

Similar to AOT compilation

- Parsing, instruction selection and scheduling, register allocation

Mixed-mode environment

- Native codes run faster than VM code → benefit from heavy optimization
- Scalar optimization such as value numbering, constant propagation, dead-code elimination, code motion

Guessing the data type

```
//  $x \leftarrow y + z$   
if  $actual\_type(y) = 32\_bit\_integer$  and  
    $actual\_type(z) = 32\_bit\_integer$  then  
     $x \leftarrow 32\_bit\_integer\_add(y, z)$   
else  
     $x \leftarrow generic\_add(y, actual\_type(y), z, actual\_type(z));$ 
```

■ **FIGURE 14.7** Code with a Fast Path for the Expected Case.

Compile-on-call

- 1 Locates the VM code for the method
- 2 Invokes the JIT to produce native code for the method
- 3 Re-links the call site to point to the newly compiled native code

Optimization level

- Low: low compilation cost, simple optimization
- High: trade-off between the compilation time and the potential benefit of faster execution

Instrumented code

- Insert native codes for counters
- Count: method invocations, loop iterations

Interrupted-driven profiles

- Time interrupt to approximate the execution time