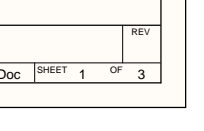
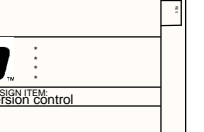
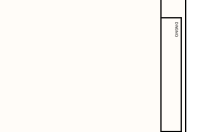
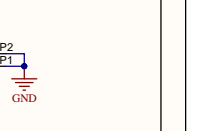
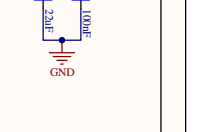
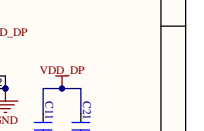
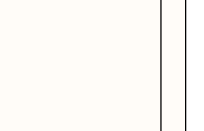
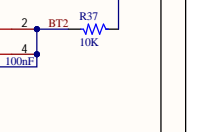
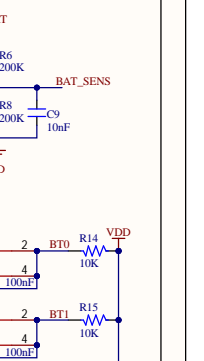
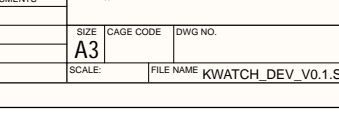
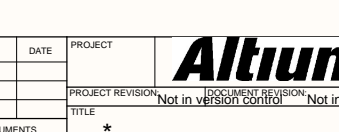
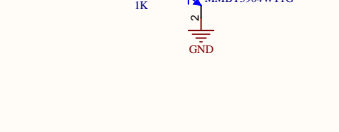
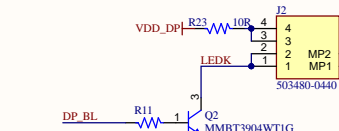
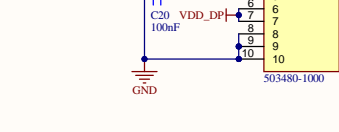
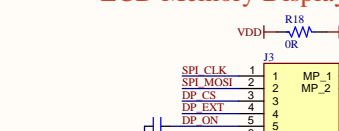
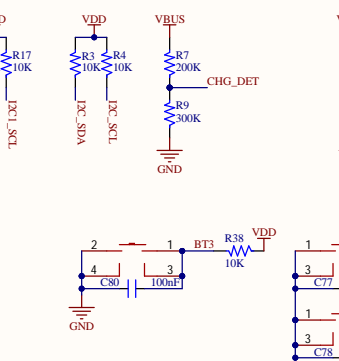
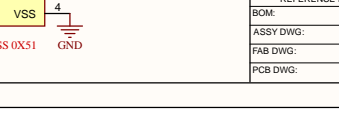
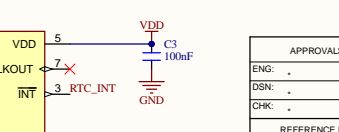
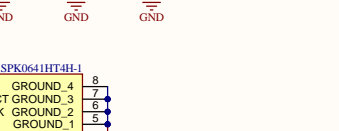
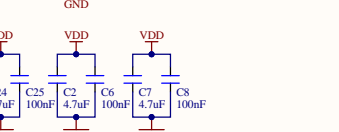
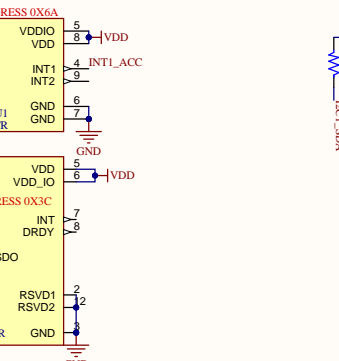
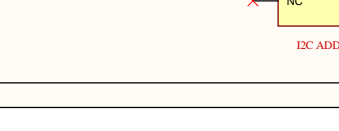
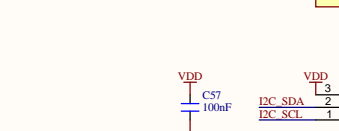
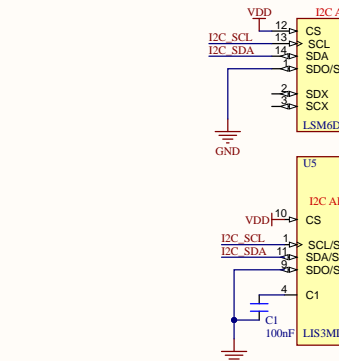
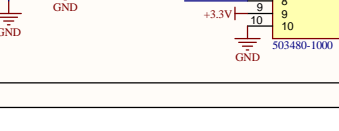
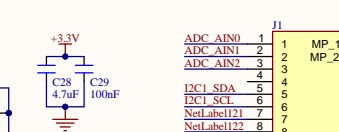
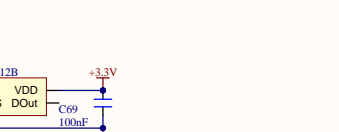
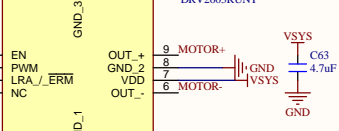
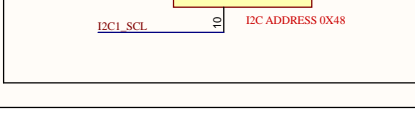
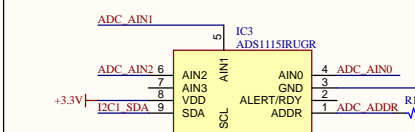
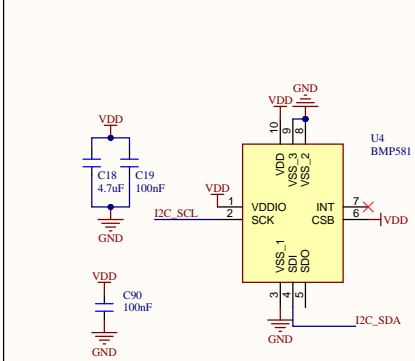
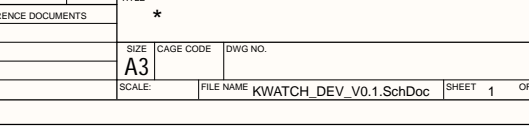
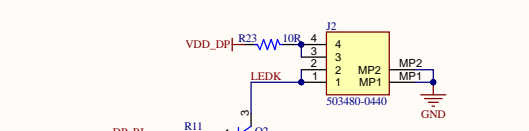
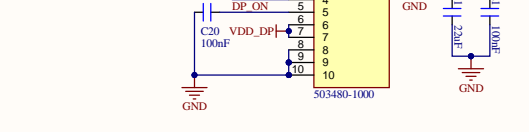
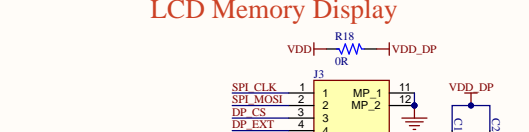
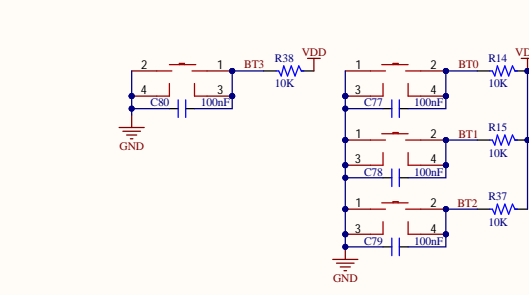
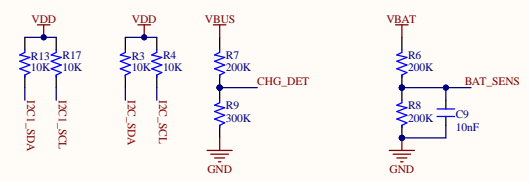
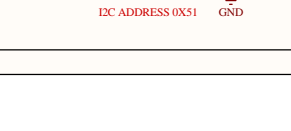
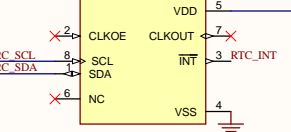
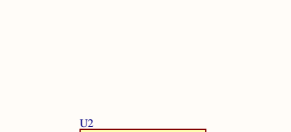
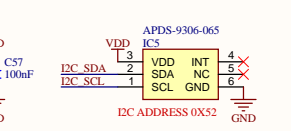
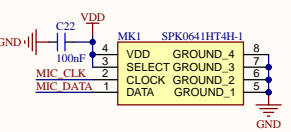
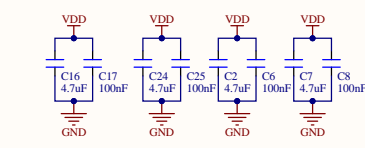
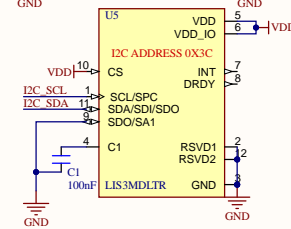
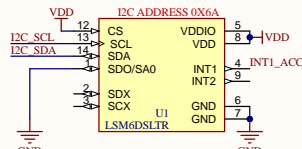
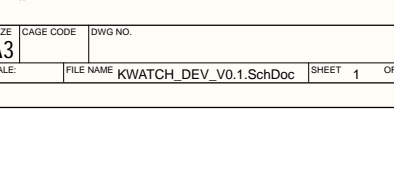
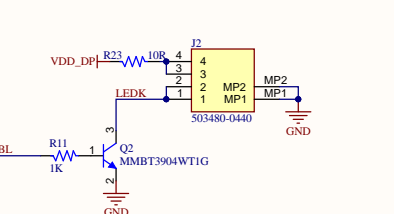
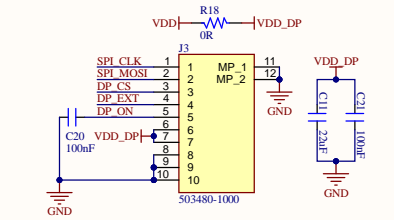


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ACC GYRO-MAGNETIC IC



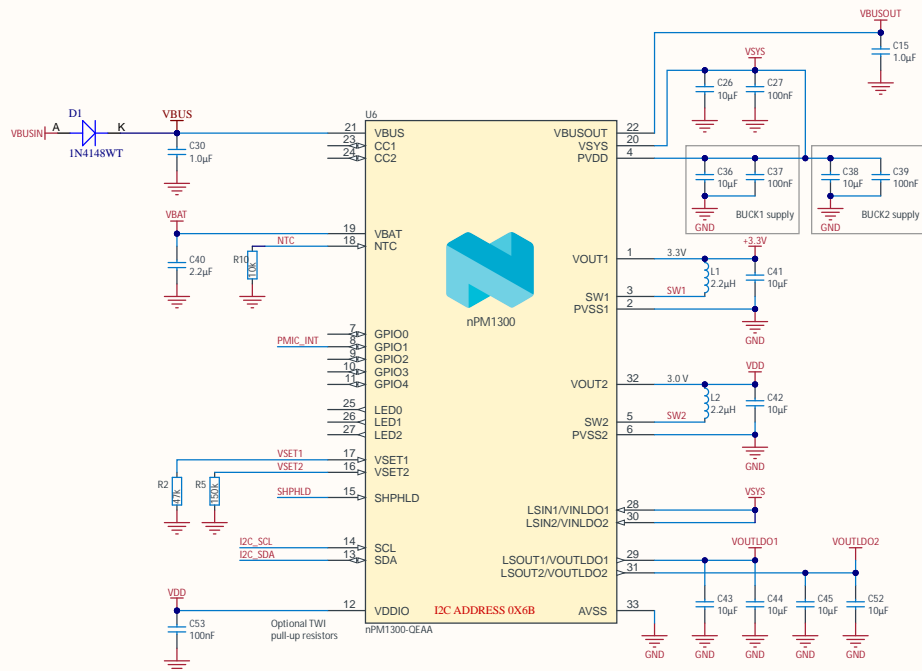
LCD Memory Display



APPROVALS	DATE	PROJECT	Altium	
ENG: .		PROJECT REVISION: .	DOCUMENT REVISION: .	DESIGN ITEM: .
DSN: .		Not in version control		
CHK: .		Not in version control		
REFERENCE DOCUMENTS				
BOM: *				
ASSY DWG:	SIZE	CAGE CODE	DWG NO.	REV
FAB DWG:	A3			
PCB DWG:	SCALE:	FILE NAME	KWATCH_DEV_V0.1.SchDoc	SHEET 1 OF 3

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Designer note:
C14, C15 and C16 are
additional high frequency
capacitors for improved
decoupling in designs
meeting stricter EMC
requirements.

APPROVALS	DATE	PROJECT	Altium	
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DSN: .		PROJECT REVISION: .	DOCUMENT REVISION: .	DESIGN ITEM: .
CHK: .		Not in version control Not in version control		
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DRAFT NO.		REV		6	
REVISION	DESCRIPTION			DATE	APPROVED

