[1. V10, 2012/02/22 2](#_Toc364695200)

[2. V11, 2012/04/30 2](#_Toc364695201)

# V10, 2013/07/19

|  |  |  |
| --- | --- | --- |
| **File**  **Listing** | **SCH** | AP-MT7621+MT7612E+MT7602E-V10-A-DUAL-CONCURRENT-DDR3-4L-20130719.DSN |
| **BOM** | AP-MT7621+MT7612E+MT7602E-V10-A-DUAL-CONCURRENT-DDR3-4L-20130719.xlsx |
| **Layout** | AP-MT7621+MT7612E+MT7612E-V10-A-DUAL-CONCURRENT-DDR3-4L-WS2255.zip |
| **Gerber** | AP-MT7621+MT7612E+MT7612E-V10-A-DUAL-CONCURRENT-DDR3-4L-WS2255GBR.zip |
| **PCB Stack** | AP-MT7621+MT7612E+MT7612E-V10-A-DUAL-CONCURRENT-DDR3-4L-疊構0510.xlsx |
| * **Initial release.** | | |

# V11, 2013/08/19

|  |  |  |
| --- | --- | --- |
| **File**  **Listing** | **SCH** | AP-MT7621+MT7612E+MT7612E-V11-A-DUAL-CONCURRENT-DDR3-4L-20130819.DSN |
| **BOM** | MT7621+MT7612E+MT7602E-V11-A-DUAL-CONCURRENT-DDR3-NO GPHY-4L-20130819.xlsx |
| **Layout** | AP-MT7621+MT7612E+MT7612E-V11-A-DUAL-CONCURRENT-DDR3-4L-20130816-WS2378.pcb |
| **Gerber** | AP-MT7621+MT7612E+MT7612E-V11-A-DUAL-CONCURRENT-DDR3-4L-20130816-WS2378GBR.zip |
| **PCB Stack** | AP-MT7621+MT7612E+MT7612E-V11-A-DUAL-CONCURRENT-DDR3-4L-疊構0510.xlsx |
| * 5G and 2.4G band RF iLNA / eLNA co-Layout * Adjust push button control GPIO | | |

# V11, 2013/10/10

|  |  |  |
| --- | --- | --- |
| **File**  **Listing** | **SCH** | AP-MT7621+MT7612E+MT7602E-V11-DBC-DDR3-4L-20131010.DSN |
| **BOM** | MT7621A+MT7612E+MT7602E\_DBC\_DDR3\_NO eGPHY\_4L\_20131010.xlsx |
| **Layout** | AP-MT7621+MT7612E+MT7612E-V11-A-DUAL-CONCURRENT-DDR3-4L-20130816-WS2378.pcb |
| **Gerber** | AP-MT7621+MT7612E+MT7612E-V11-A-DUAL-CONCURRENT-DDR3-4L-20130816-WS2378GBR.zip |
| **PCB Stack** | AP-MT7621+MT7612E+MT7612E-V11-A-DUAL-CONCURRENT-DDR3-4L-疊構0510.xlsx |
| * Fine-tune MT7612E/MT7602E/MT6605 matching circuit * Add PCB layout guide | | |