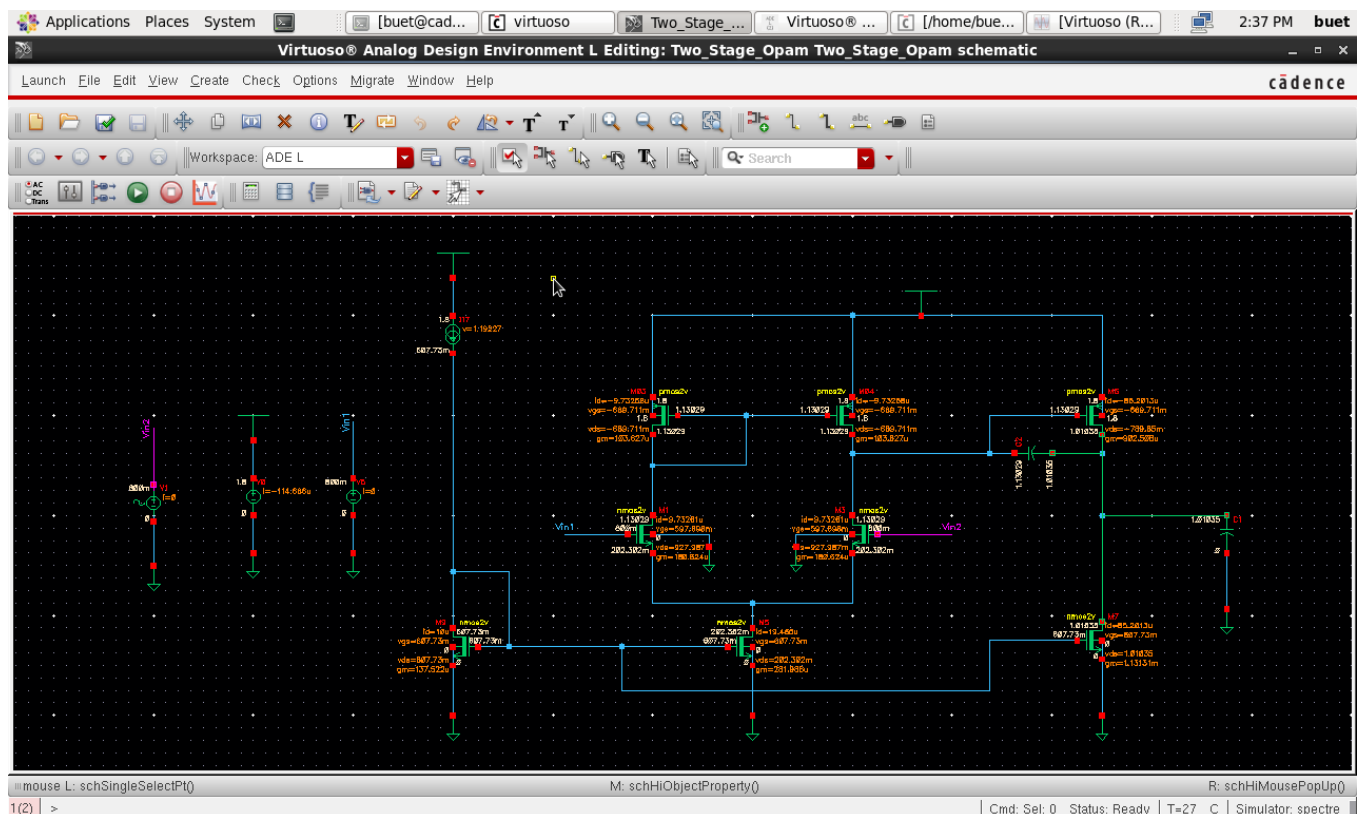


Self Project Report

Design of a two-stage op-amp having the following specifications: Gain: 60dB, Phase Margin : 60 deg., Gain Bandwidth product = 30MHz at 90nm CMOS technology.

Tool Used: Cadence Virtuoso

Circuit Design:



Simulation Results for circuit operation at ICMR = 0.8V

Plot of Gain and Phase



Simulation Results for circuit operation at ICMR = 1.6V

Plot of Gain and Phase

