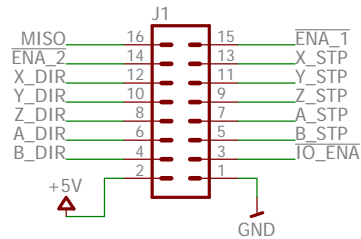
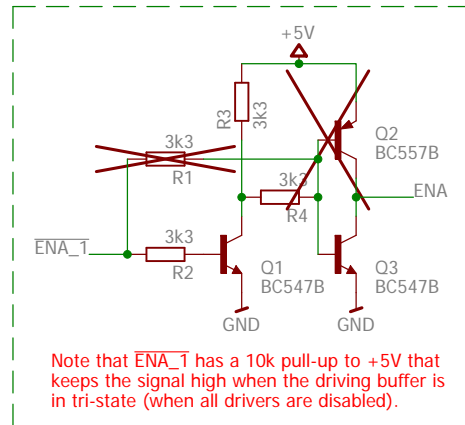


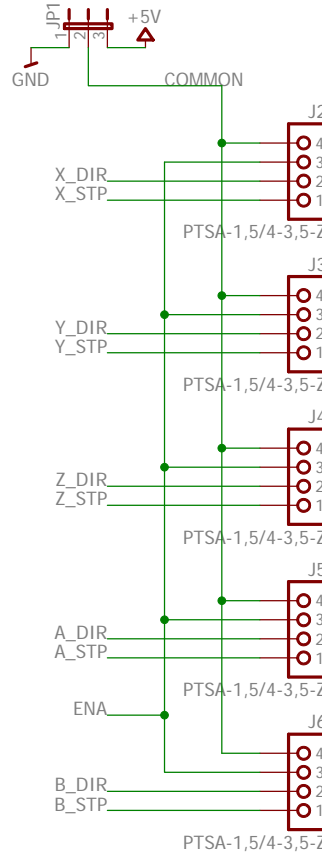
Shown Assembly Variant: SINK_ENA



for SINK disable inputs mount R1 & Q3
for SINK enable inputs mount R2, R3, R4, Q1 & Q3
for SOURCE disable inputs mount R2, R3, R4, Q1 & Q2
for SOURCE enable inputs mount R1 & Q2



SOURCE / SINK



1. STEP
2. DIR
3. ENABLE/DISABLE
4. COMMON

X_AXIS

Y_AXIS

Z_AXIS

A_AXIS

B_AXIS

DISABLE inputs:
When ENA_1 is high, the disable input should receive current (either sink or source).
When ENA_1 is low, no input current should flow.

ENABLE inputs:
When ENA_1 is high, no input current should flow.
When ENA_1 is low, the enable input should receive current (either sink or source).

SINK inputs:
A NPN transistor (Q3) sinks the input current to GND when active. Do not mount Q2.

SOURCE inputs:
A PNP transistor (Q2) sources the input current from +5 Volt when active. Do not mount Q3.

BeBoPr J5 breakout board (J5-BOB)

PROJECT: J5-BOB-R0A	REVISION: R0
DATE: 13-6-2015 21:28	SHEET: 1/1