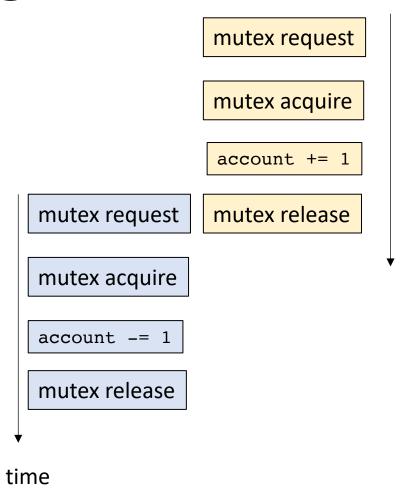
CSE113: Parallel Programming

time

Feb. 1, 2023

- Topics:
 - mutex implementations



Announcements

- HW 2 is released
 - Due Feb. 9
 - 4 free late days Feb. 13
- You have what you need to get started on Part 1
 - Hopefully for the rest of the assignment on Friday/Monday
- Use office hours and piazza if you need help!

Quiz review

Quiz review

If you run your code with the thread sanitizer and if it doesn't report any issues, then your code is guaranteed to be free from data-conflicts

Thread Sanitizer

Simply add the following to your compile line:

-fsanitize=thread

Examples:

Thread Sanitizer

- We don't have time to go into the reason why, but the thread sanitizer can even check your custom mutex implementations
 - Useful for the homework
- Thread Sanitizer is not a guarantee though:
 - Best effort dynamic analysis
 - If input or interleavings change, then your code could still have a data race
 - Tends to work pretty well in practice

Quiz review

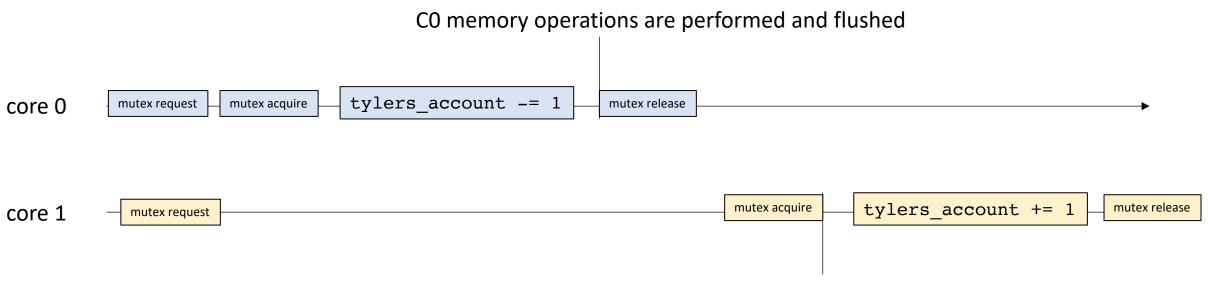
It is required to use atomic types inside of critical sections

○ True

False

Atomics

- What do those fences (compiler and memory) give us?
- Atomics were designed so that we can implement things like mutexes!



C1 memory operations have **not** yet been performed and cache is invalidated

Quiz review

Write 1 or 2 sentences about whether you agree or disagree with the following sentence and why:

"Because atomic data types can safely be accessed concurrently, we should mark all our variables as atomic just to be safe."

Quiz review

Write a few sentences about how you can reason about the correctness of a mutex implementation.

Picking up on mutexes:

Review

• Buggy mutex implementation

```
#include <atomic>
using namespace std;
class Mutex {
public:
  Mutex() {
    flag[0] = flag[1] = 0;
  void lock();
  void unlock();
private:
  atomic_bool flag[2];
```

both initialized to 0

two flags this time

```
void lock() {
  int i = thread_id;
  flag[i].store(1);
  int j = i == 0 ? 1 : 0;
  while (flag[j].load() == 1);
}
```

Thread id (0, or 1)

Mark your intention to take the lock

Wait for other thread to leave the critical section

```
void unlock() {
  int i = thread_id;
  flag[i].store(0);
}
```

Thread id (0, or 1)

Mark your flag to say you have left the critical section.

Analysis

```
void lock() {
   int i = thread_id;
   flag[i].store(1);
   int j = i == 0 ? 1 : 0;
   while (flag[j].load() == 1);
}
```

```
void unlock() {
  int i = thread_id;
  flag[i].store(0);
}
```

```
Thread 0: Thread 1:
m.lock(); m.lock();
m.unlock();
```

```
core 0 —
```

```
core 1 -
```

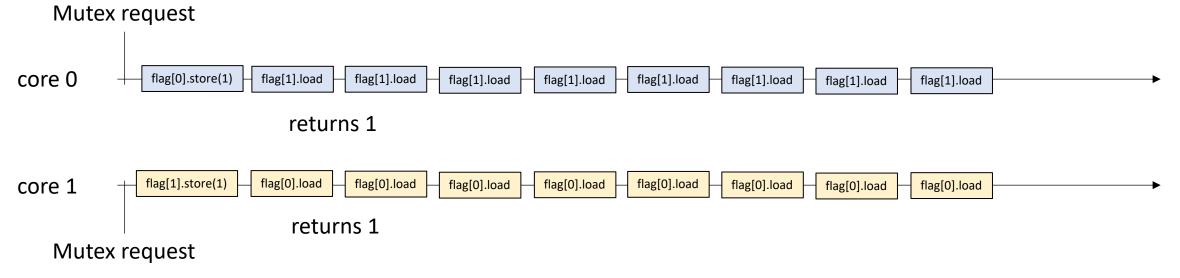
Analysis

```
void lock() {
  int i = thread_id;
  flag[i].store(1);
  int j = i == 0 ? 1 : 0;
  while (flag[j].load() == 1);
}
```

```
void unlock() {
  int i = thread_id;
  flag[i].store(0);
}
```

```
Thread 0: Thread 1:
m.lock();
m.unlock();
m.unlock();
```

Both will spin forever!



Next buggy implementation

```
class Mutex {
public:
  Mutex() {
    victim = -1;
  void lock();
  void unlock();
private:
  atomic_int victim;
```

initialized to -1

back to a single variable

```
void lock() {
  victim.store(thread_id);
  while (victim.load() == thread_id);
}
```

Volunteer to be the victim

Victims only job is to spin



No unlock!

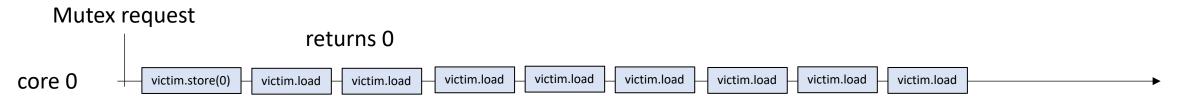
```
void lock() {
  victim.store(thread_id);
  while (victim.load() == thread_id);
}
```

```
void unlock() {}
```

```
Thread 0:
m.lock();
```

```
m.unlock();
```

spins forever if the second thread never tries to take the mutex!



```
void lock() {
  victim.store(thread_id);
  while (victim.load() == thread_id);
}
```

```
void unlock() {}
```

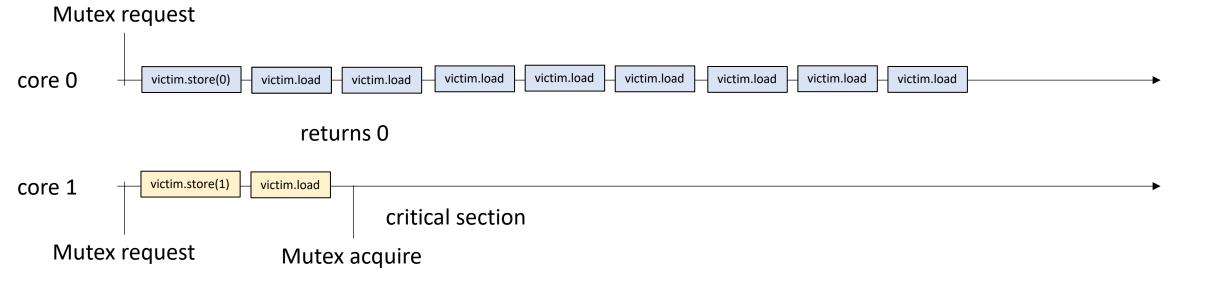
```
Thread 0: Thread 1:
m.lock();
m.unlock();
m.unlock();
```



```
void lock() {
  victim.store(thread_id);
  while (victim.load() == thread_id);
}
```

```
void unlock() {}
```

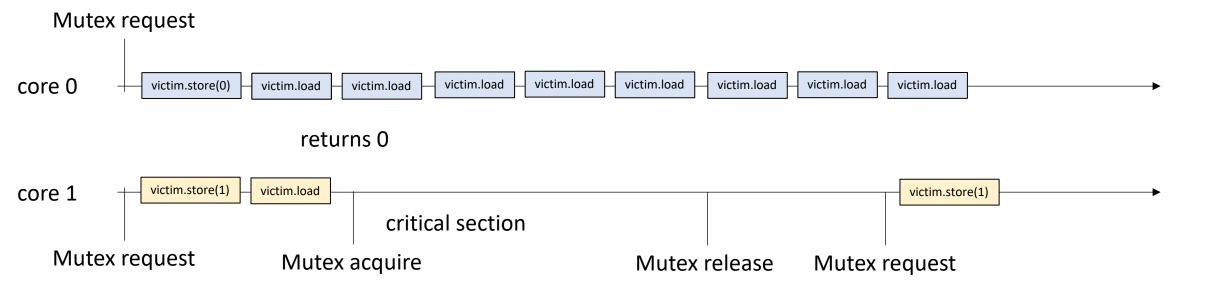
```
Thread 0: Thread 1:
m.lock();
m.unlock();
m.unlock();
```



```
void lock() {
  victim.store(thread_id);
  while (victim.load() == thread_id);
}
```

```
void unlock() {}
```

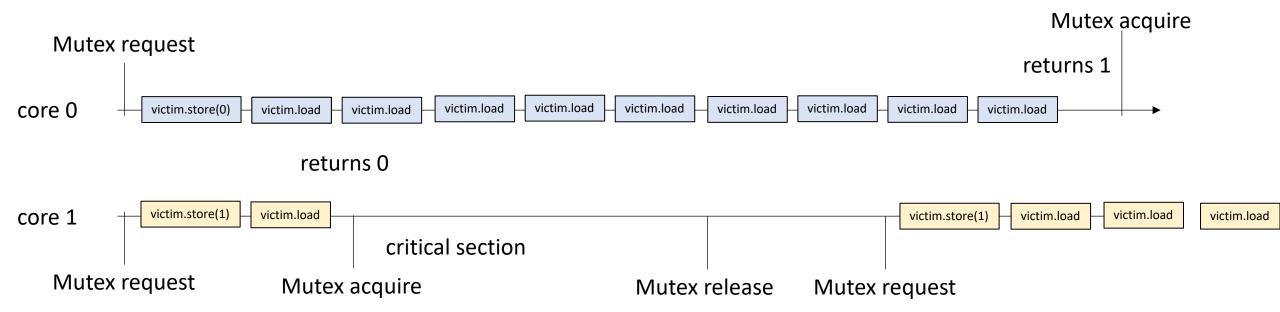
```
Thread 0: Thread 1:
m.lock();
m.unlock();
m.unlock();
```



```
void lock() {
  victim.store(thread_id);
  while (victim.load() == thread_id);
}
```

```
void unlock() {}
```

```
Thread 0: Thread 1:
m.lock();
m.unlock();
m.unlock();
```



Finally, we can can make a mutex that works:

Use flags to mark interest Use victim to break ties

Called the **Peterson Lock**

```
class Mutex {
public:
  Mutex() {
    victim = -1;
    flag[0] = flag[1] = 0;
  void lock();
  void unlock();
private:
  atomic_int victim;
  atomic_bool flag[2];
```

Initially:

No victim and no threads are interested in the critical section

flags and victim

```
void lock() {
  int j = thread_id == 0 ? 1 : 0;
  flag[thread_id].store(1);
  victim.store(thread_id);
  while (victim.load() == thread_id
         && flag[j] == 1);
}
```

j is the other thread

Mark ourself as interested

volunteer to be the victim in case of a tie

Spin only if:
 there was a tie in wanting the lock,
 and I won the volunteer raffle to spin

```
void unlock() {
  int i = thread_id;
  flag[i].store(0);
}
```

mark ourselves as uninterested

previous flag issue

```
void lock() {
  int i = thread_id;
  flag[i].store(1);
  int j = i == 0 ? 1 : 0;
  while (flag[j].load() == 1);
}
```

```
void unlock() {
  int i = thread_id;
  flag[i].store(0);
}
```

```
Thread 0: Thread 1:
m.lock();
m.unlock();
m.unlock();
```

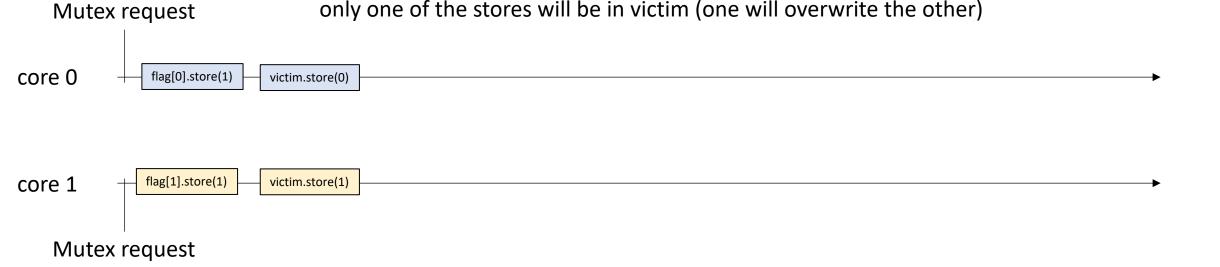
how does petersons solve this?

Both will spin forever! Mutex request flag[0].store(1) core 0 flag[1].load flag[1].load flag[1].load flag[1].load flag[1].load flag[1].load flag[1].load flag[1].load returns 1 flag[1].store(1) flag[0].load flag[0].load flag[0].load flag[0].load core 1 flag[0].load flag[0].load flag[0].load flag[0].load Mutex request

```
void lock() {
  int j = thread_id == 0 ? 1 : 0;
  flag[thread_id].store(1);
  victim.store(thread_id);
  while (victim.load() == thread_id
         && flag[j] == 1);
}
```

```
void unlock() {
  int i = thread_id;
  flag[i].store(0);
}
```

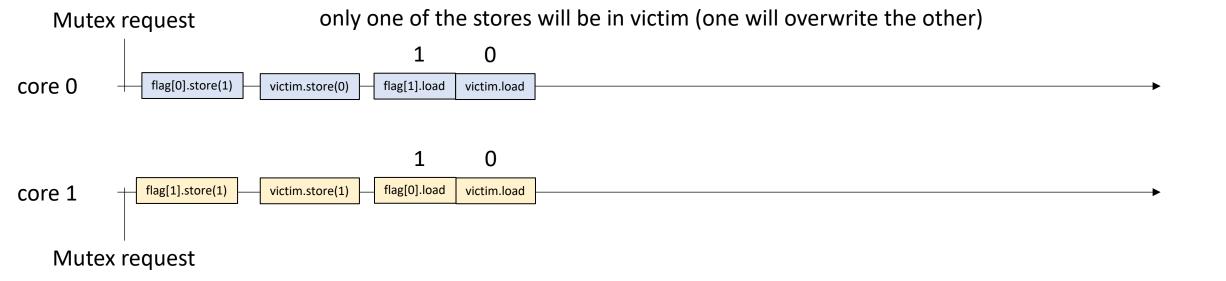
```
Thread 0: Thread 1:
m.lock();
m.unlock();
m.unlock();
```



```
void lock() {
  int j = thread_id == 0 ? 1 : 0;
  flag[thread_id].store(1);
  victim.store(thread_id);
  while (victim.load() == thread_id
         && flag[j] == 1);
}
```

```
void unlock() {
  int i = thread_id;
  flag[i].store(0);
}
```

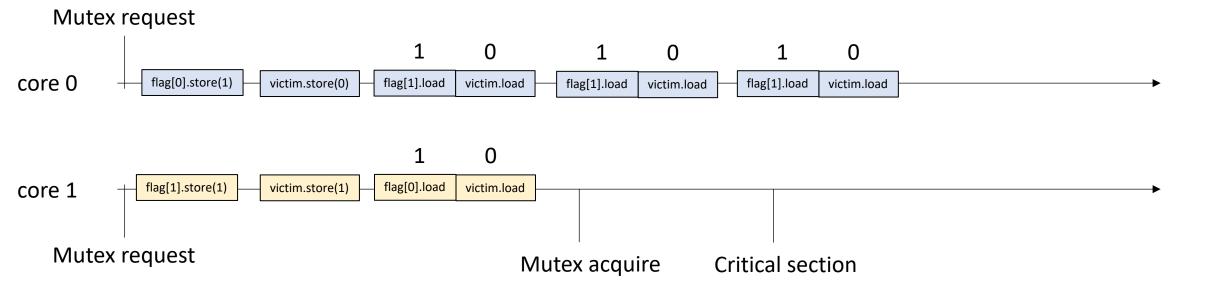
```
Thread 0: Thread 1:
m.lock();
m.unlock();
m.unlock();
```



```
void lock() {
  int j = thread_id == 0 ? 1 : 0;
  flag[thread_id].store(1);
  victim.store(thread_id);
  while (victim.load() == thread_id
         && flag[j] == 1);
}
```

```
void unlock() {
  int i = thread_id;
  flag[i].store(0);
}
```

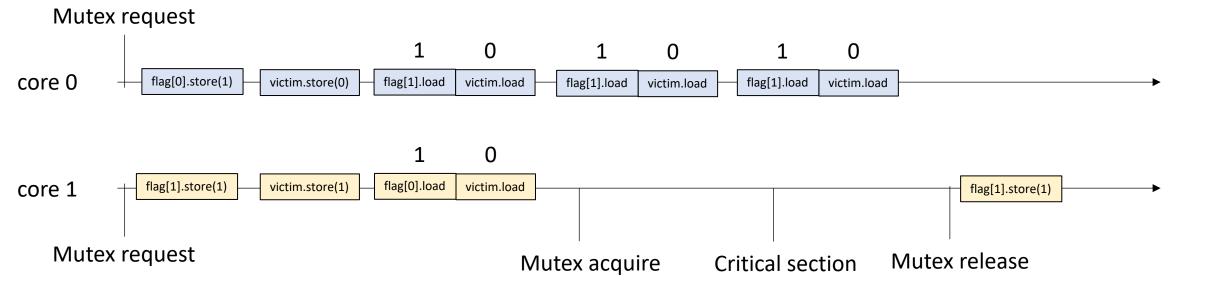
```
Thread 0: Thread 1:
m.lock(); m.lock();
m.unlock();
```



```
void lock() {
  int j = thread_id == 0 ? 1 : 0;
  flag[thread_id].store(1);
  victim.store(thread_id);
  while (victim.load() == thread_id
         && flag[j] == 1);
}
```

```
void unlock() {
  int i = thread_id;
  flag[i].store(0);
}
```

```
Thread 0: Thread 1:
m.lock(); m.lock();
m.unlock();
```

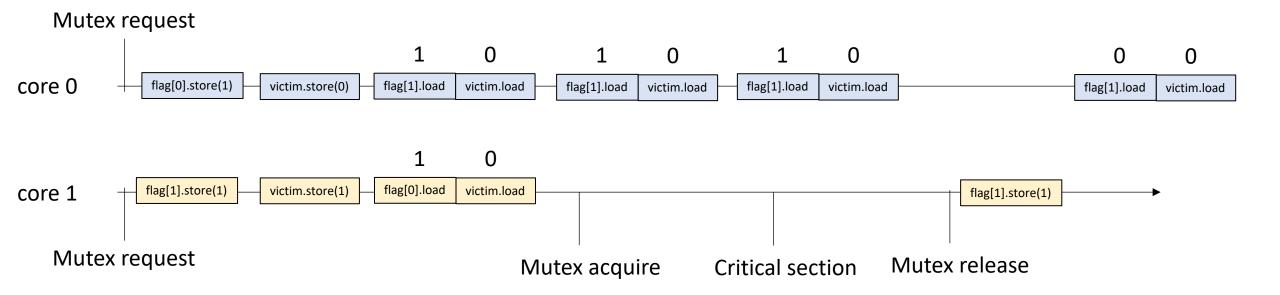


```
void lock() {
  int j = thread_id == 0 ? 1 : 0;
  flag[thread_id].store(1);
  victim.store(thread_id);
  while (victim.load() == thread_id
         && flag[j] == 1);
}
```

```
void unlock() {
  int i = thread_id;
  flag[i].store(0);
}
```

```
Thread 0: Thread 1:
m.lock(); m.lock();
m.unlock();
```

Mutex acquire



previous victim issue

```
void lock() {
  victim.store(thread_id);
  while (victim.load() == thread_id);
}
```

```
void unlock() {}
```

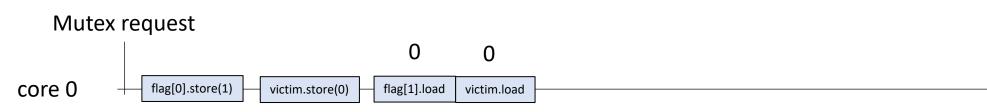
```
Thread 0:
m.lock();
m.unlock();
```

previous flag issue

```
int j = thread_id == 0 ? 1 : 0;
flag[thread_id].store(1);
victim.store(thread_id);
while (victim.load() == thread_id
         && flag[j] == 1);
}
```

```
void unlock() {
  int i = thread_id;
  flag[i].store(0);
}
```

```
Thread 0:
m.lock();
m.unlock();
```



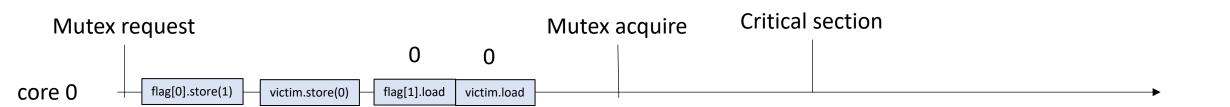
void lock() {

previous flag issue

```
void lock() {
  int j = thread_id == 0 ? 1 : 0;
  flag[thread_id].store(1);
  victim.store(thread_id);
  while (victim.load() == thread_id
          && flag[j] == 1);
}
```

```
void unlock() {
  int i = thread_id;
  flag[i].store(0);
}
```

```
Thread 0:
m.lock();
m.unlock();
```



we can enter critical section because the other thread isn't interested

This lock satisfies the two critical properties

Mutual exclusion

Deadlock freedom

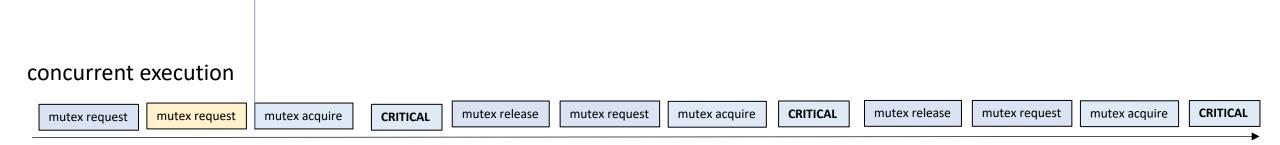
More formal proof given in the textbook

recall the starvation property:

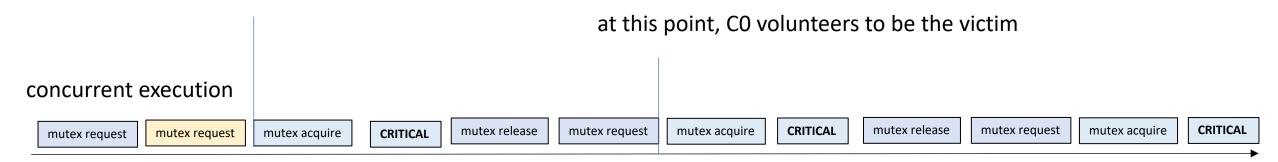
Thread 1 (yellow) requests the mutex but never gets it

Concurrent execution mutex request mutex re

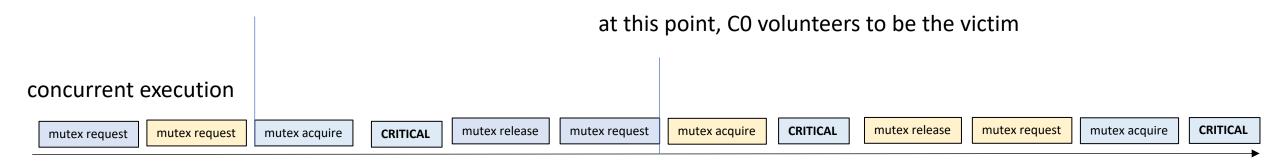
```
void lock() {
  int j = thread_id == 0 ? 1 : 0;
  flag[thread_id].store(1);
  victim.store(thread_id);
  while (victim.load() == thread_id
         && flag[j] == 1);
}
```



```
void lock() {
  int j = thread_id == 0 ? 1 : 0;
  flag[thread_id].store(1);
  victim.store(thread_id);
  while (victim.load() == thread_id
         && flag[j] == 1);
}
```

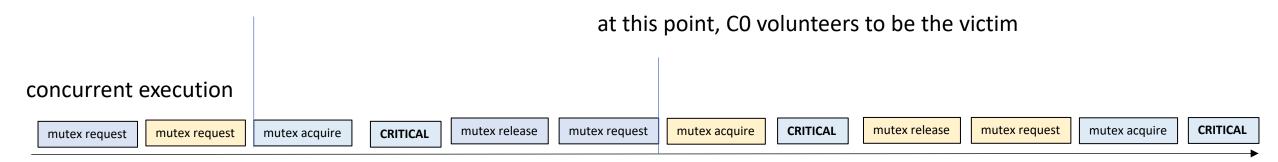


```
void lock() {
  int j = thread_id == 0 ? 1 : 0;
  flag[thread_id].store(1);
  victim.store(thread_id);
  while (victim.load() == thread_id
         && flag[j] == 1);
}
```



Threads take turns in petersons algorithm. It is starvation free

```
void lock() {
  int j = thread_id == 0 ? 1 : 0;
  flag[thread_id].store(1);
  victim.store(thread_id);
  while (victim.load() == thread_id
         && flag[j] == 1);
}
```



Mutex Implementations

Peterson only works with 2 threads.

Generalizes to the Filter Lock (Read chapter 2 in the book, part 1 of your homework!)

Historical perspective

- These locks are not very performant compared to modern solutions
 - Your HW will show this
- However, they are academically interesting: they can be implemented with plain loads and stores

 We will now turn our attention to more performant implementations that use RMWs

Start by revisiting our first mutex implementation

- A first attempt:
 - A mutex contains a boolean.
 - The mutex value set to 0 means that it is free. 1 means that some thread is holding it.
 - To lock the mutex, you wait until it is set to 0, then you store 1 in the flag.
 - To unlock the mutex, you set the mutex back to 0.
- Let's remember why it was buggy

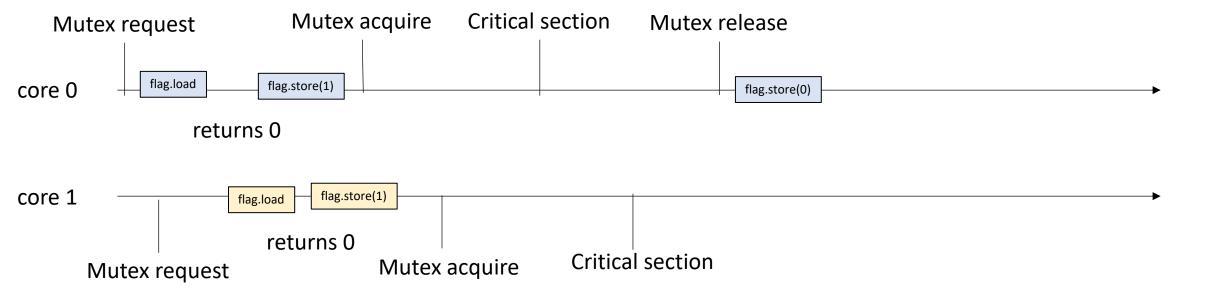
```
Buggy Mutex implementation: Analysis
```

```
void lock() {
  while (flag.load() == 1);
  flag.store(1);
}
```

```
void unlock() {
  flag.store(0);
}
```

```
Thread 0: Thread 1:
m.lock();
m.unlock();
m.unlock();
```

Critical sections overlap! This mutex implementation is not correct!



What went wrong?

- The load and stores from two threads interleaved
 - What if there was a way to prevent this?

What went wrong?

- The load and stores from two threads interleaved
 - What if there was a way to prevent this?

- Atomic RMWs
 - operate on atomic types (we already have atomic types)
 - recall the non-locking bank accounts:
 atomic_fetch_add(atomic *a, value v);

What is a RMW

A read-modify-write consists of:

- read
- modify
- write

done atomically, i.e. they cannot interleave.

Typically returns the value (in some way) from the read.

atomic_fetch_add

Recall the lock free account

Atomic Read-modify-write (RMWs): primitive instructions that implement a read event, modify event, and write event indivisibly, i.e. it cannot be interleaved.

```
atomic_fetch_add(atomic_int * addr, int value) {
   int tmp = *addr; // read
   tmp += value; // modify
   *addr = tmp; // write
}
```

atomic_fetch_add

Recall the lock free account

Atomic Read-modify-write (RMWs): primitive instructions that implement a read event, modify event, and write event indivisibly, i.e. it cannot be interleaved.

```
int atomic_fetch_add(atomic_int * addr, int value) {
   int stash = *addr; // read
   int new_value = value + stash; // modify
   *addr = new_value; // write
   return stash; // return previous value in the memory location
}
```

Tyler's coffee addiction:		<u>Tyler's employer</u>	
<pre>atomic_fetch_add(&tylers_account, -1);</pre>		<pre>atomic_fetch_add(&tylers_account, 1)</pre>) ;
tino o		time	
time			

```
Tyler's coffee addiction:

atomic_fetch_add(&tylers_account, -1);

atomic_fetch_add(&tylers_account, 1);
```

```
atomic_fetch_add(&tylers_account, -1);
```

time

```
atomic_fetch_add(&tylers_account, 1);
```

time

```
Tyler's coffee addiction:
```

```
Tyler's employer
```

```
atomic_fetch_add(&tylers_account, -1);
```

```
atomic_fetch_add(&tylers_account, 1);
```

time

```
tmp = tylers_account.load();
tmp -= 1;
tylers_account.store(tmp);
```

time

```
tmp = tylers_account.load();
tmp += 1;
tylers_account.store(tmp);
```

```
Tyler's coffee addiction:
```

```
Tyler's employer
```

```
atomic_fetch_add(&tylers_account, -1);
```

```
atomic_fetch_add(&tylers_account, 1);
```

time

```
tmp = tylers_account.load();
tmp -= 1;
tylers_account.store(tmp);
```

cannot interleave!

```
tmp = tylers_account.load();
tmp += 1;
tylers_account.store(tmp);
```

time

Tyler's coffee addiction:

Tyler's employer

```
atomic_fetch_add(&tylers_account, -1);
```

```
atomic_fetch_add(&tylers_account, 1);
```

time

```
cannot interleave!
```

```
tmp = tylers_account.load();
tmp += 1;
tylers_account.store(tmp);
```

time

```
tmp = tylers_account.load();
tmp -= 1;
tylers_account.store(tmp);
```

either way, account breaks even at the end!

RMW-based locks

A few simple RMWs enable lots of interesting mutex implementations

 When we have simpler implementations, we can focus on performance

• Simplest atomic RMW will allow us to implement an:

N-threaded mutex with 1 bit!

```
value atomic_exchange(atomic *a, value v);
```

Loads the value at a and stores the value in \mathbf{v} at a. Returns the value that was loaded.

```
value atomic_exchange(atomic *a, value v);
```

Loads the value at a and stores the value in \mathbf{v} at a. Returns the value that was loaded.

```
value atomic_exchange(atomic *a, value v) {
  value tmp = a.load();
  a.store(v);
  return tmp;
}
```

```
#include <atomic>
using namespace std;
class Mutex {
public:
  Mutex() {
    flag = false;
  void lock();
  void unlock();
private:
  atomic_bool flag;
```

Lets make a mutex with just one atomic bool!

```
#include <atomic>
using namespace std;
class Mutex {
public:
  Mutex() {
    flag = false;
  void lock();
  void unlock();
private:
  atomic_bool flag;
```

Lets make a mutex with just one atomic bool!

initialized to false

one atomic flag

```
#include <atomic>
using namespace std;
class Mutex {
public:
  Mutex() {
    flag = false;
  void lock();
  void unlock();
private:
  atomic_bool flag;
```

Lets make a mutex with just one atomic bool!

initialized to false

main idea:

The flag is false when the mutex is free.

The flag is true when some thread has the mutex.

one atomic flag

```
void lock() {
  while (atomic_exchange(&flag, true) == true);
}
```

```
void lock() {
  while (atomic_exchange(&flag, true) == true);
}
```

So what's going on?

```
void lock() {
  while (atomic_exchange(&flag, true) == true);
}
```

Two cases:

So what's going on?

mutex is free: the value loaded is false. We store true. The value returned is False, so we don't spin

mutex is taken: the value loaded is true, we put the SAME value back (true). The returned value is true, so we spin.

```
void unlock() {
  flag.store(false);
}
```

Unlock is simple: just store false to the flag, marking the mutex as available.

Analysis

core 0

```
void lock() {
  while (atomic_exchange(&flag, true) == true);
}
```

```
Thread 0: Thread 1:
m.lock(); m.lock();
m.unlock();
```

```
void unlock() {
  flag.store(false);
}
```

```
core 1
```

Analysis

core 1

```
void lock() {
  while (atomic_exchange(&flag, true) == true);
}
```

```
Thread 0: Thread 1:
m.lock(); m.lock();
m.unlock();
```

```
void unlock() {
  flag.store(false);
}
```

```
Mutex request

core 0

EXCH()

returns false
```

core 1

```
void lock() {
  while (atomic_exchange(&flag, true) == true);
}
```

```
Thread 0: Thread 1:
m.lock(); m.lock();
m.unlock();
```

```
void unlock() {
  flag.store(false);
}
```

```
Mutex request Mutex acquire

core 0

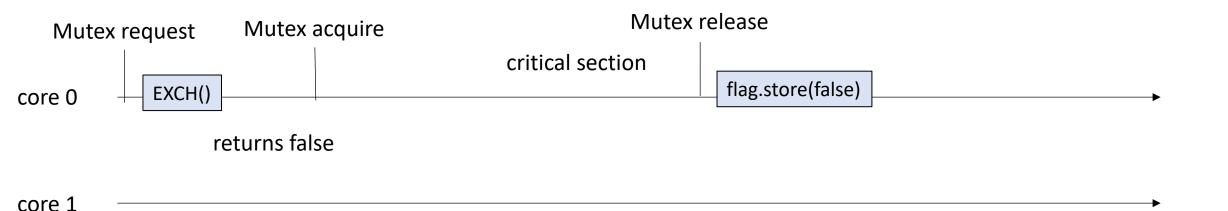
EXCH()

returns false
```

```
void lock() {
  while (atomic_exchange(&flag, true) == true);
}
```

```
Thread 0: Thread 1:
m.lock(); m.lock();
m.unlock();
```

```
void unlock() {
  flag.store(false);
}
```



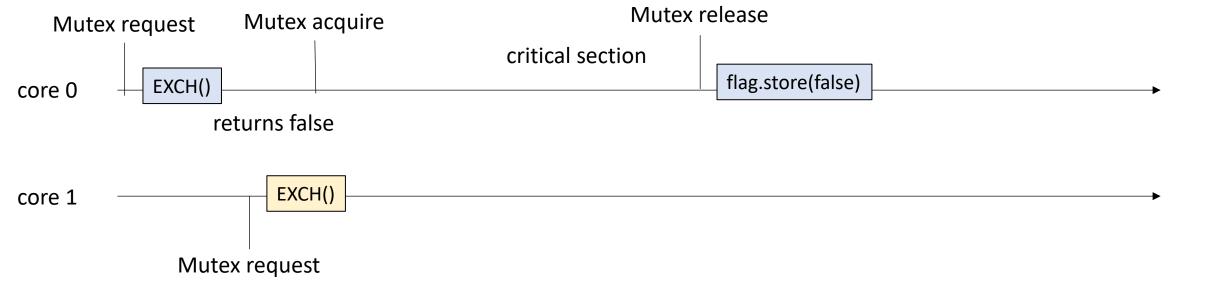
```
void lock() {
  while (atomic_exchange(&flag, true) == true);
}
```

```
void unlock() {
                                                                       flag.store(false);
   Thread 0:
                       Thread 1:
  m.lock();
                       m.lock();
  m.unlock();
                       m.unlock();
                                                                                       mutex works
                                                                                       with one thread
                                                    Mutex release
                   Mutex acquire
   Mutex request
                                         critical section
                                                            flag.store(false)
           EXCH()
core 0
                returns false
core 1
```

```
void lock() {
  while (atomic_exchange(&flag, true) == true);
}
```

```
Thread 0: Thread 1:
m.lock();
m.unlock();
m.unlock();
```

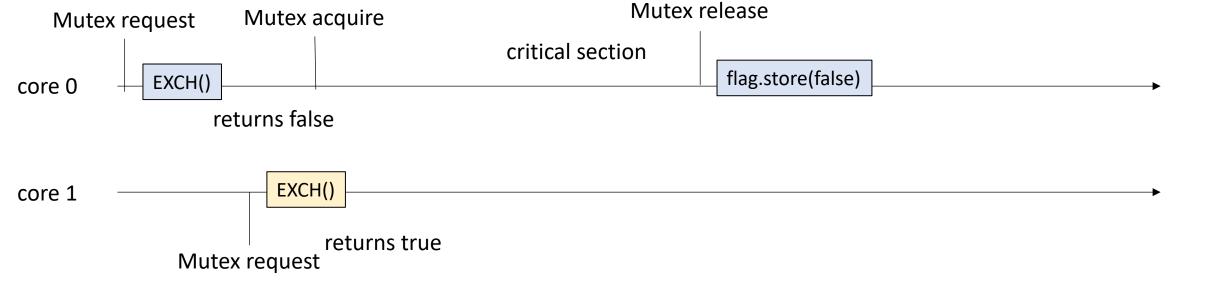
```
void unlock() {
  flag.store(false);
}
```



```
void lock() {
  while (atomic_exchange(&flag, true) == true);
}
```

```
Thread 0: Thread 1:
m.lock();
m.unlock();
m.unlock();
```

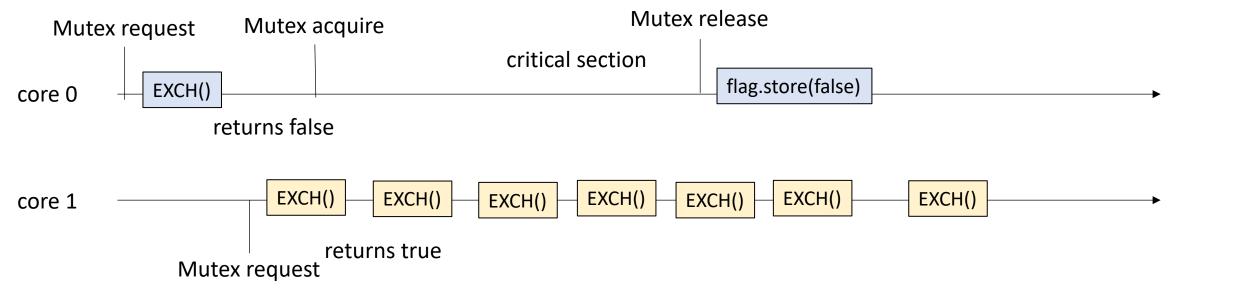
```
void unlock() {
  flag.store(false);
}
```



```
void lock() {
  while (atomic_exchange(&flag, true) == true);
}
```

```
Thread 0: Thread 1:
m.lock();
m.unlock();
m.unlock();
```

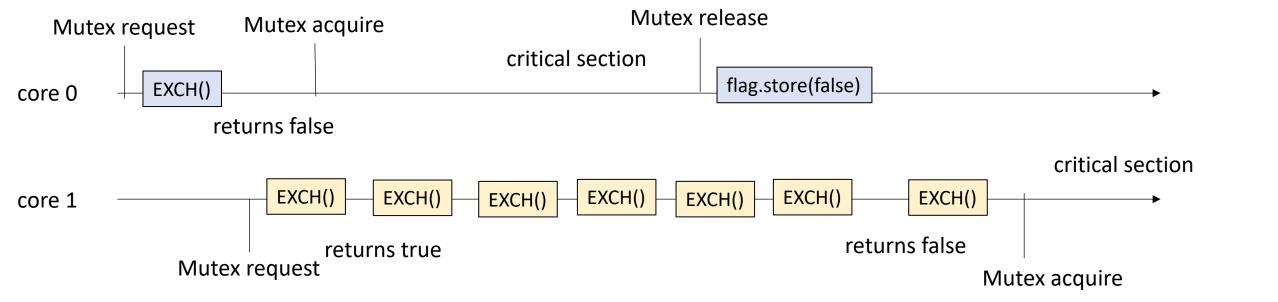
```
void unlock() {
  flag.store(false);
}
```



```
void lock() {
  while (atomic_exchange(&flag, true) == true);
}
```

```
Thread 0: Thread 1:
m.lock(); m.lock();
m.unlock();
```

```
void unlock() {
  flag.store(false);
}
```

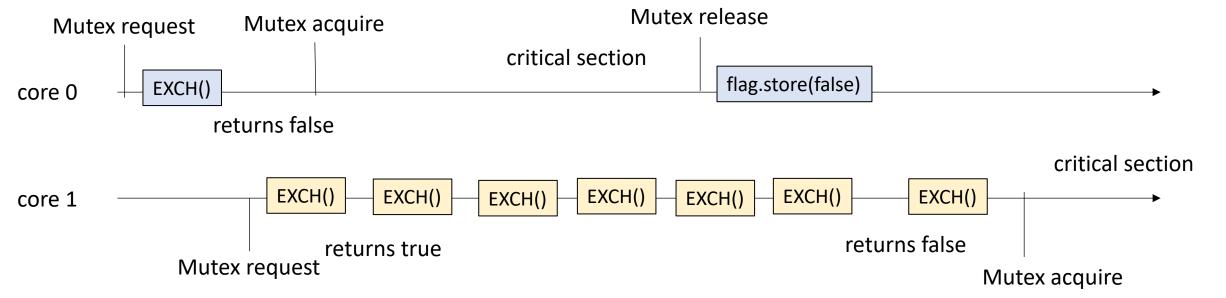


```
void lock() {
  while (atomic_exchange(&flag, true) == true);
}
```

```
Thread 0: Thread 1:
m.lock(); m.lock();
m.unlock();
```

```
void unlock() {
  flag.store(false);
}
```

what about interleavings?



```
void lock() {
  while (atomic_exchange(&flag, true) == true);
}
```

what about 4 threads?

```
void unlock() {
  flag.store(false);
}
```

```
core 0

Mutex request

EXCH()

Core 1

Mutex request

EXCH()

EXCH()

Mutex request

EXCH()
```

```
void lock() {
  while (atomic_exchange(&flag, true) == true);
}
```

```
void unlock() {
  what about 4 threads?
                                                                        flag.store(false);
                  atomic operations can't overlap
        Mutex request
                                EXCH()
core 0
         Mutex request
core 1
                                EXCH()
         Mutex request
core 2
                                EXCH()
         Mutex request
core 3
                                EXCH()
```

```
void lock() {
  while (atomic_exchange(&flag, true) == true);
}
```

```
void unlock() {
  what about 4 threads?
                                                                        flag.store(false);
                  atomic operations can't overlap
        Mutex request
                                        EXCH()
core 0
         Mutex request
core 1
                                EXCH()
         Mutex request
core 2
                                                   EXCH()
         Mutex request
core 3
                                                               EXCH()
```

```
void lock() {
  while (atomic_exchange(&flag, true) == true);
}
```

```
void unlock() {
  what about 4 threads?
                                                                         flag.store(false);
                   atomic operations can't overlap
         Mutex request
                                         EXCH()
core 0
         Mutex request
core 1
                                EXCH()
                             this one will win
         Mutex request
core 2
                                                    EXCH()
         Mutex request
core 3
                                                                EXCH()
```

```
void lock() {
  while (atomic_exchange(&flag, true) == true);
}
```

```
void unlock() {
  what about 4 threads?
                                                                          flag.store(false);
                   atomic operations can't overlap
         Mutex request
                                          EXCH()
core 0
                                                  spin
         Mutex request
core 1
                                 EXCH()
                             this one will win
         Mutex request
core 2
                                                     EXCH()
                                                  spin
         Mutex request
                                                                 EXCH()
core 3
                                                                        spin
```

```
void lock() {
  while (atomic_exchange(&flag, true) == true);
}
```

```
void unlock() {
  what about 4 threads?
                                                                          flag.store(false);
                   atomic operations can't overlap
         Mutex request
                           EXCH()
core 0
                                   spin
         Mutex acquired
                             Critical section
core 1
         Mutex request
core 2
                                      EXCH()
                                           spin
         Mutex request
core 3
                                                 EXCH()
                                                         spin
```

```
void lock() {
  while (atomic_exchange(&flag, true) == true);
}
```

```
void unlock() {
  what about 4 threads?
                                                                            flag.store(false);
                   atomic operations can't overlap
         Mutex request
                            EXCH()
core 0
                                    spin
                                                  mutex release
         Mutex acquired
                              Critical section
                                                                  flag.store(false)
core 1
         Mutex request
core 2
                                        EXCH()
                                            spin
          Mutex request
core 3
                                                   EXCH()
                                                          spin
```

```
void lock() {
  while (atomic_exchange(&flag, true) == true);
}
```

```
void unlock() {
  what about 4 threads?
                                                                              flag.store(false);
                    atomic operations can't overlap
         Mutex request
                            EXCH()
                                                                                                             EXCH()
core 0
                                    spin
                                                   mutex release
         Mutex acquired
                              Critical section
                                                                   flag.store(false)
core 1
                                                                                              some thread
                                                                                              will win
         Mutex request
core 2
                                                                                       EXCH()
                                        EXCH()
                                             spin
          Mutex request
core 3
                                                                                                  EXCH()
                                                    EXCH()
                                                            spin
```

First example: Exchange Mutex

• Questions?

Exchange was the simplest RMW (no modify)

```
bool atomic compare exchange strong(atomic *a, value *expected, value replace);
```

Exchange was the simplest RMW (no modify)

Most versatile RMW: Compare-and-swap (CAS)

bool atomic_compare_exchange_strong(atomic *a, value *expected, value replace);

Checks if value at a is equal to the value at expected. If it is equal, swap with replace. returns True if the values were equal. False otherwise.

Exchange was the simplest RMW (no modify)

Most versatile RMW: Compare-and-swap (CAS)

bool atomic_compare_exchange_strong(atomic *a, value *expected, value replace);

Checks if value at a is equal to the value at expected. If it is equal, swap with replace. returns True if the values were equal. False otherwise.

expected is passed by reference: the previous value at a is returned

Exchange was the simplest RMW (no modify)

```
bool atomic_compare_exchange_strong(atomic *a, value *expected, value replace) {
   value tmp = a.load();
   if (tmp == *expected) {
        a.store(replace);
        return true;
   }
   *expected = tmp;
   return false;
}
```

Exchange was the simplest RMW (no modify)

we will discuss this soon!

```
bool atomic_compare_exchange_strong(atomic *a, value *expected, value replace) {
   value tmp = a.load();
   if (tmp == *expected) {
        a.store(replace);
        return true;
   }
   *expected = tmp;
   return false;
}
```

```
bool atomic compare exchange strong(atomic *a, value *expected, value replace) {
   value tmp = a.load();
   if (tmp == *expected) {
     a.store(replace);
     return true;
   *expected = tmp;
   return false;
   thread 0:
   // some atomic int address a
   int e = 0;
                                                             a:0
   bool s = atomic CAS(a, \&e, 6);
```

```
bool atomic compare exchange strong(atomic *a, value *expected, value replace) {
   value tmp = a.load();
   if (tmp == *expected) {
     a.store(replace);
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   *expected = tmp;
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bool atomic compare exchange strong(atomic *a, value *expected, value replace) {
   value tmp = a.load();
   if (tmp == *expected) {
     a.store(replace);
     return true;
   *expected = tmp;
   return false;
   thread 0:
   // some atomic int address a
   int e = 0;
                                                             a:6
   bool s = atomic CAS(a, \&e, 6);
```

```
bool atomic compare exchange strong(atomic *a, value *expected, value replace) {
   value tmp = a.load();
   if (tmp == *expected) {
     a.store(replace);
     return true;
   *expected = tmp;
   return false;
   thread 0:
   // some atomic int address a
   int e = 0;
                                                             a:6
   bool s = atomic_CAS(a, \&e, 6);
      true
```

```
bool atomic compare exchange strong(atomic *a, value *expected, value replace) {
   value tmp = a.load();
   if (tmp == *expected) {
     a.store(replace);
     return true;
   *expected = tmp;
   return false;
                                      next example
   thread 0:
   // some atomic int address a
   int e = 0;
                                                             a:16
   bool s = atomic CAS(a, \&e, 6);
```

```
bool atomic compare exchange strong(atomic *a, value *expected, value replace) {
   value tmp = a.load();
   if (tmp == *expected) {
     a.store(replace);
     return true;
   *expected = tmp;
   return false;
   thread 0:
   // some atomic int address a
   int e = 0;
                                                             a:16
   bool s = atomic_CAS(a, \&e, 6);
                           16
```

false

CAS lock

```
#include <atomic>
using namespace std;
class Mutex {
public:
  Mutex() {
    flag = false;
  void lock();
  void unlock();
private:
  atomic_bool flag;
```

Pretty intuitive: only 1 bit required again:

CAS lock

```
void lock() {
  bool e = false;
  int acquired = false;
  while (acquired == false) {
    acquired = atomic_compare_exchange_strong(&flag, &e, true);
    e = false;
  }
}
```

Check if the mutex is free, if so, take it.

compare the mutex to free (false), if so, replace it with taken (true). Spin while the thread isn't able to take the mutex.

CAS lock

```
void unlock() {
  flag.store(false);
}
```

Unlock is simple! Just store false back

Starvation

Are these RMW locks fair?

```
void lock() {
  while (atomic_exchange(&flag, true) == true);
}
```

```
void unlock() {
  flag.store(false);
}
```

```
mutex
request

core 0

mutex
request
core 1
```

```
void lock() {
  while (atomic_exchange(&flag, true) == true);
}
```

```
void unlock() {
  flag.store(false);
}
```

```
mutex request EXCH()

core 0

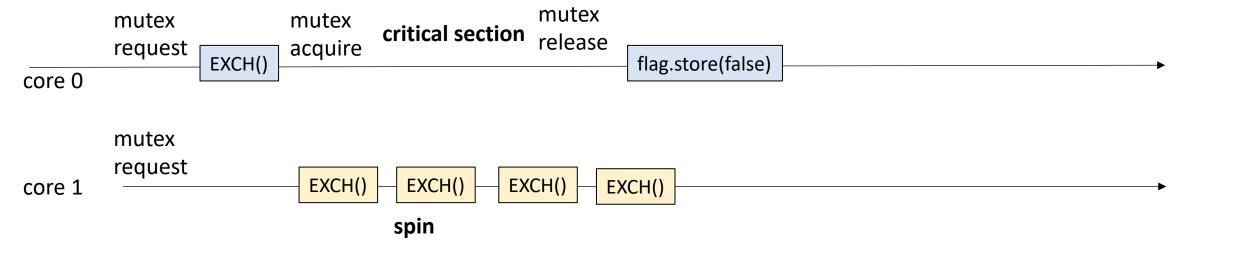
mutex acquire

request request

request spin
```

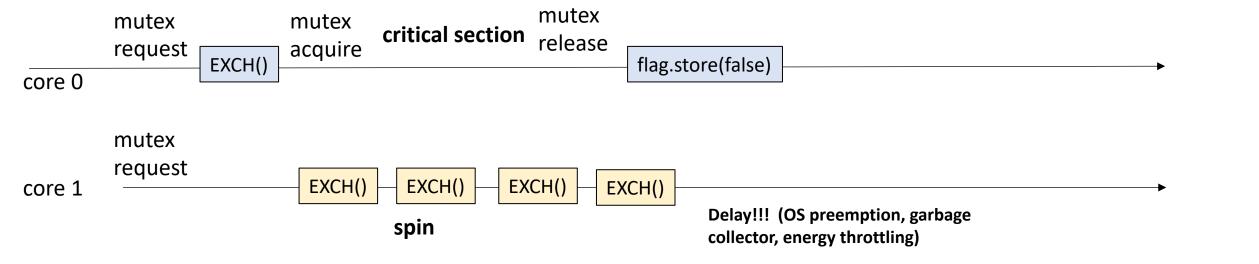
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void lock() {
  while (atomic_exchange(&flag, true) == true);
}
```

```
void unlock() {
  flag.store(false);
}
```



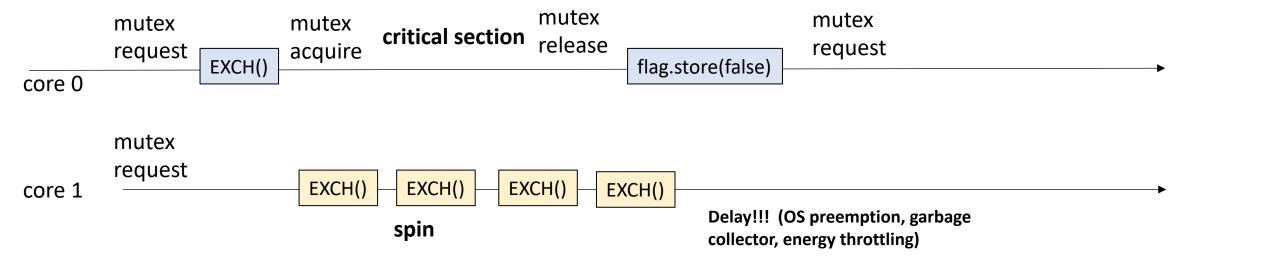
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void lock() {
  while (atomic_exchange(&flag, true) == true);
}
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void unlock() {
  flag.store(false);
}
```



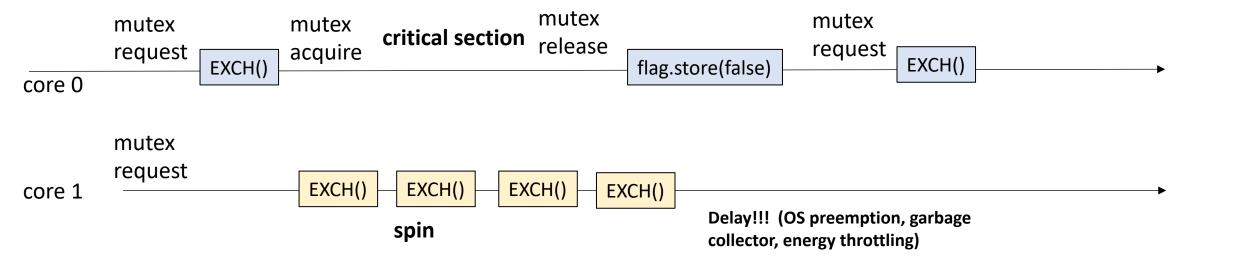
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void lock() {
  while (atomic_exchange(&flag, true) == true);
}
```

```
void unlock() {
  flag.store(false);
}
```



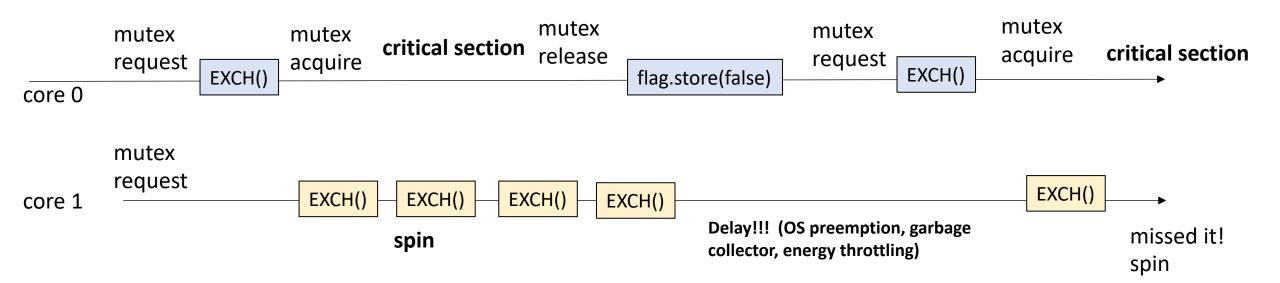
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void lock() {
  while (atomic_exchange(&flag, true) == true);
}
```

```
void unlock() {
  flag.store(false);
}
```



```
void lock() {
  while (atomic_exchange(&flag, true) == true);
}
```

```
void unlock() {
  flag.store(false);
}
```



How about in practice?

• Code demo

Thanks!

- Next time:
 - A fair RMW lock
 - optimizations (yield)
 - Reader-Writer locks
- Start on HW 2 part 1
- Do the quiz please!