ERICA WARD

Embedded Software Engineer

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Sunnyvale, CA

in LinkedIn

WORK EXPERIENCE

Embedded Software Engineer

Amazon Lab126

- # 2020 current
- Sunnyvale, CA
- Developed embedded software for 6 new consumer electronics products using Keil µVision which booted 9 seconds earlier compared to previous innovations.
- Designed SPI communication protocols for quick data transfer between embedded systems and peripherals, keeping data transmission errors at an all-time low of 1.4%.
- Conducted detailed code reviews to maintain code quality, slashing instances of post-release bug reports by 22%.
- Incorporated continuous integration with Buildroot for automating tasks, helping Amazon's agile team release a product 2 weeks before deadlines.

Systems Engineer

SpaceX

- **==** 2020 2022
- Hawthorne, CA
- Implemented ARM-based firmware solutions for satellite communication modules, maintaining a 97.2% reliability rate in establishing communication links.
- Managed configuration management processes on SVN, facilitating easier collaboration with engineering teams.
- Engineered LLVM compiler configurations for aerospace applications, optimizing code for spaceborne processors while keeping memory footprint at a maximum of 27%.
- Used hardware abstraction layers (HAL) for sensor interfaces, ensuring compatibility for 4 spacecraft platforms.

Technical Support Representative

Google

- **==** 2018 2020
- Mountain View, CA
- Maintained Google's internal knowledge base, lowering the average needed to handle support requests by 28 minutes.
- Assisted customers in debugging the browser's interface, decreasing overall system downtime by 8.3%.
- Guided the internal team for VHDL-based projects, resulting in a *36% increase in on-time project completion rate*.
- Helped customers in configuring the search engine to their location, helping them get 79% more accurate search results.

EDUCATION

Bachelor of Science Electrical Engineering Stanford University

- **2014 2018**
- Stanford, CA

SKILLS

- Keil µVision
- ARM
- Micrium µC/OS
- SVN
- SPI
- In-circuit Emulators
- LLVM Compiler Infrastructure
- VHDL
- Buildroot
- Simics