

BJT biasing circuits: (eliminates need of additional power supply)

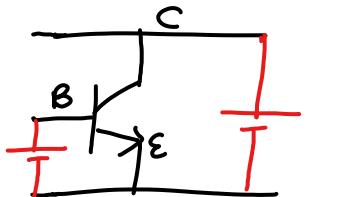
① Fixed bias

↳ Emitter bias (fixed bias with emitter resistor)

② Collector to base bias

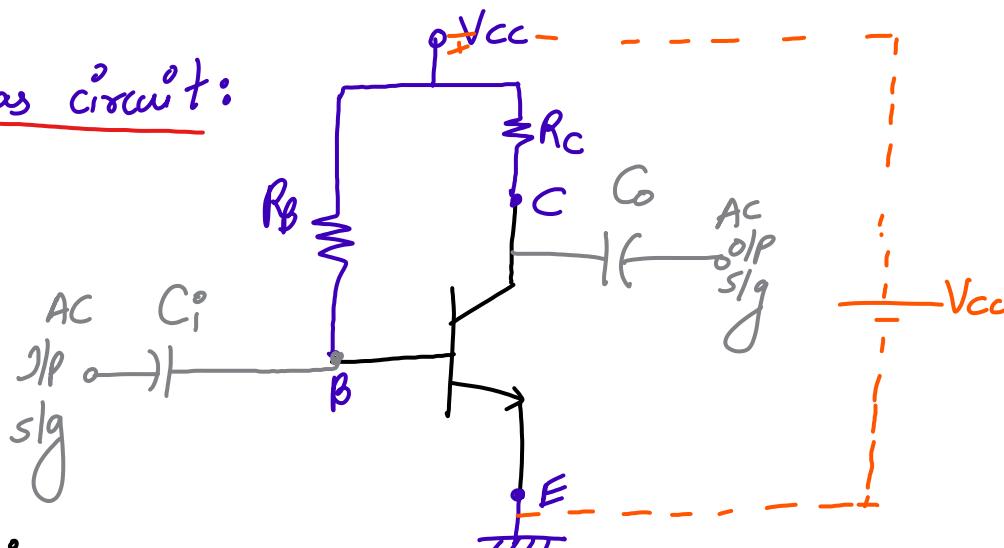
↳ (C to B bias with R_E)

③ Voltage-divider bias



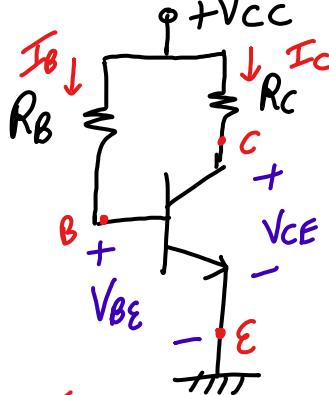
This requires 2 external supplies

I] Fixed bias circuit:



a) DC Analysis:

For $dc \rightarrow f=0 \rightarrow X_C = \frac{1}{2\pi f C} = \infty \rightarrow \text{Capacitors acts as open ckt}$



fixed-bias ckt

① Find Q -pt $\equiv (V_{CEQ}, I_{CQ})$

② Find Stability factor S or $S_{I_{CQ}}$

③ Plot Q -pt on DC load line

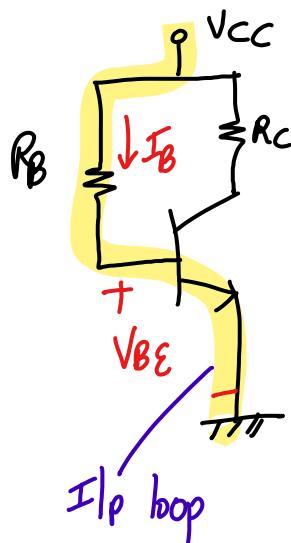
} Objectives

a) Q -point $\equiv (V_{CEQ}, I_{CQ})$

i) Apply KVL to $\text{I}_{\text{p}} \text{ loop}$

$$V_{CC} - I_B R_B - V_{BE} = 0$$

$$I_{BQ} = \frac{V_{CC} - V_{BE}}{R_B}$$



$$I_C = \beta I_B$$

$$V_{BE_{ON}} = 0.7V, \quad V_{CE} > 10V, \quad V_{CC} > V_{BE}$$

$$I_{BQ} \approx \frac{V_{CC} - \text{fixed}}{R_B - \text{fixed}} \quad \text{Hence, the name "fixed bias"}$$

$$I_{CQ} = \beta \left(\frac{V_{CC} - V_{BE}}{R_B} \right) \quad \text{--- (1)}$$

ii) Apply KVL to o/p loop,

$$V_{CC} - I_C R_C - V_{CE} = 0$$

$$V_{CEQ} = V_{CC} - I_C R_C \quad \text{--- (2)}$$

$$Q_{opt} = (V_{CEQ}, I_{CQ})$$

b) Stability factor (S or S_{I_C})

$$S = \frac{1 + \beta}{1 - \beta \left(\frac{\partial I_B}{\partial I_C} \right)}$$

$$\text{KVL to o/p, } V_{CC} - I_B R_B - V_{BE} = 0$$

Differentiate w.r.t I_C ,

$$0 - R_B \frac{\partial I_B}{\partial I_C} - 0 = 0 \rightarrow \frac{\partial I_B}{\partial I_C} = 0$$

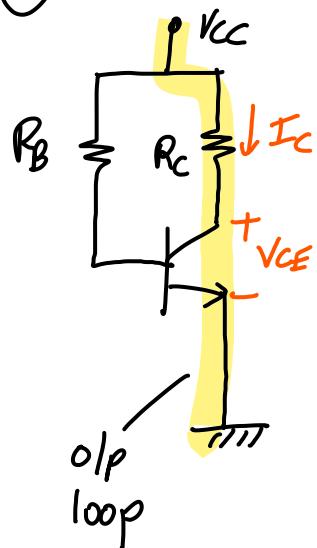
$$S = 1 + \beta \quad \text{--- Fixed bias ckt}$$

$$S = \frac{\partial I_C}{\partial I_{Co}}$$

$$\text{If } \beta = 100, S = 101$$

i.e. If I_{Co} changes by 1% $\rightarrow I_C$ will change by 101%

Stability of ckt is very poor



c) Plotting of DC load line & locating Q pt on it

load line (KVL to o/p loop),

$$V_{CC} - I_C R_C - V_{CE} = 0 \quad @$$

$$I_C R_C = V_{CC} - V_{CE}$$

i.e.

$$I_C = -\frac{V_{CE}}{R_C} + \frac{V_{CC}}{R_C}$$

→ Eqⁿ of DC load line

Slope = $m = -\frac{1}{R_C}$, $c = \frac{V_{CC}}{R_C}$

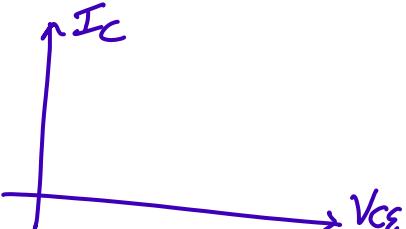
$$y = mx + c$$

slope

y-intercept

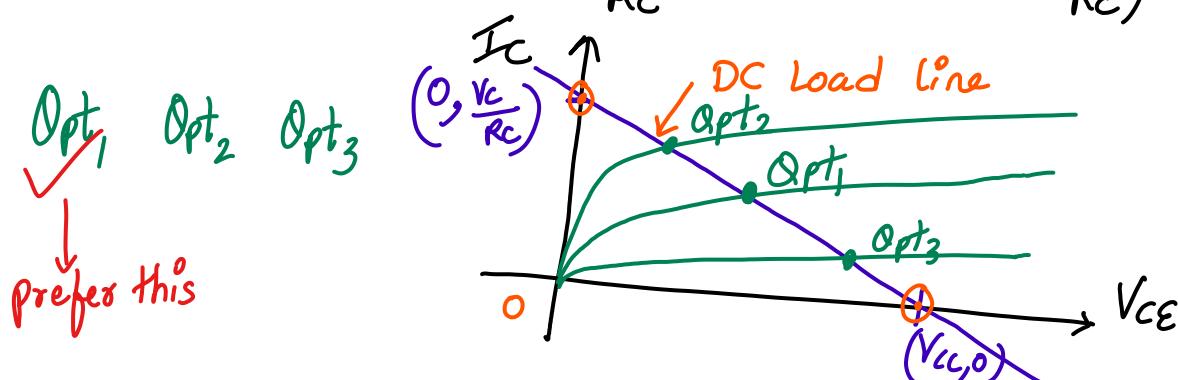
To plot a straight line, we require 2 co-ordinates

$$V_{CC} - I_C R_C - V_{CE} = 0$$



① Put $I_C = 0 \Rightarrow V_{CE} = V_{CC} \rightarrow 1^{st}$ point: $(V_{CC}, 0)$

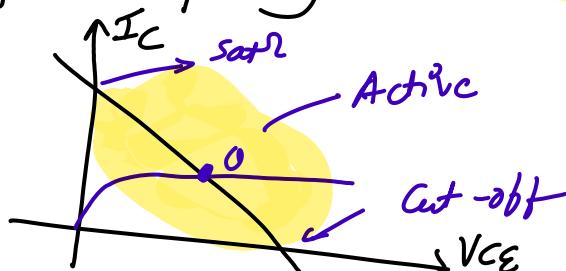
② Put $V_{CE} = 0 \Rightarrow I_C = \frac{V_{CC}}{R_C} \rightarrow 2^{nd}$ point: $(0, \frac{V_{CC}}{R_C})$



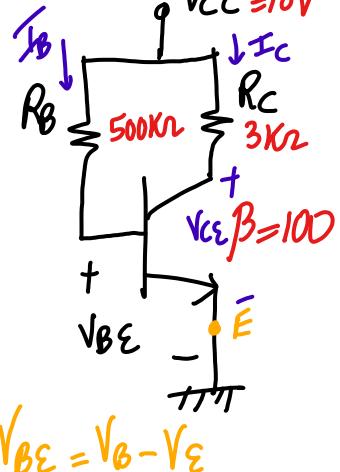
Intersection of o/p characteristics & DC load line is Opt

Opt → Operating point / Quiescent (Silent) point

→ guarantees that bjt is operating in "Active region"



Numerical 1: For the circuit shown below, find I_{CQ} , V_{CEQ} , S , V_B , V_C & V_{BC} . Also comment on region of operation



$$V_{BE(\text{on})} \approx 0.7V \text{ (Assume)}$$

$$K\Omega = 10^3$$

$$\textcircled{1} \quad V_{CC} - I_B R_B - V_{BE} = 0$$

$$I_{BQ} = \frac{V_{CC} - V_{BE}}{R_B} = \frac{10V - 0.7V}{500K\Omega}$$

$$I_{BQ} = \frac{18.6}{\mu\text{A}} \quad \mu\text{A} = 10^{-6}\text{A}$$

$$\textcircled{2} \quad I_{CQ} = \beta I_{BQ} = 100 \times 18.6 \mu\text{A}$$

$$I_{CQ} = \frac{1.86}{\text{mA}} \quad \text{mA} = 10^{-3}\text{A}$$

$$\textcircled{3} \quad V_{CC} - I_C R_C - V_{CE} = 0$$

$$\rightarrow V_{CEQ} = V_{CC} - I_{CQ} R_C = 10 - 1.86 \times 10^3 \times 3 \times 10^3$$

$$V_{CEQ} = 4.42 \text{ V}$$

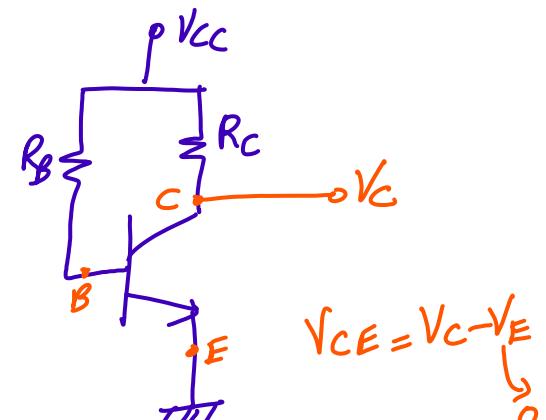
$$\textcircled{4} \quad S = 1 + \beta = 1 + 100 = 101$$

$$\textcircled{5} \quad V_B = V_{BE} = 0.7V \quad (\because V_E = 0)$$

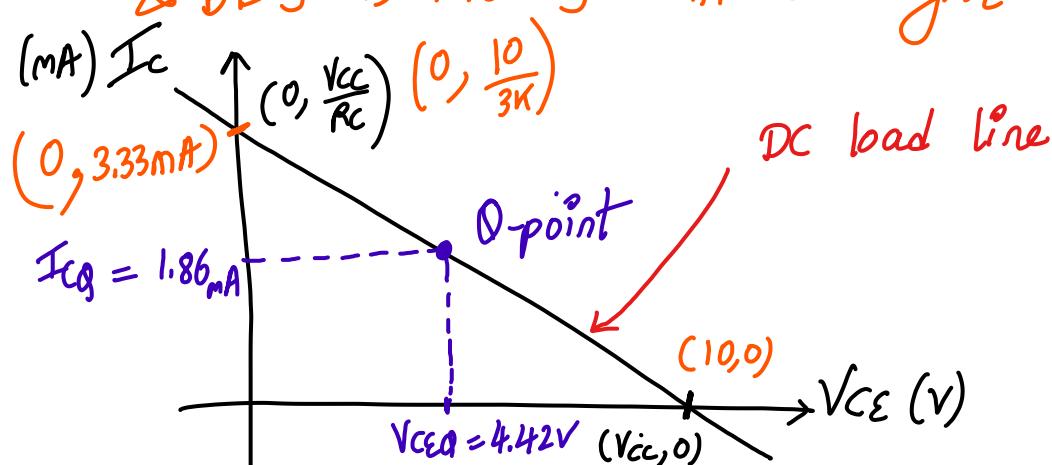
$$\textcircled{6} \quad V_C = V_{CE} = 4.42V$$

$$\textcircled{7} \quad V_{BC} = V_B - V_C = 0.7 - 4.42V$$

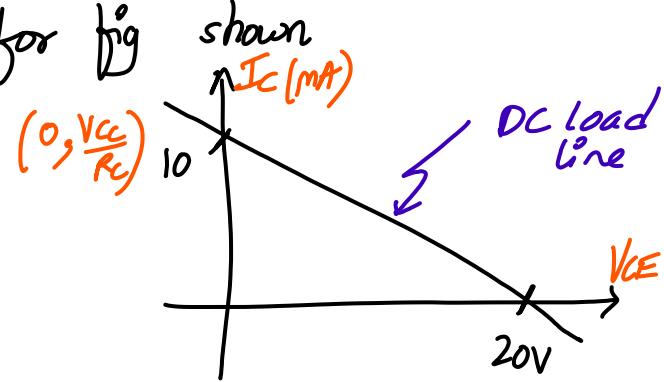
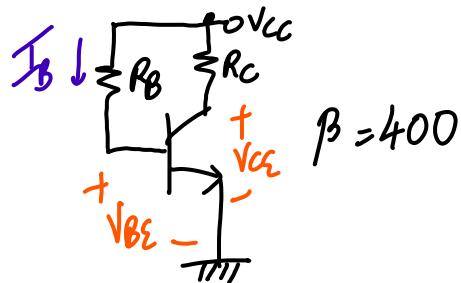
$$V_{BC} = -3.72V$$



$\rightarrow B-C J^n$ is R.B] $\& B-E J^n$ is F.B] BJT is operating in active region



Numerical 2: Find V_{CC} , R_C & R_B for fig shown



Solution: a) $V_{CE} = V_{CC} = 20V$

b) $\frac{V_{CC}}{R_C} = 10\text{mA} \rightarrow R_C = \frac{V_{CC}}{10\text{mA}} = \frac{20}{10\text{mA}} = 2\text{K}\Omega$

c) KVL to Jlp, $V_{CC} - I_B R_B - V_{BE} = 0$

$$I_B = \frac{V_{CC} - V_{BE}}{R_B}$$

$$V_{BE} = 0.7V \quad (\text{assume})$$

$$I_C = \beta I_B \rightarrow I_B = \frac{I_C}{\beta} = \frac{10\text{mA}}{400} = 25\mu\text{A}$$

d) $R_B = \frac{V_{CC} - V_{BE}}{I_B} = \frac{20 - 0.7}{25\mu\text{A}} = 772 \text{ K}\Omega$

Numerical 3: For the fixed-bias ckt, $\alpha = 0.98$, $I_{CBO} = 10\mu\text{A}$, $R_C = 3\text{K}\Omega$, $R_B = 820\text{K}\Omega$, $V_{CC} = 12V$ → Find I_{CQ} & V_{CEQ}

Solution:

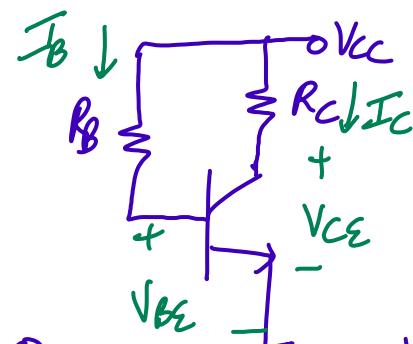
$$\textcircled{1} \quad \beta = \frac{\alpha}{1-\alpha} = \frac{0.98}{1-0.98} = 49$$

$$\textcircled{2} \quad \text{KVL to Jlp, } V_{CC} - I_B R_B - V_{BE} = 0$$

$$I_B = \frac{V_{CC} - V_{BE}}{R_B} = \frac{12 - 0.7}{820\text{K}\Omega} = 13.78 \mu\text{A}$$

$$\textcircled{3} \quad I_{CQ} = \beta I_{BQ} + (1+\beta) I_{CBO}$$

$$I_{CQ} = 49 \times 13.78 \times 10^{-6} + 50 \times 10 \times 10^{-6}$$



$$V_{BE} = 0.7V$$

$$I_{CQ} = \underline{1.175} \text{ mA}$$

④ KVL to o/p loop, $V_{CC} - I_C R_C - V_{CE} = 0$

$$\rightarrow V_{CEQ} = V_{CC} - I_C R_C$$

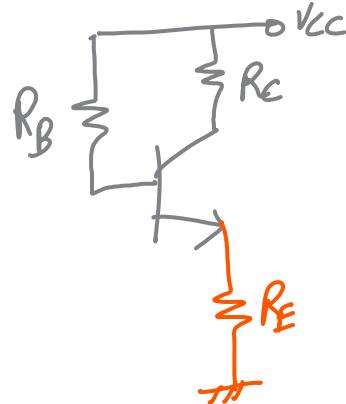
$$V_{CEQ} = 12 - 1.175 \times 10^3 \times 4 \times 10^3$$

$$\therefore V_{CEQ} = \underline{7.3} \text{ V}$$

— x — x —

Next time: Emitter Bias (Fixed bias with R_E)

Investigate
next lecture



- a) Opt (V_{CEQ}, I_{CQ})
- b) S
- c) Plot DC Load line & locate Opt

