40/134	Table 1	2. Alt	ernate f	unction	ns for poi	rt A
4	Port & Pin Name	AF0	AF1	AF2	AF3	AF
GPIO/(QUADENC,ADC)A	PA0	-	TIM2_ CH1_ ETR	-	TSC_ G1_IO1	-
GPIO/(QUADENC,ADC)B	PA1	RTC_ REFIN	TIM2_ CH2	-	TSC_ G1_IO2	-
LED/RED	PA2	-	TIM2_ CH3	-	TSC_ G1_IO3	-
LED/GREEN	PA3	-	TIM2_ CH4	-	TSC_ G1_IO4	-
ADC/V_BAT [8	PA4	-	-	TIM3_ CH2	TSC_ G2_IO1	-
ADC/V_BAT Rev 2	PA5	-	TIM2_ CH1_ ETR	-	TSC_ G2_IO2	-
QUADENC/A [№]	PA6	-	TIM16_ CH1	TIM3_ CH1	TSC_ G2_IO3	-
QUADENC/B	PA7	-	TIM17_ CH1	TIM3_ CH2	TSC_ G2_IO4	-
MOTOR/PWM_A	PA8	мсо	-	-	-	I2C2 SMB
MOTOR/PWM_B	PA9	-	-	-	TSC_ G4_IO1	I2C2 SCL
MOTOR/EN_A	PA10		TIM17_ BKIN	-	TSC_ G4_IO2	I2C2 SDA
MOTOR/EN_B	PA11	-	-	-	-	-
3						

Port & Pin	AF0	AF1	AF2	AF3	AF4	AF5	AF6	AF7	AF8	AF9	AF10	AF11	AF12	AF14	AF15
Name															
PA0	-	TIM2_ CH1_ ETR	-	TSC_ G1_IO1	-	-	-	USART2 _CTS	COMP1 _OUT	-	-	-	-	-	EVENT OUT
PA1	RTC_ REFIN	TIM2_ CH2	-	TSC_ G1_IO2	-	-	-	USART2 _RTS		TIM15_ CH1N	-	-	-	-	EVENT OUT
PA2	-	TIM2_ CH3	-	TSC_ G1_IO3	-	-	-	USART2 _TX	COMP2 _OUT	TIM15_ CH1	-	-	-	-	EVENT OUT
PA3	-	TIM2_ CH4	-	TSC_ G1_IO4	-	-	-	USART2 _RX	-	TIM15_ CH2	-	-	-	-	EVENT OUT
PA4	-	-	TIM3_ CH2	TSC_ G2_IO1	-	SPI1_ NSS	SPI3_NSS, I2S3_WS	USART2 _CK	-	-	-	-	-	-	EVENT OUT
PA5	-	TIM2_ CH1_ ETR	-	TSC_ G2_IO2	-	SPI1_ SCK	-	-	-	-	-	-	-	-	EVENT OUT
PA6	-	TIM16_ CH1	TIM3_ CH1	TSC_ G2_IO3	-	SPI1_ MISO	TIM1_BKIN	-	COMP1 _OUT	-	-	-	-	-	EVENT OUT
PA7	-	TIM17_ CH1	TIM3_ CH2	TSC_ G2_IO4	-	SPI1_ MOSI	TIM1_CH1N	-	COMP2 _OUT	-	-	-	-	-	EVENT OUT
PA8	МСО	-	-	-	I2C2_ SMBA	I2S2_ MCK	TIM1_CH1	USART1 _CK	-	-	TIM4_ ETR	-	-	-	EVENT OUT
PA9	-	-	-	TSC_ G4_IO1	I2C2_ SCL	I2S3_ MCK	TIM1_CH2	USART1 _TX	-	TIM15_ BKIN	TIM2_ CH3	-	-	-	EVENT OUT
PA10	-	TIM17_ BKIN	-	TSC_ G4_IO2	I2C2_ SDA	-	TIM1_CH3	USART1 _RX	COMP6 _OUT	-	TIM2_ CH4	-	-	-	EVENT OUT
PA11	-	-	-	-	-	-	TIM1_CH1N	USART1 _CTS	COMP1 _OUT	CAN_RX	TIM4_ CH1	TIM1_CH4	TIM1_ BKIN2	USB_ DM	EVENT OUT

		Table 1	2. Alt	ernate fu	unction	s for por	t A (cor	ntinued)								
	W	Port & Pin Name	AF0	AF1	AF2	AF3	AF4	AF5	AF6	AF7	AF8	AF9	AF10	AF11	AF12	AF14	AF15
LED/BL	UE	PA12	-	TIM16_ CH1	-	=	-	-	TIM1_CH2N	USART1 _RTS	COMP2 _OUT	CAN_TX	TIM4_ CH2	TIM1_ETR	-	USB_ DP	EVENT OUT
DBG/SW	DIO	PA13	SWDIO -JTMS	TIM16_ CH1N	-	TSC_ G4_IO3	-	IR_ OUT	-	USART3 _CTS	-	-	TIM4_ CH3	-	-	-	EVENT OUT
DBG/SW	/DCK	PA14	SWCLK -JTCK	-	1		I2C1_ SDA	-	TIM1_BKIN	USART2 _TX	-	-	1	-	-	1	EVENT OUT
TESTPI	1	PA15	JTDI	TIM2_ CH1_ ETR	-	-	I2C1_ SCL	SPI1_ NSS	SPI3_NSS, I2S3_WS	USART2 _RX	-	TIM1_ BKIN	-	-	-	-	EVENT OUT

DocID025186 Rev 2

	/134	Table 1
	4	Port & Pin Name
		PB0
		PB1
QUADENC/		PB2
DBG/TX	Do	PB3
DBG/RX	DocID025186 Rev 2	PB4
	5186 Re	PB5
	2	PB6
		PB7
CAN/RX		PB8
CAN/TX		PB9
		PB10
		PB11

Port													
& Pin Name	AF0	AF1	AF2	AF3	AF4	AF5	AF6	AF7	AF8	AF9	AF10	AF12	AF15
PB0	-	-	TIM3_ CH3	TSC_ G3_IO2	-	-	TIM1_CH2N	-	-	-	-	-	EVENT OUT
PB1	-	-	TIM3_ CH4	TSC_ G3_IO3	-	-	TIM1_CH3N	-	COMP4_ OUT	-	-	-	EVENT OUT
PB2	-	-	-	TSC_ G3_IO4	-	-	-	-	-	-	-	-	EVENT OUT
PB3	JTDO- TRACES WO	TIM2_ CH2	TIM4_ ETR	TSC_ G5_IO1	-	SPI1_ SCK	SPI3_SCK, I2S3_CK	USART2_ TX	-	-	TIM3_ ETR	-	EVENT OUT
PB4	NJTRST	TIM16_ CH1	TIM3_ CH1	TSC_ G5_IO2	-	SPI1_ MISO	SPI3_MISO, I2S3ext_SD	USART2_ RX	-	-	TIM17_ BKIN	-	EVENT OUT
PB5	-	TIM16_ BKIN	TIM3_ CH2		I2C1_ SMBA	SPI1_ MOSI	SPI3_MOSI, I2S3_SD	USART2_ CK	-	-	TIM17_ CH1	-	EVENT OUT
PB6	-	TIM16_ CH1N	TIM4_ CH1	TSC_ G5_IO3	I2C1_SCL	-	-	USART1_ TX	-	-	-	-	EVENT OUT
PB7	-	TIM17_ CH1N	TIM4_ CH2	TSC_ G5_IO4	I2C1_ SDA	-	-	USART1_ RX	-	-	TIM3_ CH4	-	EVENT OUT
PB8	-	TIM16_ CH1	TIM4_ CH3	TSC_ SYNC	I2C1_SCL	-	-	-	COMP1_ OUT	CAN_RX	-	TIM1_ BKIN	EVENT OUT
PB9	-	TIM17_ CH1	TIM4_ CH4		I2C1_ SDA	-	IR_OUT	-	COMP2_ OUT	CAN_TX	-	-	EVENT OUT
PB10	-	TIM2_ CH3	-	TSC_ SYNC	-	-	-	USART3_ TX	-	-	-	-	EVENT OUT
PB11	-	TIM2_ CH4	-	TSC_ G6_IO1	-	-	-	USART3_ RX	-	-	-	-	EVENT OUT
PB12	-		-	TSC_ G6_IO2	I2C2_ SMBA	SPI2_NSS, I2S2_WS	TIM1_ BKIN	USART3_ CK	-	-	-	-	EVENT OUT



Table 13. Alternate functions for port B (continued)

Port & Pin Name	AF0	AF1	AF2	AF3	AF4	AF5	AF6	AF7	AF8	AF9	AF10	AF12	AF15
PB13	-	-	-	TSC_ G6_IO3	-	SPI2_SCK, I2S2_CK	TIM1_ CH1N	USART3_ CTS	-	-	-	-	EVENT OUT
PB14	-	TIM15_ CH1	-	TSC_ G6_IO4	-	SPI2_MISO, I2S2ext_SD	TIM1_ CH2N	USART3_ RTS	-	-	-	-	EVENT OUT
PB15	RTC_ REFIN	TIM15_ CH2	TIM15_ CH1N	-	TIM1_ CH3N	SPI2_MOSI, I2S2_SD	-	-	-	-	-	-	EVENT OUT

Table 14.	Alternate functions for port C
IUDIO I TI	Alternate failetierie for pert e

Port & Pin	AF1	AF2	AF3	AF4	AF5	AF6	A E 7
Name	AFI	AF2	AF3	AF4	AF5	Aro	AF7
PC0	EVENTOUT	=	-	-	-	-	-
PC1	EVENTOUT	=	-	-	-	-	-
PC2	EVENTOUT	-	COMP7_OUT	-	-	-	-
PC3	EVENTOUT	-	-	-	-	TIM1_BKIN2	-
PC4	EVENTOUT	=	-	-	-	-	USART1_TX
PC5	EVENTOUT	-	TSC_G3_IO1	-	-	-	USART1_RX
PC6	EVENTOUT	TIM3_CH1	-	-	-	I2S2_MCK	COMP6_OUT
PC7	EVENTOUT	TIM3_CH2	-	-	-	I2S3_MCK	-
PC8	EVENTOUT	TIM3_CH3	-	-	-	-	-
PC9	EVENTOUT	TIM3_CH4	-	-	I2S_CKIN	-	-
PC10	EVENTOUT	-	-	-	UART4_TX	SPI3_SCK, I2S3_CK	USART3_TX
PC11	EVENTOUT	-	-	-	UART4_RX	SPI3_MISO, I2S3ext_SD	USART3_RX
PC12	EVENTOUT	-	-	-	UART5_TX	SPI3_MOSI, I2S3_SD	USART3_CK
PC13	-	-	-	TIM1_CH1N	-	-	-
PC14	-	-	-	-	-	-	-
PC15	-	=	-	-	-	-	-





Table 15. Alternate functions for port D

Port & Pin Name	AF1	AF2	AF3	AF4	AF5	AF6	AF7
PD0	EVENTOUT	-	-	-	-	-	CAN_RX
PD1	EVENTOUT	-	-	-	-	-	CAN_TX
PD2	EVENTOUT	TIM3_ETR	-	-	UART5_RX	-	-
PD3	EVENTOUT	TIM2_CH1_ETR	-	-	-	-	USART2_CTS
PD4	EVENTOUT	TIM2_CH2	-	-	-	-	USART2_RTS
PD5	EVENTOUT	-	-	-	-	-	USART2_TX
PD6	EVENTOUT	TIM2_CH4	-	-	-	-	USART2_RX
PD7	EVENTOUT	TIM2_CH3	-	-	-	-	USART2_CK
PD8	EVENTOUT	-	-	-	-	-	USART3_TX
PD9	EVENTOUT	-	-	-	-	-	USART3_RX
PD10	EVENTOUT	-	-	-	-	-	USART3_CK
PD11	EVENTOUT	-	-	=	-	-	USART3_CTS
PD12	EVENTOUT	TIM4_CH1	TSC_G8_IO1	=	-	-	USART3_RTS
PD13	EVENTOUT	TIM4_CH2	TSC_G8_IO2	=	-	-	-
PD14	EVENTOUT	TIM4_CH3	TSC_G8_IO3	=	-	-	-
PD15	EVENTOUT	TIM4_CH4	TSC_G8_IO4	-	-	SPI2_NSS	-

Table 16.	Alternate functions for port E

Port & Pin Name	AF0	AF1	AF2	AF3	AF4	AF6	AF7
PE0	-	EVENTOUT	TIM4_ETR	-	TIM16_CH1	-	USART1_TX
PE1	-	EVENTOUT	-	-	TIM17_CH1	-	USART1_RX
PE2	TRACECK	EVENTOUT	TIM3_CH1	TSC_G7_IO1	-	-	-
PE3	TRACED0	EVENTOUT	TIM3_CH2	TSC_G7_IO2	-	-	-
PE4	TRACED1	EVENTOUT	TIM3_CH3	TSC_G7_IO3	-	-	-
PE5	TRACED2	EVENTOUT	TIM3_CH4	TSC_G7_IO4	-	-	-
PE6	TRACED3	EVENTOUT		-	-	-	-
PE7	-	EVENTOUT	TIM1_ETR	-	-	-	-
PE8	-	EVENTOUT	TIM1_CH1N	-	-	-	-
PE9	-	EVENTOUT	TIM1_CH1	-	-	-	-
PE10	-	EVENTOUT	TIM1_CH2N	-	-	-	-
PE11	-	EVENTOUT	TIM1_CH2	-	-	-	-
PE12	-	EVENTOUT	TIM1_CH3N	-	-	-	-
PE13	-	EVENTOUT	TIM1_CH3	-	-	-	-
PE14	=	EVENTOUT	TIM1_CH4	-	-	TIM1_BKIN2	-
PE15	-	EVENTOUT	TIM1_BKIN	-	-	-	USART3_RX





Table 17. Alternate functions for port F

OSC/IN OSC/OUT

145.6 17.	Alternate famoti	one ioi poi i i					
Port & Pin Name	AF1	AF2	AF3	AF4	AF5	AF6	AF7
PF0	-	-	-	I2C2_SDA	-	TIM1_CH3N	-
PF1	-	-	-	I2C2_SCL	-	-	-
PF2	EVENTOUT	-	-	-	-	-	-
PF4	EVENTOUT	COMP1_OUT	-	-	-	-	-
PF6	EVENTOUT	TIM4_CH4	-	I2C2_SCL	-	-	USART3_RTS
PF9	EVENTOUT	-	TIM15_CH1	-	SPI2_SCK	-	-
PF10	EVENTOUT	-	TIM15_CH2	-	SPI2_SCK	-	-