

Savitribai Phule Pune University  
University of Pune

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**TRANSCRIPT**

This is to certify that MR./MS. BHONDAVE ADITYA SUNIL Mother's Name: VAISHALI (SEAT NO. B80073095) has appeared for the BACHELOR OF ENGINEERING (ELECTRONICS & TELECOMMU.) (2008 PATTERN) examination of this University held in MAY, 2014 and is declared to have passed the said examination in HIGHER SECOND CLASS.

He/She has obtained following marks at the said examination (P.R.NO.70925711C)

SUBJECT	MARKS OBTAINED
<b>F.E. SEAT NO. F8070511 MAY, 2011</b>	
ENGINEERING MATHEMATICS I	PP 40/100
APPLIED SCIENCE I	PP 40/100
APPLIED SCIENCE I	TW 20/25
FUNDA.OF PROGRAMMING LANGUAGES	PR 30/50
BASIC ELECTRICAL ENGINEERING	PP 40/100
BASIC ELECTRICAL ENGINEERING	TW 20/25
BASIC CIVIL & ENVIRONMENTAL ENGG.	PP 40/100
BASIC CIVIL & ENVIRONMENTAL ENGG.	TW 20/25
ENGINEERING GRAPHICS I	PP 40/100
MANUFACTURING PRACTICES	TW 18/25
ENGINEERING MATHEMATICS II	PP 45/100
APPLIED SCIENCE II	PP 47/100
APPLIED SCIENCE II	TW 19/25
ENGINEERING MECHANICS	PP 40/100
ENGINEERING MECHANICS	TW 12/25
BASIC ELECTRONICS ENGINEERING	PP 41/100
BASIC ELECTRONICS ENGINEERING	TW 17/25
ENGINEERING GRAPHICS II	TW 35/50
BASIC MECHANICAL ENGINEERING	PP 40/100
BASIC MECHANICAL ENGINEERING	TW 16/25

**GRAND TOTAL: 620/1300**

**RESULT: PASS CLASS.**

**S.E. SEAT NO. S80073159 NOV, 2012**

SIGNAL AND SYSTEMS	PP 44/100
SIGNAL AND SYSTEMS	OR 25/50
SOLID STATES DEVICES AND CIRCUITS	PP 43/100
SOLID STATES DEVICES AND CIRCUITS	PR 25/50
NETWORK ANALYSIS	PP 40/100
DIGITAL LOGIC DESIGN	PP 43/100
DIGITAL LOGIC DESIGN	PR 24/50
POWER DEVICES AND MACHINES	PP 43/100
NETWORK AND POWER LAB.	TW 20/50
ELECTRONIC INSTRUMENTS AND TOOLS	TW 20/50
ENGINEERING MATHEMATICS III	PP 40/100
ENGINEERING MATHEMATICS III	TW 20/25
INTEGRATED CIRCUITS APPLICATIONS	PP 40/100
INTEGRATED CIRCUITS APPLICATIONS	PR 24/50
ELECTROMAGNETIC	PP 54/100
ELECTROMAGNETIC	TW 18/25
DATA STRUCTURES	PP 40/100
DATA STRUCTURES	PR 28/50
COMMUNICATION THEORY	PP 40/100
COMMUNICATION THEORY	OR 21/50
CIRCUIT SIMULATION AND TOOLS	TW 37/50

**GRAND TOTAL: 689/1500**

**RESULT: PASS CLASS.**



*Signature*



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**T.E. SEAT NO. T80073015 NOV, 2013**

CONTROL SYSTEMS	PP	40/100
DIGITAL COMMUNICATION	PP	40/100
DIGITAL COMMUNICATION	PR	28/50
NETWORK SYNTHESIS & FILTER DESIGN	PP	44/100
NETWORK SYNTHESIS & FILTER DESIGN	TW	44/50
MICROCONTROLLERS & APPLICATION	PP	50/100
MICROCONTROLLERS & APPLICATION	PR	22/50
DIGITAL SIGNAL PROCESSING	PP	46/100
DIGITAL SIGNAL PROCESSING	OR	28/50
ELECTRONIC DESIGN PRACTICE	OR	22/50
SIGNAL CODING & ESTIMATION THEORY	PP	45/100
SIGNAL CODING & ESTIMATION THEORY	PR	27/50
SYSTEM PROGRA.& OPERATING SYS.	PP	46/100
SYSTEM PROGRA.& OPERATING SYS.	TW	36/50
COMPUTER ORGANIZATION & ARCHITEC	PP	40/100
INDUSTRIAL MANAGEMENT	PP	53/100
WAVE THEORY & ANTENNA	PP	40/100
WAVE THEORY & ANTENNA	PR	31/50
MINI PROJECT & SEMINAR	OR	24/50
TEST & MEASUREMENT TECHNIQUES	OR	25/50

**GRAND TOTAL: 731/1500**

**RESULT: PASS CLASS.**

**B.E. SEAT NO. B80073095 MAY, 2014**

ELCTRONICS PRODUCT DESIGN	PP	40/100
ELCTRONICS PRODUCT DESIGN	TW	20/25
VLSI DESIGN & TECHNOLOGY	PP	53/100
VLSI DESIGN & TECHNOLOGY	PR	22/50
COMPUTER NETWORK	PP	52/100
COMPUTER NETWORK	OR	38/50
DIGITAL IMAGE PROCESSING	PP	45/100
DIGITAL IMAGE PROCESSING	TW	20/25
DIGITAL IMAGE PROCESSING	PR	40/50
MOBILE COMMUNICATION	PP	41/100
PROJECT (PART-1)	TW	38/50
TELECOMM. & SWITCHING SYSTEM	PP	51/100
TELECOMM. & SWITCHING SYSTEM	OR	37/50
OPTICAL FIBER COMMUNICATION	PP	46/100
OPTICAL FIBER COMMUNICATION	TW	22/25
OPTICAL FIBER COMMUNICATION	PR	35/50
TELEVISION AND VIDEO ENGG	PP	52/100
TELEVISION AND VIDEO ENGG	TW	21/25
TELEVISION AND VIDEO ENGG	PR	35/50
AUTOMOTIVE ELECTRONICS	PP	30#/100
PROJECT II	TW	95/100
PROJECT II	OR	45/50

**GRAND TOTAL: 878/1500**

**RESULT: HIGHER SECOND CLASS #(0.4)**

PP-PAPER, PR-PRACTICAL, TW-TERM WORK, OR-ORAL

AWARD OF CLASS: FIRST CLASS WITH DISTINCTION-66%, FIRST CLASS-60%,  
HIGHER SECOND CLASS-55%, SECOND CLASS-50%

REF.NO.:EXAM./CERTI./

DATE:16/09/2014



FOR CONTROLLER OF EXAMINATION.

*Signature*