# REGISTER TRANSFER AND MICROOPERATIONS

### conte

### Register Transfer Language

- **✓** Register Transfer
- **✓** Bus and Memory Transfers
- **✓** Arithmetic Microoperations
- **✓ Logic Microoperations**
- **✓** Shift Microoperations
- **✓** Arithmetic Logic Shift Unit

# Register Transfer Language (RTL)

### **Digital System**

 An interconnection of hardware modules that do a certain task on the information

### Digital Module

- Registers + Operations performed on the data stored in them
- Modules are interconnected with common data and control paths to form a digital computer system

### RTL cont.

### Microoperations

- operations executed on data stored in one or more registers
- For any function of the computer, a sequence of microoperations is used to describe it

#### The result of the operation may be

- replace the previous binary information of a register or
- transferred to another register

101101110011 Shift Right Operation 010110111001

### RTL cont.

The internal hardware organization of a digital computer is defined by specifying:

- The set of registers it contains and their function
- The sequence of microoperations performed on the binary information stored in the registers
- The control that initiates the sequence of microoperations

Registers + Microoperations Hardware + Control Functions = Digital Computer

### RTL cont.

### Register Transfer Language (RTL)

 A symbolic notation to describe the microoperation transfers among registers

#### Steps:

- Define symbols for various types of microoperations,
- Describe the hardware that implements these microoperations

# Register Transfer (our first microoperation)

Computer registers are designated by capital letters (sometimes followed by numerals) to denote the function of the register

- R1: processor register
- MAR: Memory Address Register (holds an address for a memory unit)
- PC: Program Counter
- IR: Instruction Register
- SR: Status Register

The individual flip-flops in an n-bit register are numbered in sequence from 0 to n-1 (from the right position toward the left position)

R1

7 6 5 4 3 2 1 0

Register R1

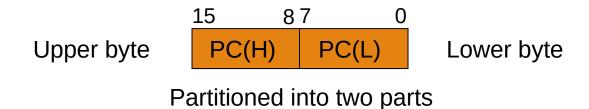
Showing individual bits

A block diagram of a register

Other ways of drawing the block diagram of a register:



Numbering of bits



Information transfer from one register to another is described by a replacement operator:

- ✓ This statement denotes a transfer of the content of register R1 into register R2
- ✓ The transfer happens in one clock cycle
- ✓ The content of the R1 (source) does not change
- ✓ The content of the R2 (destination) will be lost and replaced by the new data transferred from R1

We are assuming that the circuits are available from the outputs of the source register to the inputs of the destination register, and that the destination register has a parallel load capability

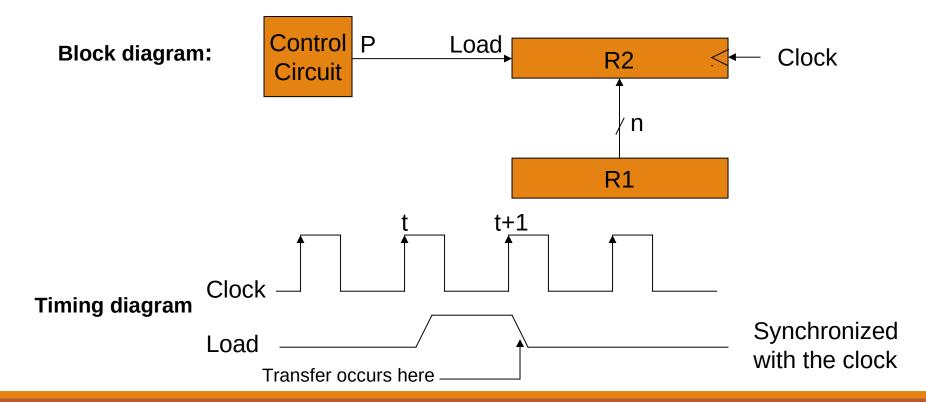
Conditional transfer occurs only under a control condition

Representation of a (conditional) transfer

P: 
$$R2 \leftarrow R1$$

- ✓ A binary condition (P equals to 0 or 1) determines when the transfer occurs
- ✓ The content of R1 is transferred into R2 only if P is 1

Hardware implementation of a controlled transfer:



### Basic Symbols for Register Transfers

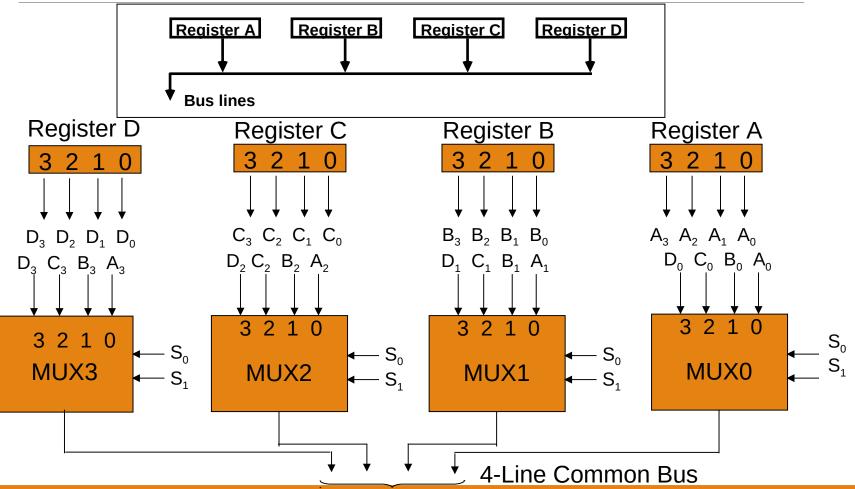
Symbol	Description	Examples
Letters & numerals	Denotes a register	MAR, R2
Parenthesis ( )	Denotes a part of a register	R2(0-7), R2(L)
Arrow ←	Denotes transfer of information	R2 ← R1
Comma ,	Separates two microoperations	R2 ← R1, R1 ← R2

## **Bus and Memory Transfers**

Paths must be provided to transfer information from one register to another

- A <u>Common Bus System</u> is a scheme for transferring information between registers in a multiple-register configuration
- A bus: set of common lines, one for each bit of a register, through which binary information is transferred one at a time
- Control signals determine which register is selected by the bus during each particular register transfer

## **Bus and Memory Transfers**



### **Bus and Memory Transfers**

The transfer of information from a bus into one of many destination registers is done:

- By connecting the bus lines to the inputs of all destination registers and then:
- activating the load control of the particular destination register selected

We write: R2 ← C to symbolize that the content of register C is *loaded into* the register R2 using the common system bus

It is equivalent to: BUS  $\leftarrow$ C, (select C)

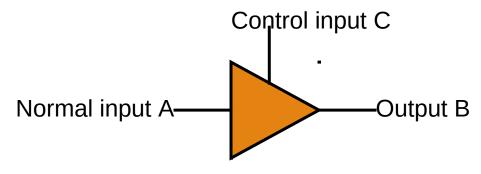
 $R2 \leftarrow BUS \text{ (Load R2)}$ 

### Bus and Memory Transfers: Three State Bus Buffers

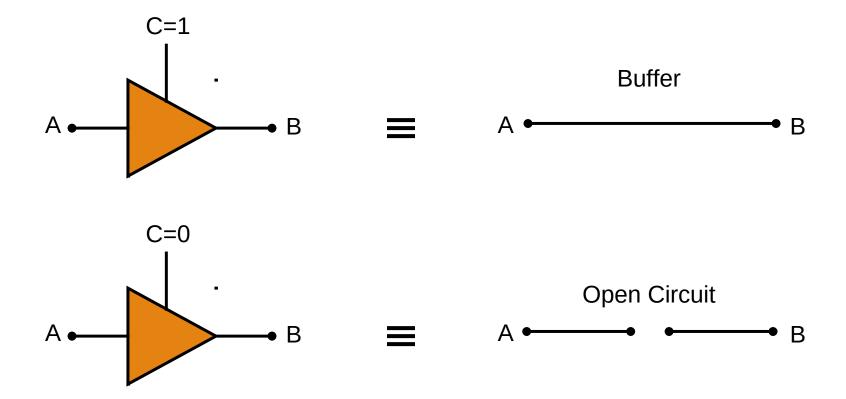
A bus system can be constructed with three-state buffer gates instead of multiplexers

A three-state buffer is a digital circuit that exhibits three states: logic-0, logic-1, and high-impedance (Hi-Z)

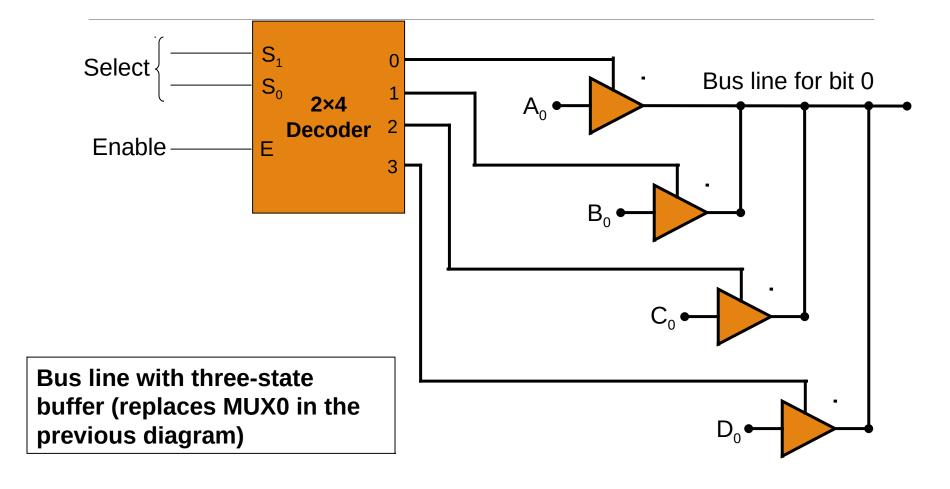
Bus and Memory Transfers: Three State Bus Buffers



# Bus and Memory Transfers: Three-State Bus Buffers



# Bus and Memory Transfers: Three-State Bus Buffers



# Bus and Memory Transfers: Memory Transfer

Memory read: Transfer from memory

Memory write: Transfer to memory

Data being read or wrote is called a memory word (called M)-(refer to section 2-7)

It is necessary to specify the address of M when writing /reading memory

This is done by enclosing the address in square brackets following the letter M

Example: M[0016]: the memory contents at address 0x0016

# Bus and Memory Transfers: Memory Transfer cont.

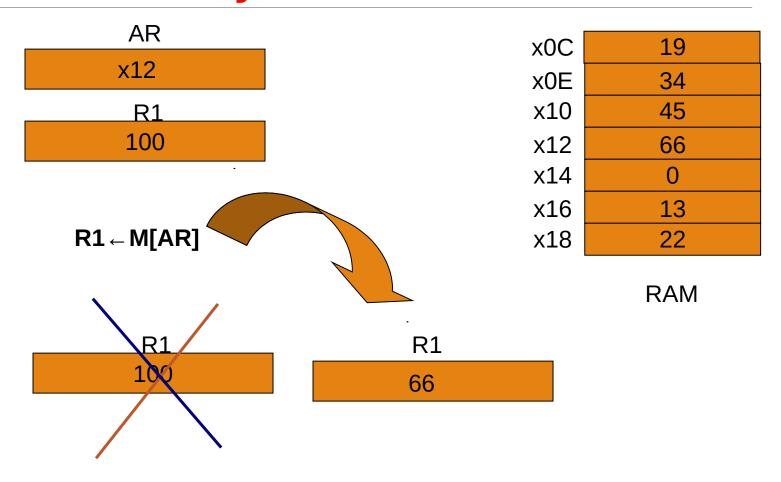
Assume that the address of a memory unit is stored in a register called the Address Register AR

Lets represent a Data Register with DR, then:

Read: DR  $\leftarrow$  M[AR]

Write:  $M[AR] \leftarrow DR$ 

# Bus and Memory Transfers: Memory Transfer cont.



### **Arithmetic Microoperations**

The microoperations most often encountered in digital computers are classified into four categories:

- 1. Register transfer microoperations
- 2. Arithmetic microoperations (on numeric data stored in the registers)
- 3. Logic microoperations (bit manipulations on non-numeric data)
- 4. Shift microoperations

# Arithmetic Microoperations cont.

The basic arithmetic microoperations are: addition, subtraction, increment, decrement, and shift

Addition Microoperation:

Subtraction Microoperation:

$$R3 \leftarrow R1-R2 \text{ or } :$$

1's complement

# Arithmetic Microoperations cont.

One's Complement Microoperation:

Two's Complement Microoperation:

Increment Microoperation:

Decrement Microoperation:

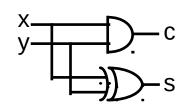
### Half Adder/Full Adder

#### **Half Adder**

Х	У	С	S
0	0	0	0
0	1	00	1
1	0	0	1
1	1	1	0

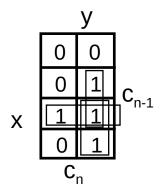
$$c = xy$$

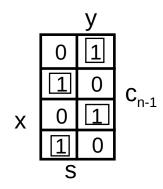
$$s = xy' + x'y$$
$$= x \oplus y$$



#### **Full Adder**

Χ	У	$C_{n-1}$	C <sub>n</sub>	S
0	0	0	0	0
0	0	1	0	1
0	1	0	0	1
0	1	1	1	0
1	0	0	0	1
1	0	1	1	0
1	1	0	1	0
1	1	1	1	1
		· C		

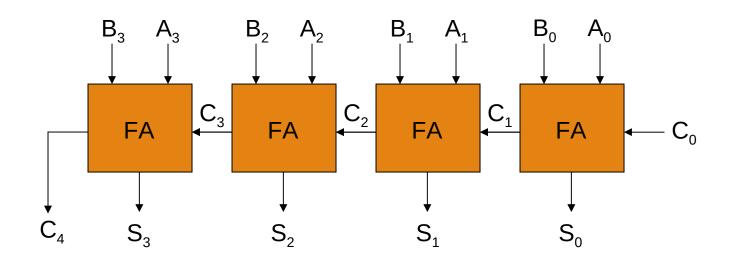




$$c_n = xy + xc_{n-1} + yc_{n-1}$$
  
=  $xy + (x \oplus y)c_{n-1}$ 

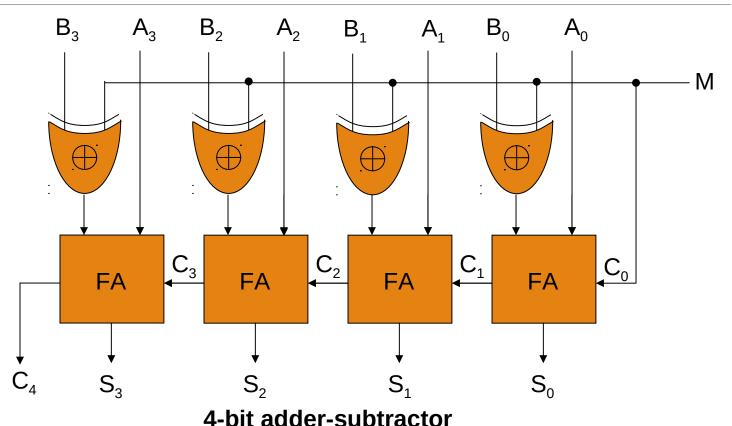
$$S = X'y'C_{n-1} + X'yC'_{n-1} + Xy'C'_{n-1} + XyC_{n-1}$$
  
=  $X \oplus Y \oplus C_{n-1} = (X \oplus Y) \oplus C_{n-1}$ 

## Arithmetic Microoperations Binary Adder



4-bit binary adder (connection of FAs)

# Arithmetic Microoperations Binary Adder-Subtractor



# Arithmetic Microoperations Binary Adder-Subtractor

For unsigned numbers, this gives A - B if  $A \ge B$  or the 2's complement of (B - A) if A < B

(example: 
$$3 - 5 = -2 = 1110$$
)

For signed numbers, the result is A - B provided that there is no overflow. (example : -3 - 5 = -8) 1101

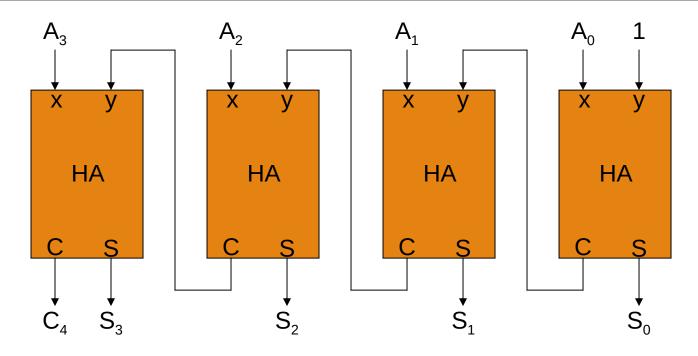
$$C_3$$
 =V 
$$\begin{cases} 1, & \text{if overflow} \\ 0, & \text{if no overflow} \end{cases}$$

**Overflow detector for signed numbers** 

# Arithmetic Microoperations Binary Adder-Subtractor

- What is the range of unsigned numbers that can be represented in 4 bits?
- What is the range of signed numbers that can be represented in 4 bits?
- Repeat for n-bit?!

# Arithmetic Microoperations Binary Incrementer



**4-bit Binary Incrementer** 

## Arithmetic Microoperations Binary Incrementer

Binary Incrementer can also be implemented using a counter

A binary decrementer can be implemented by adding 1111 to the desired register each time!

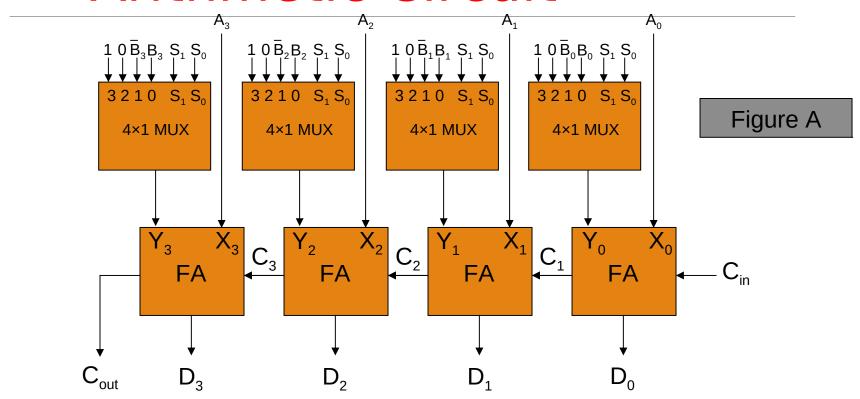
### Arithmetic Microoperations Arithmetic Circuit

This circuit performs seven distinct arithmetic operations and the basic component of it is the parallel adder

The output of the binary adder is calculated from the following arithmetic sum:

$$\circ$$
 D = A + Y + C<sub>in</sub>

### Arithmetic Microoperations Arithmetic Circuit cont.



**4-bit Arithmetic Circuit** 

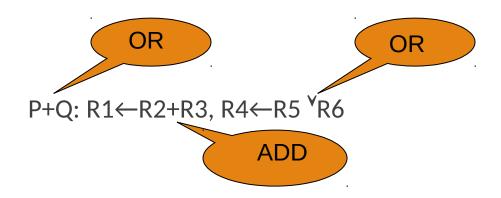
# Logic Microoperations The four basic microoperations

#### **OR Microoperation**

Symbol: <sup>v</sup>, +

Gate:

Example:  $100110_2$  V  $1010110_2$  =  $1110110_2$ 

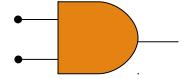


# Logic Microoperations The four basic microoperations cont.

#### **AND Microoperation**

Symbol: ^

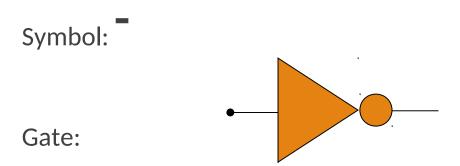
Gate:



Example:  $100110_2$  ^  $1010110_2$  =  $0000110_2$ 

# Logic Microoperations The four basic microoperations cont.

#### **Complement (NOT) Microoperation**



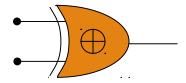
Example:  $1010110_2 = 0101001_2$ 

# Logic Microoperations The four basic microoperations cont.

**XOR (Exclusive-OR) Microoperation** 

Symbol: ⊕

Gate:



Example:  $100110_2 \oplus 1010110_2 = 1110000_2$ 

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#### **Selective-set Operation**

Used to force selected bits of a register into logic-1 by using the OR operation

Example:  $0100_2$  Y  $1000_2$  =  $1100_2$ 

In a processor register

Loaded into a register from memory to perform the selective-set operation

#### **Selective-complement (toggling) Operation**

Used to force selected bits of a register to be complemented by using the XOR operation

Example:  $0001_2 \oplus 1000_2 = 1001_2$ 

In a processor register

Loaded into a register from memory to perform the selective-complement operation

#### **Insert Operation**

Step1: mask the desired bits

Step2: OR them with the desired value

Example: suppose R1 = 0110 1010, and we desire to replace the leftmost 4 bits (0110) with 1001 then:

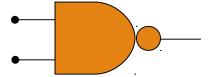
- Step1: 0110 1010 ^ 0000 1111
- ° Step2: 0000 1010 <sup>¥</sup> 1001 0000

 $\rightarrow$  R1 = 1001 1010

#### **NAND Microoperation**

Symbols: ^ and -

Gate:

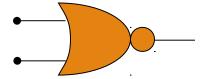


Example:  $100110_2 ^ 1010110_2 = 1111001_2$ 

#### **NOR Microoperation**

Symbols: <sup>v</sup> and

Gate:



Example:  $100110_2$   $^{4}$   $\frac{1010110_2}{10001001_2}$   $\frac{10001001_2}{10001001_2}$ 

#### **Set (Preset) Microoperation**

Force all bits into 1's by ORing them with a value in which all its bits are being assigned to logic-1

Example:  $100110_2$  V  $1111111_2$  =  $1111111_2$ 

#### **Clear (Reset) Microoperation**

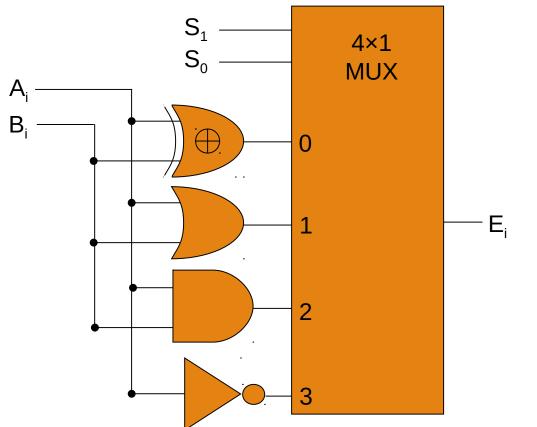
Force all bits into 0's by ANDing them with a value in which all its bits are being assigned to logic-0

Example:  $100110_2$  ^  $000000_2$  =  $000000_2$ 

# Logic Microoperations Hardware Implementation

- The hardware implementation of logic microoperations requires that logic gates be inserted for each bit or pair of bits in the registers to perform the required logic function
- Most computers use only four (AND, OR, XOR, and NOT) from which all others can be derived.

## Logic Microoperations Hardware Implementation cont.



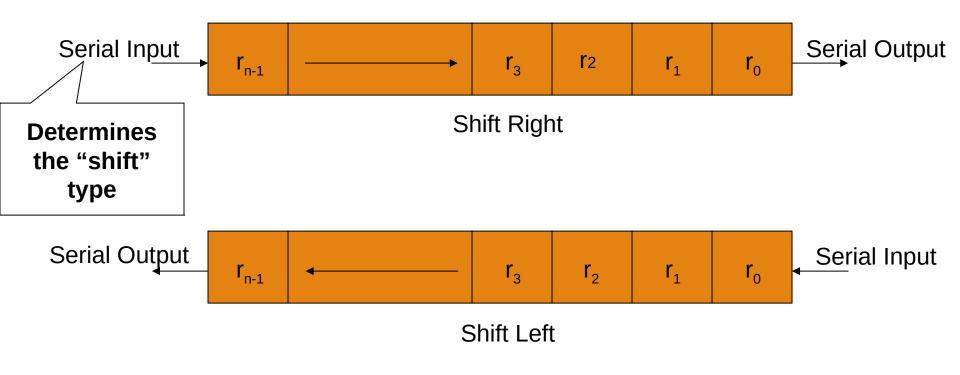
S <sub>1</sub>	S <sub>0</sub>	Output	Operatio n
0	0	E = A ⊕ B	XOR
0	1	E = A <sup>v</sup> B	OR
1	0	E = A ^ B	AND
1	1	E = A	Complem ent

This is for one bit i

### Shift Microoperations

- Used for serial transfer of data
- Also used in conjunction with arithmetic, logic, and other dataprocessing operations
- The contents of the register can be shifted to the left or to the right
- As being shifted, the first flip-flop receives its binary information from the serial input
- Three types of shift: Logical, Circular, and Arithmetic

### Shift Microoperations cont.



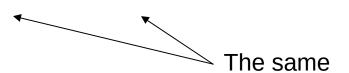
\*\*Note that the bit  $r_i$  is the bit at position (i) of the register

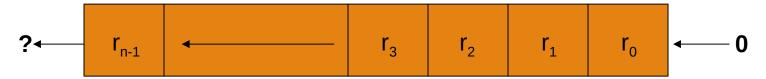
## Shift Microoperations: Logical Shifts

- Transfers 0 through the serial input
- ► Logical Shift Right: R1←shr R1



► Logical Shift Left: R2←shl R2





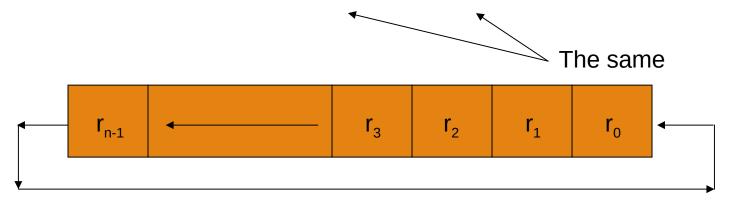
Logical Shift Left

### Shift Microoperations: Circular Shifts

- Circulates the bits of the register around the two ends without loss of information
- Circular Shift Right: R1←cir R1



Circular Shift Left: R2←cil R2

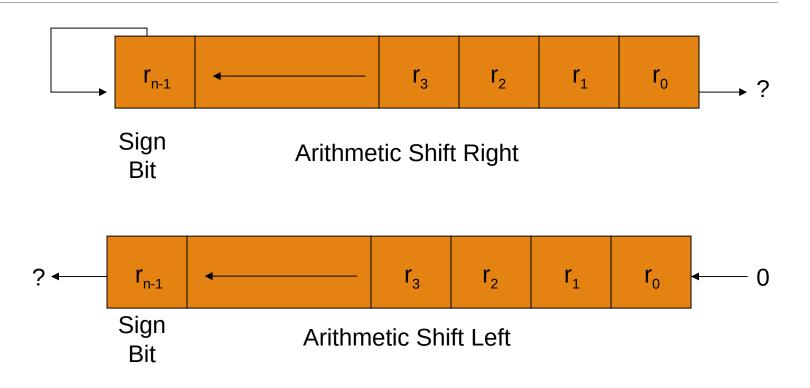


Circular Shift Left

## Shift Microoperations: Arithmetic Shifts

- Shifts a signed binary number to the left or right
- An arithmetic shift-left multiplies a signed binary number by 2: ashl (00100): 01000
- An arithmetic shift-right divides the number by 2 ashr (00100) : 00010
- An overflow may occur in arithmetic shift-left, and occurs when the sign bit is changed (sign reversal)

## Shift Microoperations: Arithmetic Shifts cont.



## Shift Microoperations: Arithmetic Shifts cont.

An overflow flip-flop V<sub>s</sub> can be used to detect an arithmetic shift-left overflow

$$V_s = R_{n-1} \oplus R_{n-2}$$

$$R_{n-1}$$
  $V_s = \begin{cases} 1 \rightarrow \text{ overflow} \\ 0 \rightarrow \text{ no overflow} \end{cases}$ 

### Shift Microoperations cont.

#### Example: Assume R1=11001110, then:

- Arithmetic shift right once : R1 = 11100111
- Arithmetic shift right twice : R1 = 11110011
- Arithmetic shift left once : R1 = 10011100
- Arithmetic shift left twice : R1 = 00111000
- Logical shift right once : R1 = 01100111
- Logical shift left once : R1 = 10011100
- Circular shift right once : R1 = 01100111
- Circular shift left once : R1 = 10011101

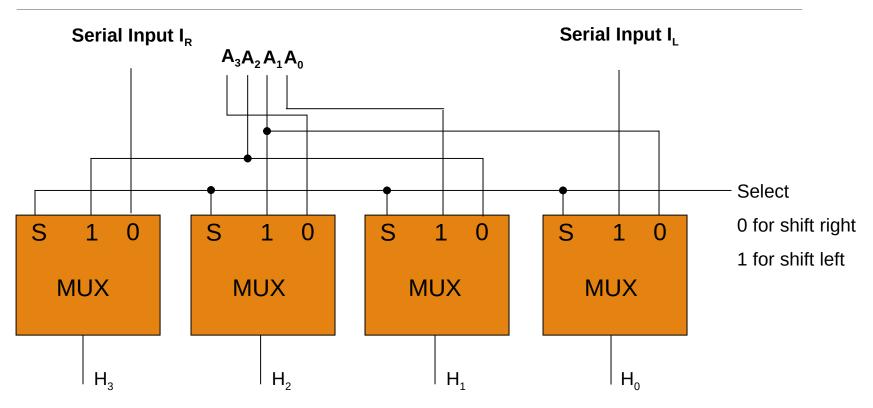
#### Shift Microoperations: Hardware Implementation cont.

A possible choice for a shift unit would be a bidirectional shift register with parallel load (refer to Fig 2-9). Has drawbacks:

- Needs two pulses (the clock and the shift signal pulse)
- Not efficient in a processor unit where multiple number of registers share a common bus

It is more efficient to implement the shift operation with a combinational circuit

## Shift Microoperations Hardware Implementation cont.



4-bit Combinational Circuit Shifter

#### **Arithmetic Logic Shift Unit**

Instead of having individual registers performing the microoperations directly, computer systems employ a number of storage registers connected to a common operational unit called an Arithmetic Logic Unit (ALU)

## Arithmetic Logic Shift Unit cont.

