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SOURABH S. SHENOY

EDUCATION

TEXAS A&M UNIVERSITY, COLLEGE STATION (TAMU)

Master of Science (MS), Computer Science

CGPA: **4.0/4.0**

Fall 2016 – Fall 2018 (Expected)

VISVESVARAYA TECHNOLOGICAL UNIVERSITY (VTU), INDIA

Bachelor of Engineering (BE), Computer Science

CGPA: **9.24/10**

2012 – 2016

INTERNSHIPS / MENTORSHIPS / WORK EXPERIENCE

MICROSOFT MENTORSHIP, MTC

AUGUST 2014 – MARCH 2015

- Implemented various Buffer page replacement policies in PostgreSQL Database as proof of concept
- Gained knowledge on Clock Sweep, LRU, 2Q, MRU, Levenshtein Distance, Boyer-Moore, Hashing, MVCC, Deadlocks, Selectivity modules in Postgres

INTERN, INFOSYS MANGALORE (Catch Them Young 2009)

SUMMER 2010

- Developed a tool that allowed around 5000 Infosys Employees to book/cancel shuttles
- Technologies Used: **HTML5, CSS, JavaScript**, JQuery, MS Access, SQL, JSP

MAJOR PROJECTS

SOURCE CODE CLASSIFIER USING NLPTOOLS

FALL 2016

- Developing an extension to PopClip, a popular Mac app, that can automatically identify the highlighted code snippet (In 13 languages) and execute it online or locally. Languages Used: **PHP, AppleScript**

UTILITIES FOR PORTING TCP EVALUATION SUITE TO NS3

FINAL YEAR PROJECT (2015)

- Developed modules to collect statistics from bottleneck links and plot graphs for the same
- Integrated Tmix and DelayBox modules into NS 3.24 and wrote Test suite and Example script
- Contributed to 2 Google Summer of Code (GSoC) projects. Language Used: **C++**

SWIFT – AN OPTIMIZED STRING MATCHING ALGORITHM FOR NVIDIA GPU

SUMMER 2015

- Work accepted for poster presentation at Student Research Symposium (SRS), co-located with IEEE Conference on HiPC, 2015. API Used: **CUDA, C++, OpenMP, MPI**
- Accepted for full oral presentation at International Symposium on Parallel and Distributed Computing, 2016, held in China.
- Won IEEE TCPP award for “Best Poster”. Also, received scholarship from IEEE HiPC and TEQUIP program of **World Bank** to attend the conference, amounting to around \$1000.

SOME ACHIEVEMENTS

- Awarded \$1000 **National Science Foundation (NSF)** Grant to attend POPL Conference ‘17 at Paris, France.
- Resource Person for Workshop on **CUDA** at Manipal Institute of Technology (MIT), India
- Grader for the graduate course on Software Engineering (CSCE 606) at TAMU, College Station.