

DENNA ÄR KLAR!

V0.3

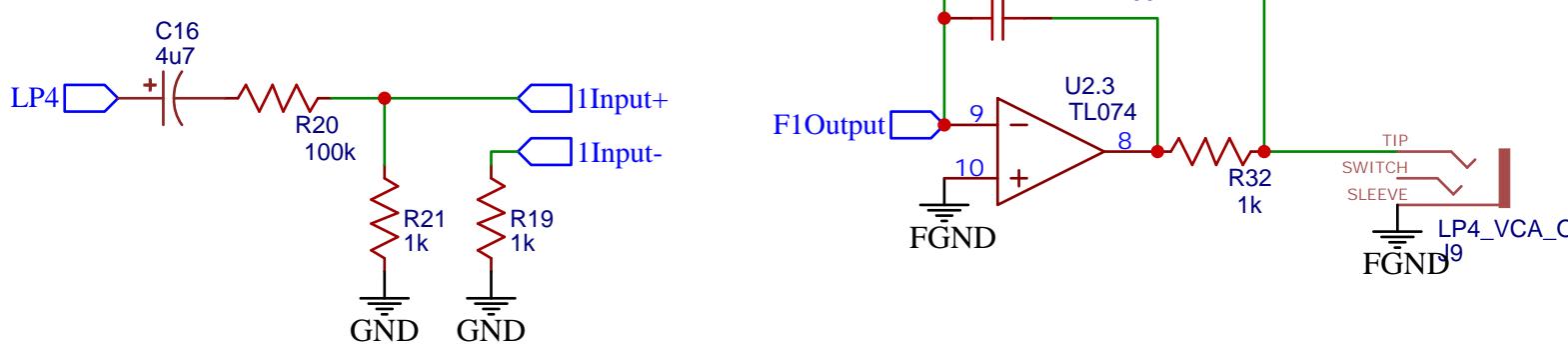
Changed some components and footprints.

InJack connected to Resonance (wasn't before)

Connected support to GND

Solder pads elec caps

U5 LM13700AN	<table border="1"> <tr> <td>I_VCA</td><td>1</td><td>Amp_bias_input</td></tr> <tr> <td></td><td>2</td><td>Diode_bias</td></tr> <tr> <td>1Input+</td><td>3</td><td>Input+</td></tr> <tr> <td>1Input-</td><td>4</td><td>Input-</td></tr> <tr> <td>1Output</td><td>5</td><td>Output</td></tr> <tr> <td></td><td>6</td><td>V-</td></tr> <tr> <td>VEE</td><td>7</td><td>Buffer_input</td></tr> <tr> <td></td><td>8</td><td>Buffer_output</td></tr> </table>	I_VCA	1	Amp_bias_input		2	Diode_bias	1Input+	3	Input+	1Input-	4	Input-	1Output	5	Output		6	V-	VEE	7	Buffer_input		8	Buffer_output	<table border="1"> <tr> <td>Amp_bias_input</td><td>16</td><td>I_RESO</td></tr> <tr> <td>Diode_bias</td><td>15</td><td></td></tr> <tr> <td>Input+</td><td>14</td><td>2Input+</td></tr> <tr> <td>Input-</td><td>13</td><td>2Input-</td></tr> <tr> <td>Output</td><td>12</td><td>2Output</td></tr> <tr> <td>V-</td><td>11</td><td>V+</td></tr> <tr> <td>Buffer_input</td><td>10</td><td>VCC</td></tr> <tr> <td>Buffer_output</td><td>9</td><td></td></tr> </table>	Amp_bias_input	16	I_RESO	Diode_bias	15		Input+	14	2Input+	Input-	13	2Input-	Output	12	2Output	V-	11	V+	Buffer_input	10	VCC	Buffer_output	9	
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EasyEDA	Date: 2025-05-23	Drawn By: Kristian Blåsol

