

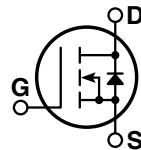
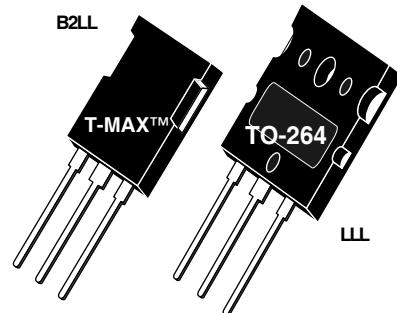


APT8020B2LL(G)
APT8020LLL(G)
800V 38A 0.200Ω

*G Denotes RoHS Compliant. Pb Free Terminal Finish.

POWER MOS 7® MOSFET

Power MOS 7® is a new generation of low loss, high voltage, N-Channel enhancement mode power MOSFETS. Both conduction and switching losses are addressed with Power MOS 7® by significantly lowering $R_{DS(ON)}$ and Q_g . Power MOS 7® combines lower conduction and switching losses along with exceptionally fast switching speeds inherent with APT's patented metal gate structure.



MAXIMUM RATINGS

All Ratings: $T_C = 25^\circ\text{C}$ unless otherwise specified.

Symbol	Parameter	APT8020B2LL_LLL(G)	UNIT
V_{DSS}	Drain-Source Voltage	800	Volts
I_D	Continuous Drain Current @ $T_C = 25^\circ\text{C}$	38	Amps
I_{DM}	Pulsed Drain Current ①	152	
V_{GS}	Gate-Source Voltage Continuous	± 30	Volts
V_{GSM}	Gate-Source Voltage Transient	± 40	
P_D	Total Power Dissipation @ $T_C = 25^\circ\text{C}$	694	Watts
	Linear Derating Factor	5.56	W/ $^\circ\text{C}$
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55 to 150	$^\circ\text{C}$
T_L	Lead Temperature: 0.063" from Case for 10 Sec.	300	
I_{AR}	Avalanche Current ① (Repetitive and Non-Repetitive)	38	Amps
E_{AR}	Repetitive Avalanche Energy ②	50	mJ
E_{AS}	Single Pulse Avalanche Energy ④	3000	

STATIC ELECTRICAL CHARACTERISTICS

Symbol	Characteristic / Test Conditions	MIN	TYP	MAX	UNIT
BV_{DSS}	Drain-Source Breakdown Voltage ($V_{GS} = 0\text{V}$, $I_D = 250\mu\text{A}$)	800			Volts
$R_{DS(on)}$	Drain-Source On-State Resistance ② ($V_{GS} = 10\text{V}$, $I_D = 19\text{A}$)			0.200	Ohms
I_{DSS}	Zero Gate Voltage Drain Current ($V_{DS} = 800\text{V}$, $V_{GS} = 0\text{V}$)			100	μA
	Zero Gate Voltage Drain Current ($V_{DS} = 640\text{V}$, $V_{GS} = 0\text{V}$, $T_C = 125^\circ\text{C}$)			500	
I_{GSS}	Gate-Source Leakage Current ($V_{GS} = \pm 30\text{V}$, $V_{DS} = 0\text{V}$)			± 100	nA
$V_{GS(th)}$	Gate Threshold Voltage ($V_{DS} = V_{GS}$, $I_D = 2.5\text{mA}$)	3		5	Volts

CAUTION: These Devices are Sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.

DYNAMIC CHARACTERISTICS

APT8020B2LL_LLL(G)

Symbol	Characteristic	Test Conditions	MIN	TYP	MAX	UNIT
C_{iss}	Input Capacitance	$V_{GS} = 0V$ $V_{DS} = 25V$ $f = 1\text{ MHz}$		5200		pF
C_{oss}	Output Capacitance			1000		
C_{rss}	Reverse Transfer Capacitance			190		
Q_g	Total Gate Charge ③	$V_{GS} = 10V$ $V_{DD} = 400V$ $I_D = 38A @ 25^\circ C$		195		nC
Q_{gs}	Gate-Source Charge			27		
Q_{gd}	Gate-Drain ("Miller") Charge			130		
$t_{d(on)}$	Turn-on Delay Time	RESISTIVE SWITCHING $V_{GS} = 15V$ $V_{DD} = 400V$ $I_D = 38A @ 25^\circ C$ $R_G = 0.6\Omega$		12		ns
t_r	Rise Time			14		
$t_{d(off)}$	Turn-off Delay Time			39		
t_f	Fall Time			9		
E_{on}	Turn-on Switching Energy ⑥	INDUCTIVE SWITCHING @ 25°C $V_{DD} = 533V, V_{GS} = 15V$ $I_D = 38A, R_G = 5\Omega$		875		μJ
E_{off}	Turn-off Switching Energy			825		
E_{on}	Turn-on Switching Energy ⑥	INDUCTIVE SWITCHING @ 125°C $V_{DD} = 533V, V_{GS} = 15V$ $I_D = 38A, R_G = 5\Omega$		1450		μJ
E_{off}	Turn-off Switching Energy			985		

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS

Symbol	Characteristic / Test Conditions	MIN	TYP	MAX	UNIT
I_S	Continuous Source Current (Body Diode)			38	Amps
I_{SM}	Pulsed Source Current ① (Body Diode)			152	
V_{SD}	Diode Forward Voltage ② ($V_{GS} = 0V, I_S = -38A$)			1.3	Volts
t_{rr}	Reverse Recovery Time ($I_S = -38A, dI_S/dt = 100A/\mu s$)			920	ns
Q_{rr}	Reverse Recovery Charge ($I_S = -38A, dI_S/dt = 100A/\mu s$)			20.7	μC
dv/dt	Peak Diode Recovery dv/dt ⑤			10	V/ns

THERMAL CHARACTERISTICS

Symbol	Characteristic	MIN	TYP	MAX	UNIT
$R_{θJC}$	Junction to Case			0.18	°C/W
$R_{θJA}$	Junction to Ambient			40	

① Repetitive Rating: Pulse width limited by maximum junction temperature

④ Starting $T_j = +25^\circ C$, $L = 4.16mH$, $R_G = 25\Omega$, Peak $I_L = 38A$

② Pulse Test: Pulse width < 380 μs , Duty Cycle < 2%

⑤ dv/dt numbers reflect the limitations of the test circuit rather than the device itself. $I_S \leq -I_D 38A$ $dI_S/dt \leq 700A/\mu s$ $V_R \leq 800$ $T_j \leq 150^\circ C$

③ See MIL-STD-750 Method 3471

⑥ Eon includes diode reverse recovery. See figures 18, 20.

APT Reserves the right to change, without notice, the specifications and information contained herein.

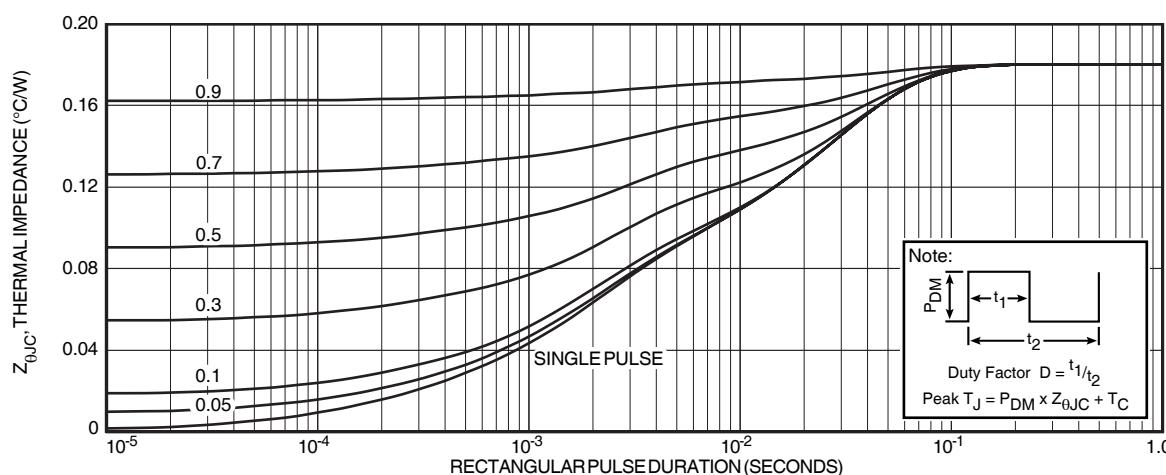


FIGURE 1, MAXIMUM EFFECTIVE TRANSIENT THERMAL IMPEDANCE, JUNCTION-TO-CASE vs PULSE DURATION

Typical Performance Curves

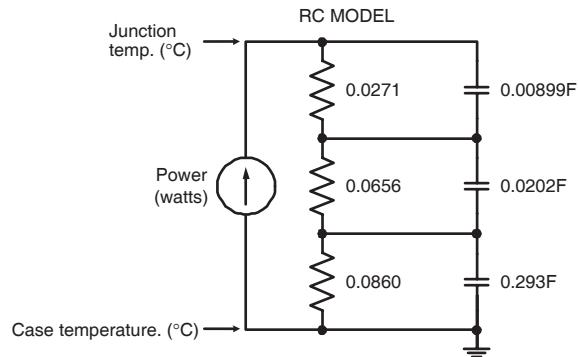


FIGURE 2, TRANSIENT THERMAL IMPEDANCE MODEL

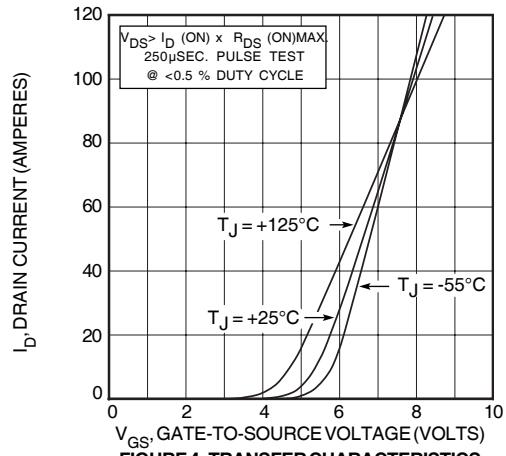


FIGURE 4, TRANSFER CHARACTERISTICS

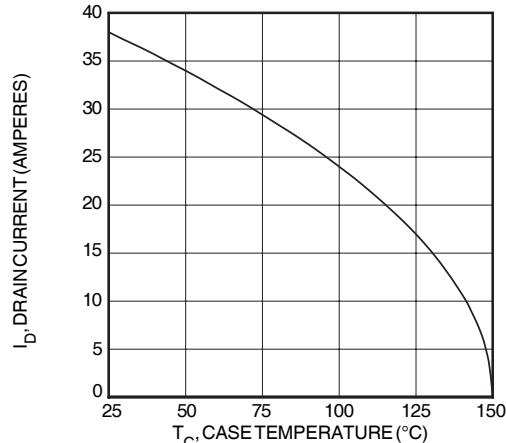


FIGURE 6, MAXIMUM DRAIN CURRENT vs CASE TEMPERATURE

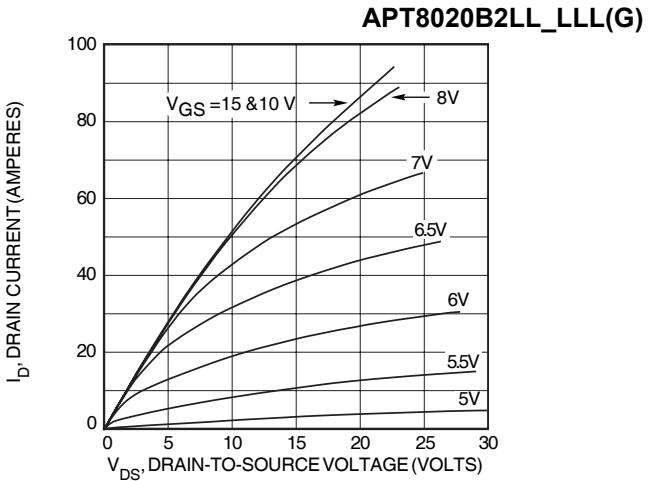
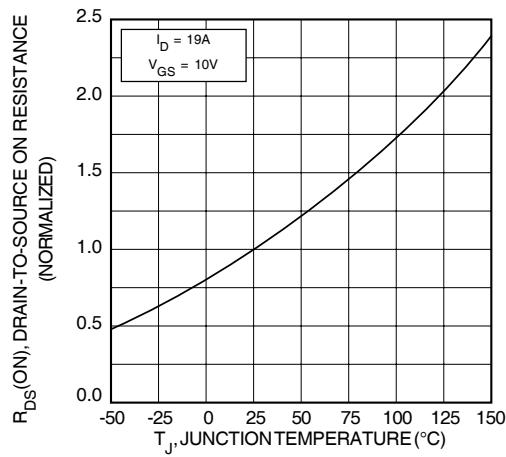


FIGURE 3, LOW VOLTAGE OUTPUT CHARACTERISTICS

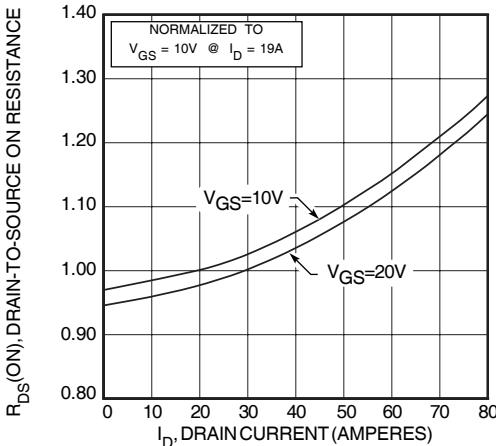


FIGURE 5, R_DS(ON) vs DRAIN CURRENT

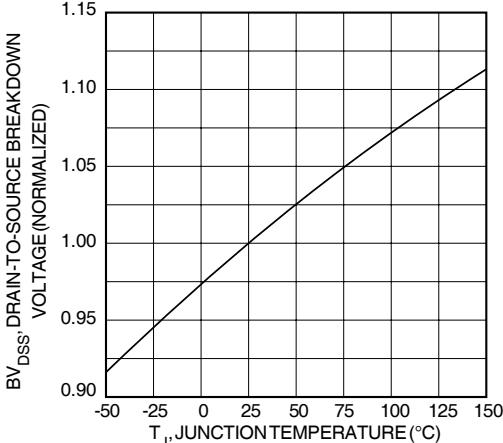
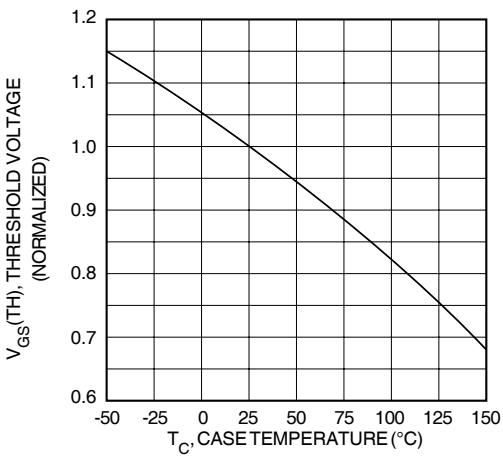
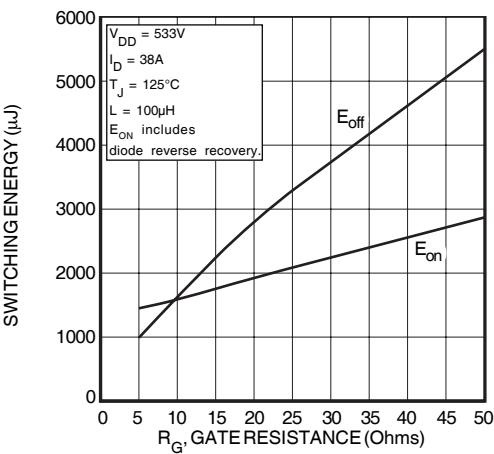
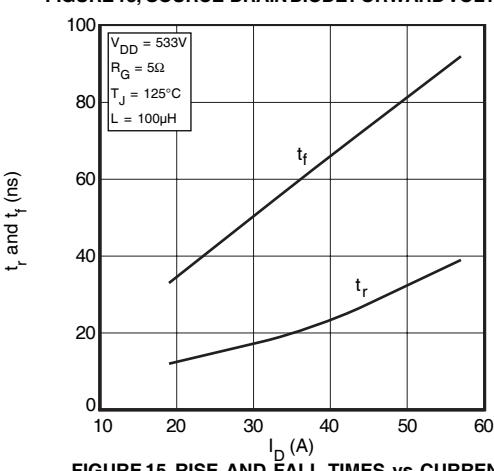
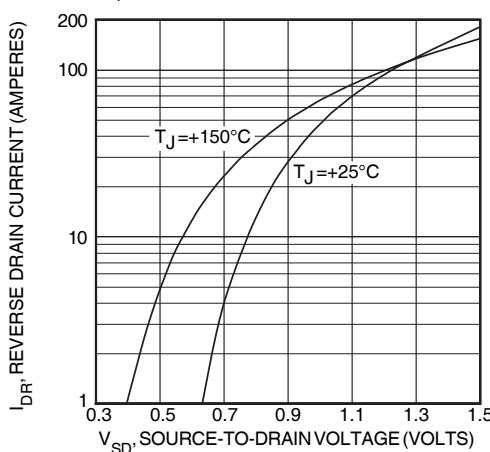
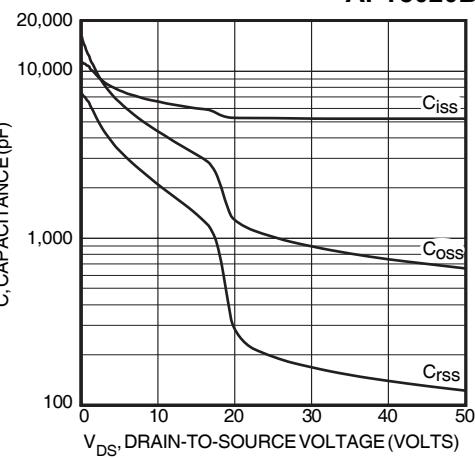
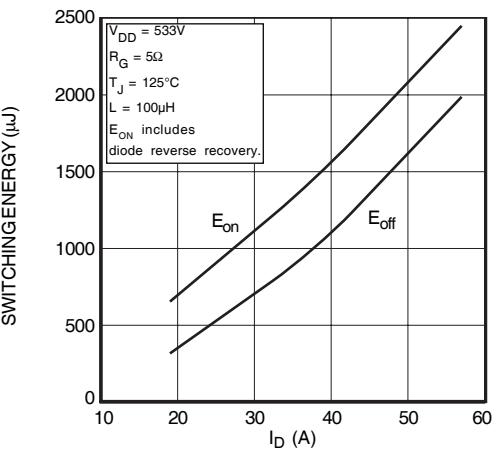
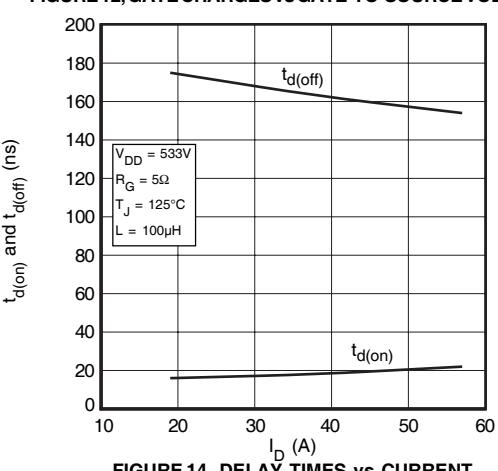
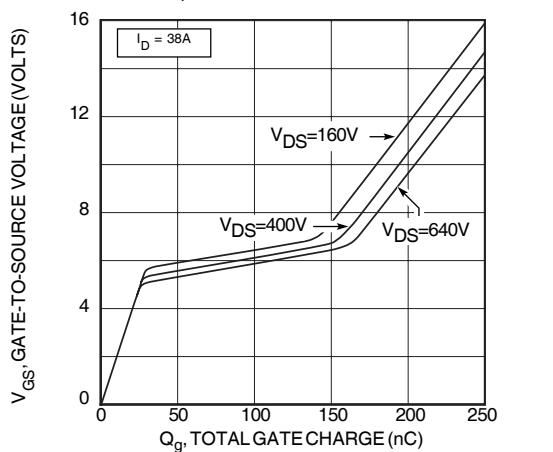
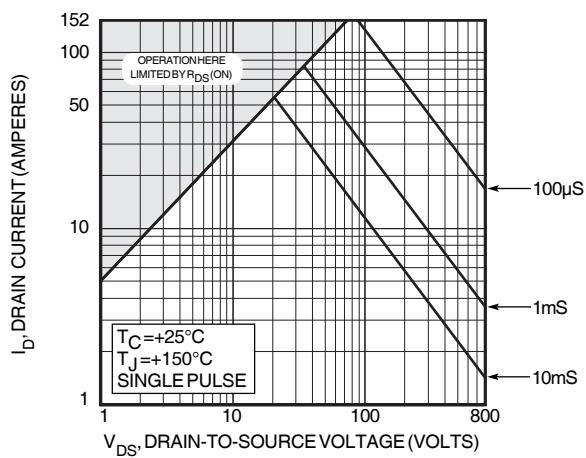


FIGURE 7, BREAKDOWNVOLTAGEvs TEMPERATURE



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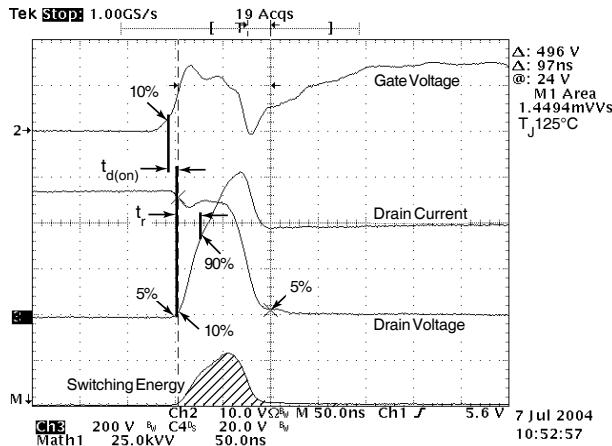


Figure 18, Turn-on Switching Waveforms and Definitions

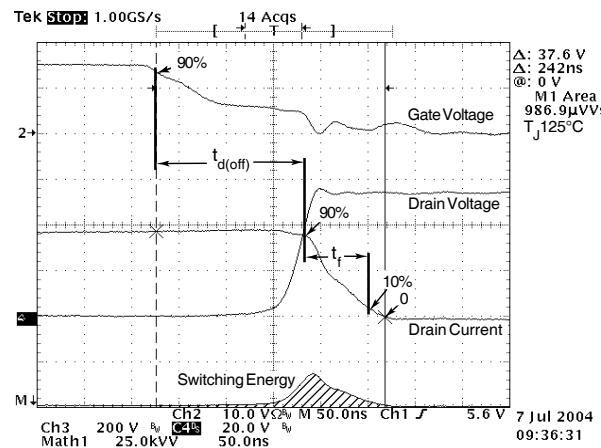


Figure 19, Turn-off Switching Waveforms and Definitions

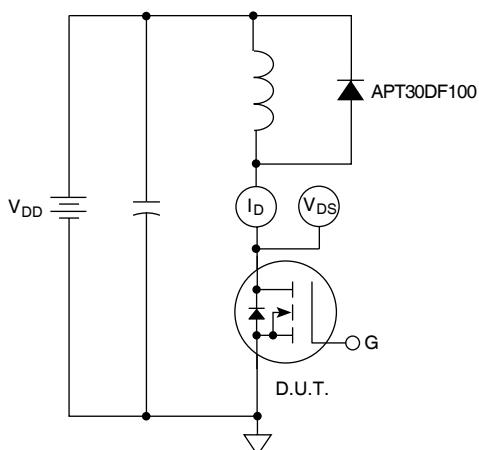
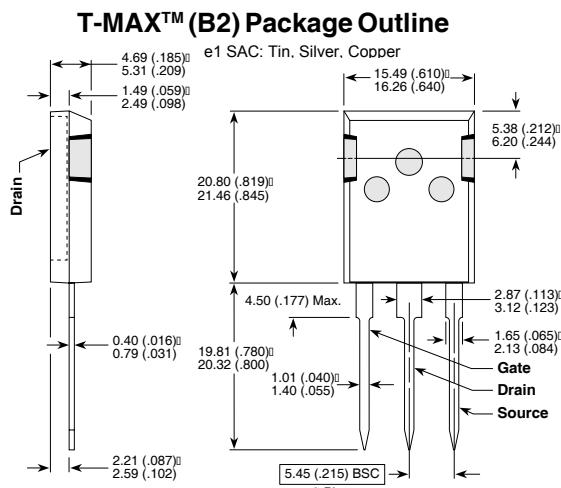
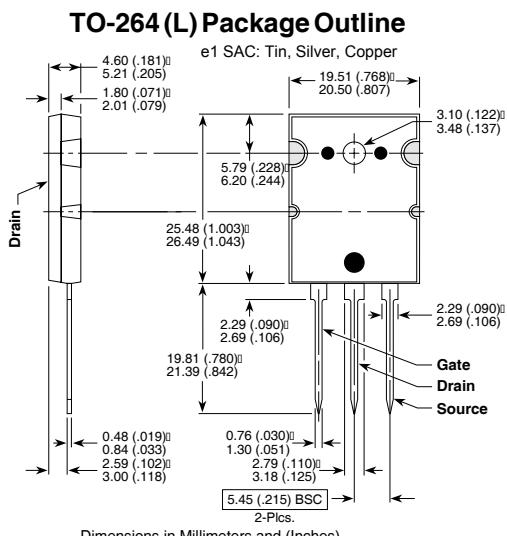


Figure 20, Inductive Switching Test Circuit



These dimensions are equal to the TO-247 without the mounting hole.

Dimensions in Millimeters and (Inches)



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