

# Lonten N-channel 650V, 78A, 0.041Ω LonFET™ Power MOSFET

## Description

LonFET™ Power MOSFET is fabricated using advanced super junction technology. The resulting device has extremely low on resistance, making it especially suitable for applications which require superior power density and outstanding efficiency.

## Features

- ◆ Ultra low  $R_{DS(on)}$
- ◆ Ultra low gate charge (typ.  $Q_g = 110\text{nC}$ )
- ◆ 100% UIS tested
- ◆ RoHS compliant

## Applications

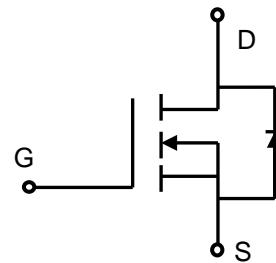
- ◆ Power factor correction (PFC).
- ◆ Switched mode power supplies (SMPS).
- ◆ Uninterruptible power supply (UPS).

## Product Summary

$V_{DS} @ T_{j,max}$	700V
$R_{DS(on),max}$	0.041Ω
$I_{DM}$	230A
$Q_{g,typ}$	110nC



TO-247



N-Channel MOSFET



## Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DSS}$	650	V
Continuous drain current ( $T_C = 25^\circ\text{C}$ )	$I_D$	78	A
( $T_C = 100^\circ\text{C}$ )		46	A
Pulsed drain current <sup>1)</sup>	$I_{DM}$	230	A
Gate-Source voltage	$V_{GSS}$	$\pm 30$	V
Avalanche energy, single pulse <sup>2)</sup>	$E_{AS}$	2350	mJ
Power Dissipation TO-247 ( $T_C = 25^\circ\text{C}$ )	$P_D$	500	W
- Derate above $25^\circ\text{C}$		4.0	W/ $^\circ\text{C}$
Operating and Storage Temperature Range	$T_J, T_{STG}$	-55 to +150	$^\circ\text{C}$
Continuous diode forward current	$I_S$	78	A
Diode pulse current	$I_{S,pulse}$	230	A

## Thermal Characteristics TO-247

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	0.25	$^\circ\text{C/W}$
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	62	$^\circ\text{C/W}$
Soldering temperature, wavesoldering only allowed at leads. (1.6mm from case for 10s)	$T_{sold}$	260	$^\circ\text{C}$

## Package Marking and Ordering Information

Device	Device Package	Marking	Units/Tube	Units/Reel
LSB65R041GF	TO-247	LSB65R041GF	30	

## Electrical Characteristics

$T_c = 25^\circ\text{C}$  unless otherwise noted

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
<b>Static characteristics</b>						
Drain-source breakdown voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}}=0 \text{ V}, I_{\text{D}}=0.25 \text{ mA}$	650	-	-	V
Gate threshold voltage	$V_{\text{GS(th)}}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=0.25 \text{ mA}$	2.0	3.0	4.0	V
Drain cut-off current	$I_{\text{DSS}}$	$V_{\text{DS}}=650 \text{ V}, V_{\text{GS}}=0 \text{ V},$ $T_j = 25^\circ\text{C}$ $T_j = 125^\circ\text{C}$	-	-	1	$\mu\text{A}$
Gate leakage current, Forward	$I_{\text{GSSF}}$	$V_{\text{GS}}=30 \text{ V}, V_{\text{DS}}=0 \text{ V}$	-	-	100	nA
Gate leakage current, Reverse	$I_{\text{GSSR}}$	$V_{\text{GS}}=-30 \text{ V}, V_{\text{DS}}=0 \text{ V}$	-	-	-100	nA
Drain-source on-state resistance	$R_{\text{DS(on)}}$	$V_{\text{GS}}=10 \text{ V}, I_{\text{D}}=39 \text{ A}$ $T_j = 25^\circ\text{C}$ $T_j = 150^\circ\text{C}$	-	0.036 0.094	0.041	$\Omega$
<b>Dynamic characteristics</b>						
Input capacitance	$C_{\text{iss}}$	$V_{\text{DS}} = 25 \text{ V}, V_{\text{GS}} = 0 \text{ V},$ $f = 1 \text{ MHz}$	-	6000	-	pF
Output capacitance	$C_{\text{oss}}$		-	4800	-	
Reverse transfer capacitance	$C_{\text{rss}}$		-	35	-	
Turn-on delay time	$t_{\text{d(on)}}$	$V_{\text{DD}} = 400 \text{ V}, I_{\text{D}} = 39 \text{ A}$ $R_G = 10 \Omega, V_{\text{GS}}=10 \text{ V}$	-	46	-	ns
Rise time	$t_r$		-	52	-	
Turn-off delay time	$t_{\text{d(off)}}$		-	342	-	
Fall time	$t_f$		-	8.6	-	
<b>Gate charge characteristics</b>						
Gate to source charge	$Q_{\text{gs}}$	$V_{\text{DD}}=400 \text{ V}, I_{\text{D}}=39 \text{ A},$ $V_{\text{GS}}=0 \text{ to } 10 \text{ V}$	-	25.7	-	nC
Gate to drain charge	$Q_{\text{gd}}$		-	42.2	-	
Gate charge total	$Q_g$		-	110	-	
Gate plateau voltage	$V_{\text{plateau}}$		-	6.0	-	V
<b>Reverse diode characteristics</b>						
Diode forward voltage	$V_{\text{SD}}$	$V_{\text{GS}}=0 \text{ V}, I_{\text{F}}=39 \text{ A}$	-	-	1.2	V
Reverse recovery time	$t_{\text{rr}}$	$V_R=50 \text{ V}, I_{\text{F}}=39 \text{ A},$ $dI_{\text{F}}/dt=100 \text{ A}/\mu\text{s}$	-	200	-	ns
Reverse recovery charge	$Q_{\text{rr}}$		-	1.9	-	$\mu\text{C}$
Peak reverse recovery current	$I_{\text{rrm}}$		-	18.3	-	A

### Notes:

- Limited by maximum junction temperature, maximum duty cycle is 0.75.
- $I_{\text{AS}} = 10 \text{ A}, V_{\text{DD}} = 60 \text{ V}$ , Starting  $T_j = 25^\circ\text{C}$ .

## Electrical Characteristics Diagrams

Figure 1. On-Region Characteristics

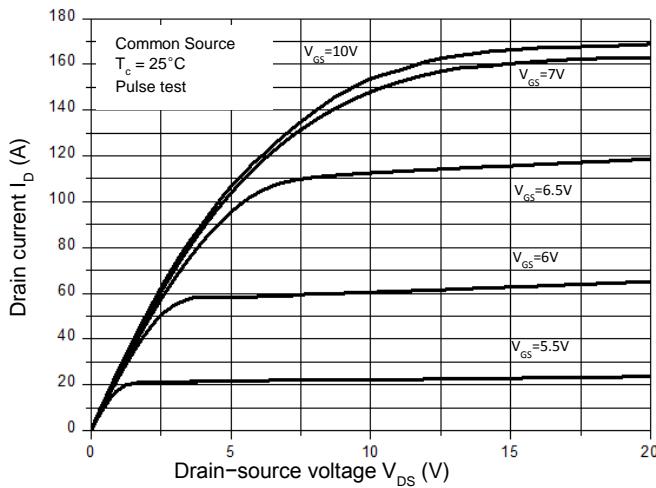


Figure 2. Transfer Characteristics

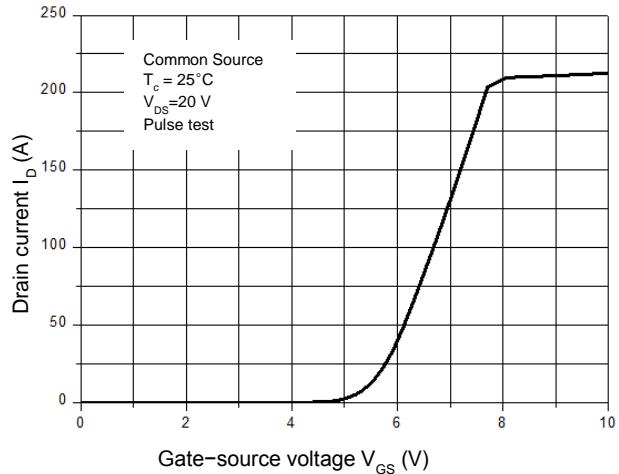


Figure 3. On-Resistance Variation vs. Drain Current

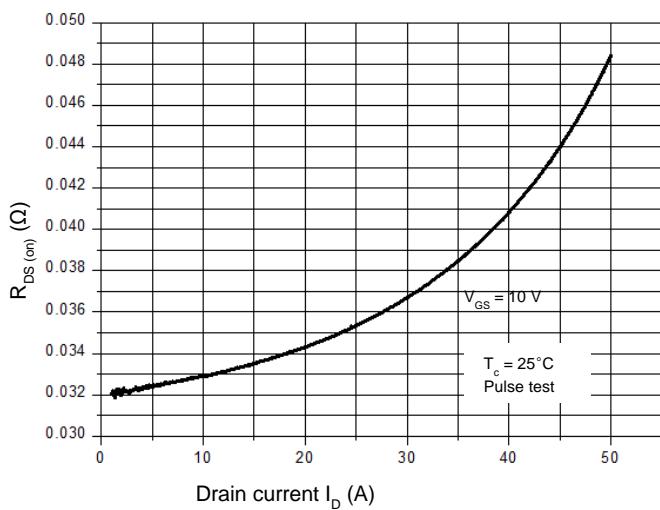


Figure 4. Threshold Voltage vs. Temperature

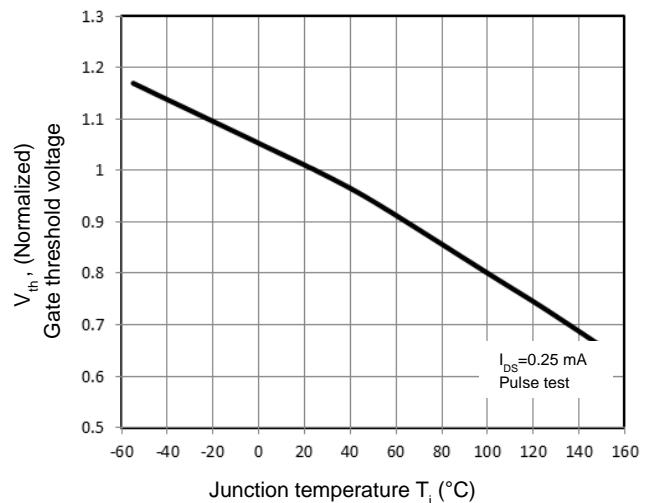


Figure 5. Breakdown Voltage vs. Temperature

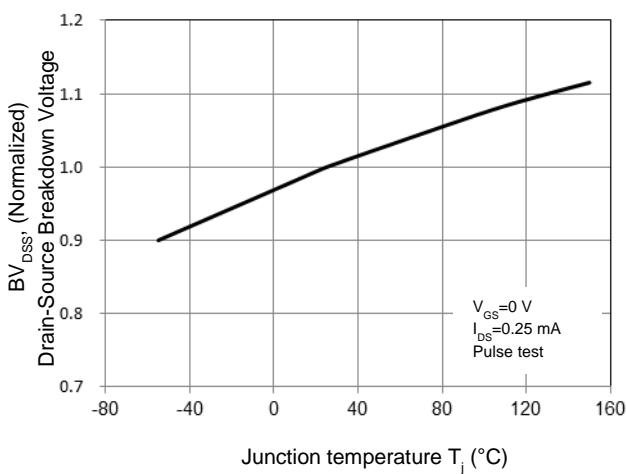


Figure 6. On-Resistance vs. Temperature

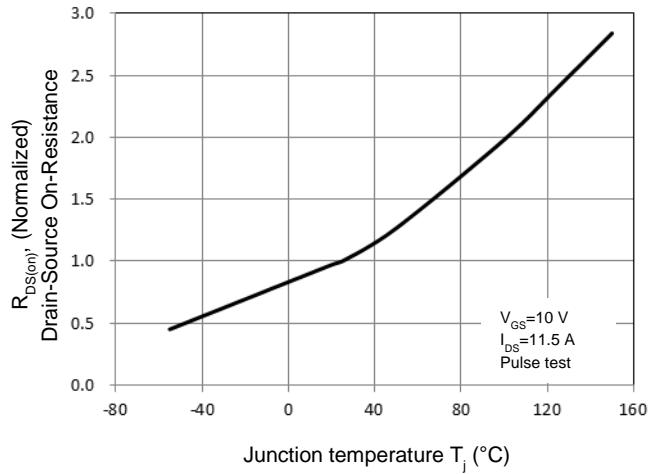


Figure 7. Capacitance Characteristics

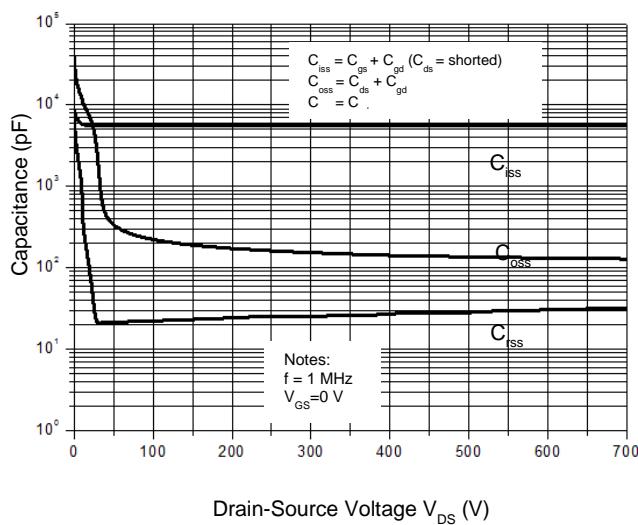


Figure 8. Gate Charge Characterist

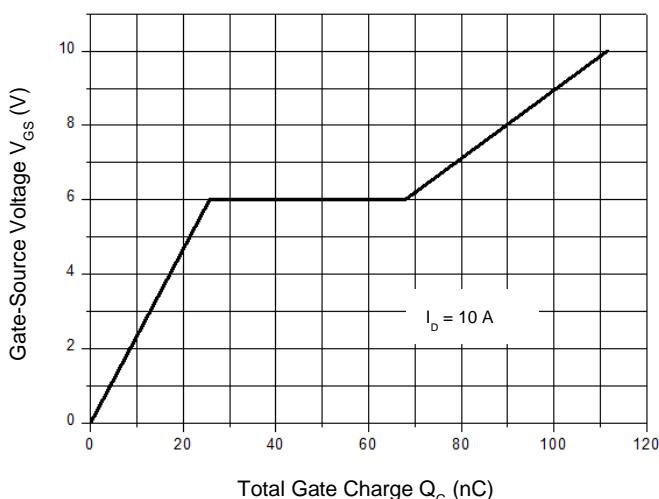


Figure 9 Maximum Safe Operating Area

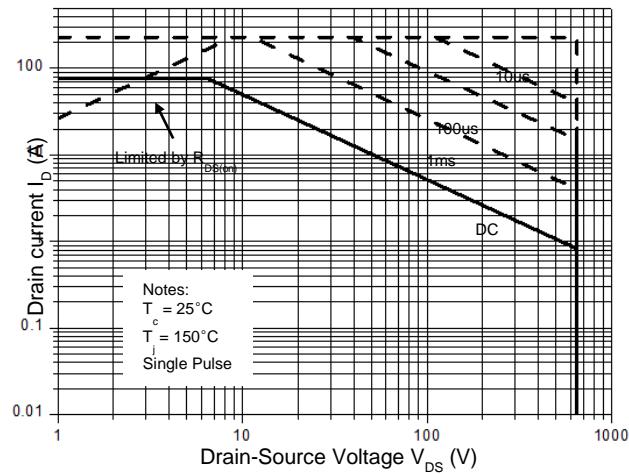
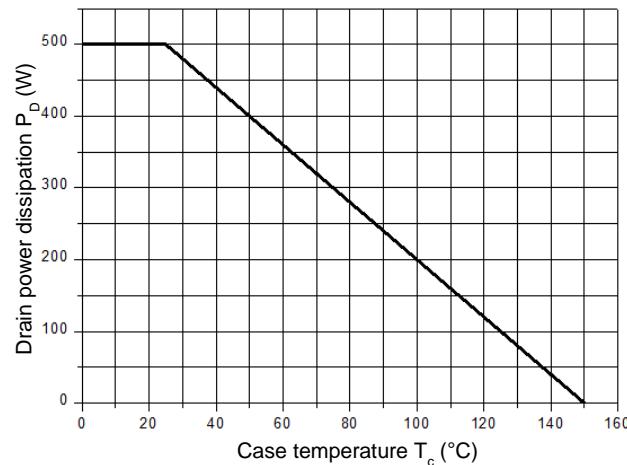
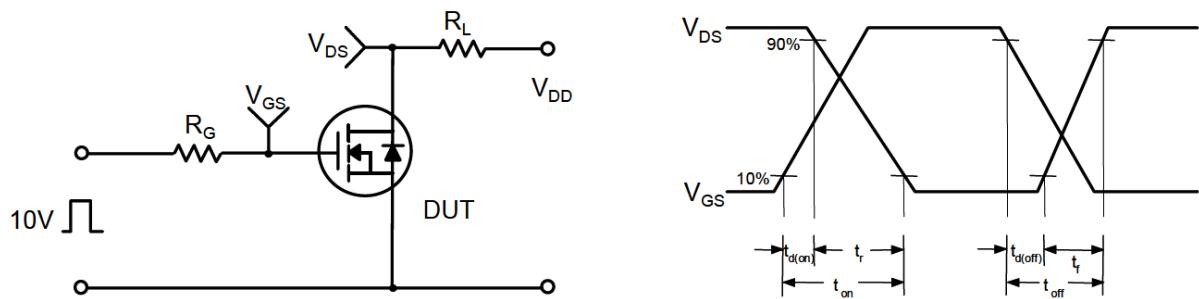
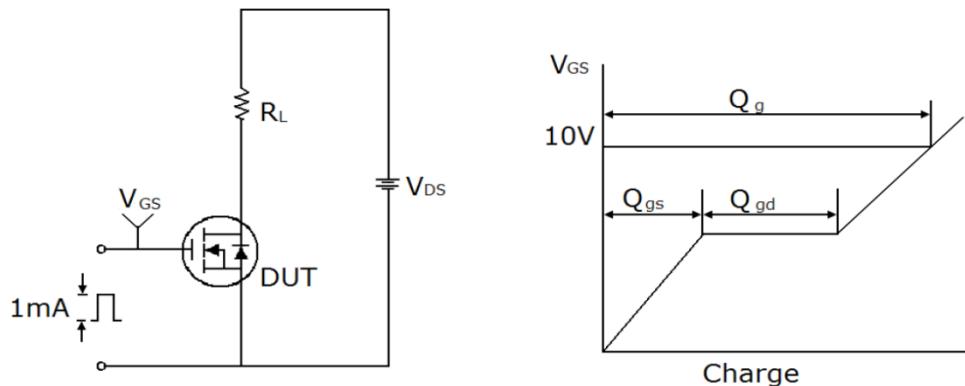


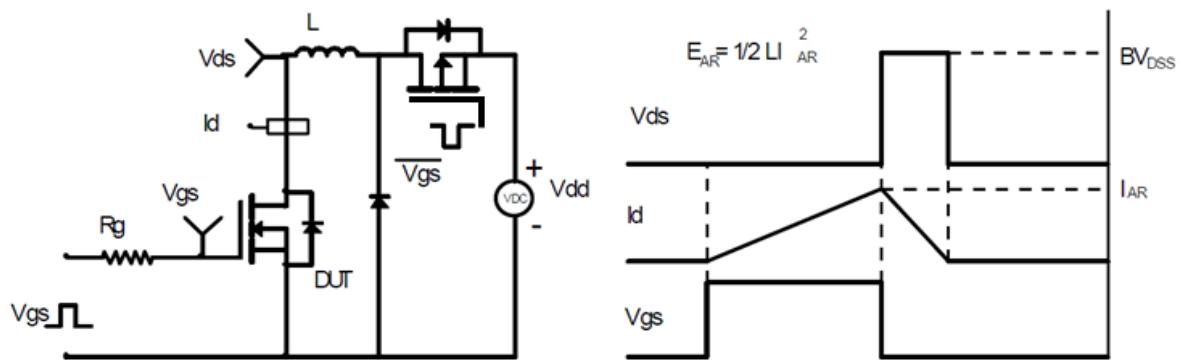
Figure 10 Power Dissipation vs. Temperature



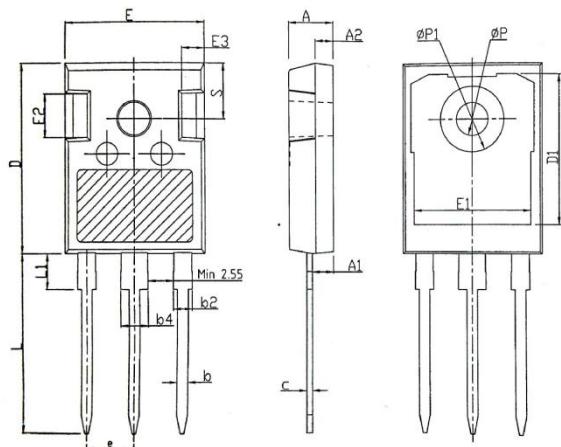
### Gate Charge Test Circuit & Waveform



### Unclamped Inductive Switching Test Circuit & Waveforms

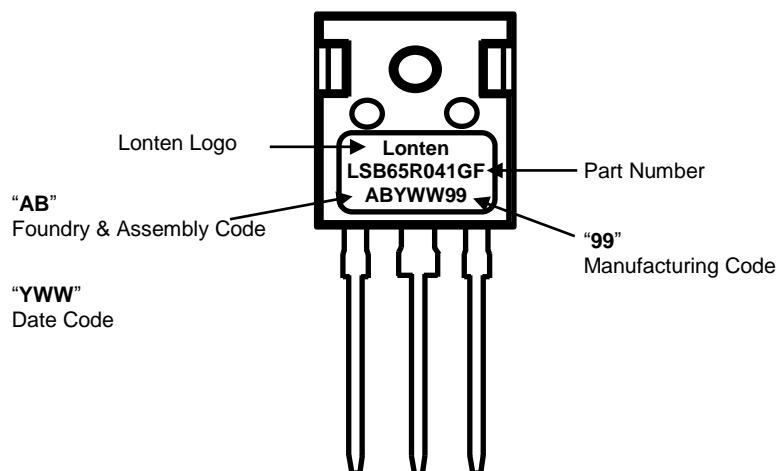


### Mechanical Dimensions for TO-247



SYMBOL	mm		
	MIN	NOM	MAX
A	4.80	5.00	5.20
A1	2.21	2.41	2.59
A2	1.85	2.00	2.15
b	1.11	1.21	1.36
b2	1.91	2.01	2.21
b4	2.91	3.01	3.21
c	0.51	0.61	0.75
D	20.80	21.00	21.30
D1	16.25	16.55	16.85
E	15.50	15.80	16.10
E1	13.00	13.30	13.60
E2	4.80	5.00	5.20
E3	2.30	2.50	2.70
e	5.44BSC		
L	19.82	19.92	20.22
L1	—	—	4.30
ØP	3.40	3.60	3.80
ØP1	—	—	7.30
S	6.15BSC		

### TO-247 Part Marking Information



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