

Department of Electrical & Electrical Engineering
BRAC University
Semester- Fall 2025



EEE205L

ELECTRONIC CIRCUITS I LABORATORY

Section: 01

Experiment:7 - BJT AMPLIFIER

Group Number: 1

Group Members:

SL	Student ID	Name
01.	24121204	Abir Chowdhury Ratul
02.	24121076	Tashin Ahmed Sakib
03.	24121308	Alif Tamjid
04.	24121205	Souvik Barman Ratul
05.	24121058	Muhammad Mushfiqur Rahman

Submitted by :

Submitted To : Aldrin Nippon Bobby

Name : Abir Chowdhury Ratul

Aldrin Nippon Bobby

ID : 24121204

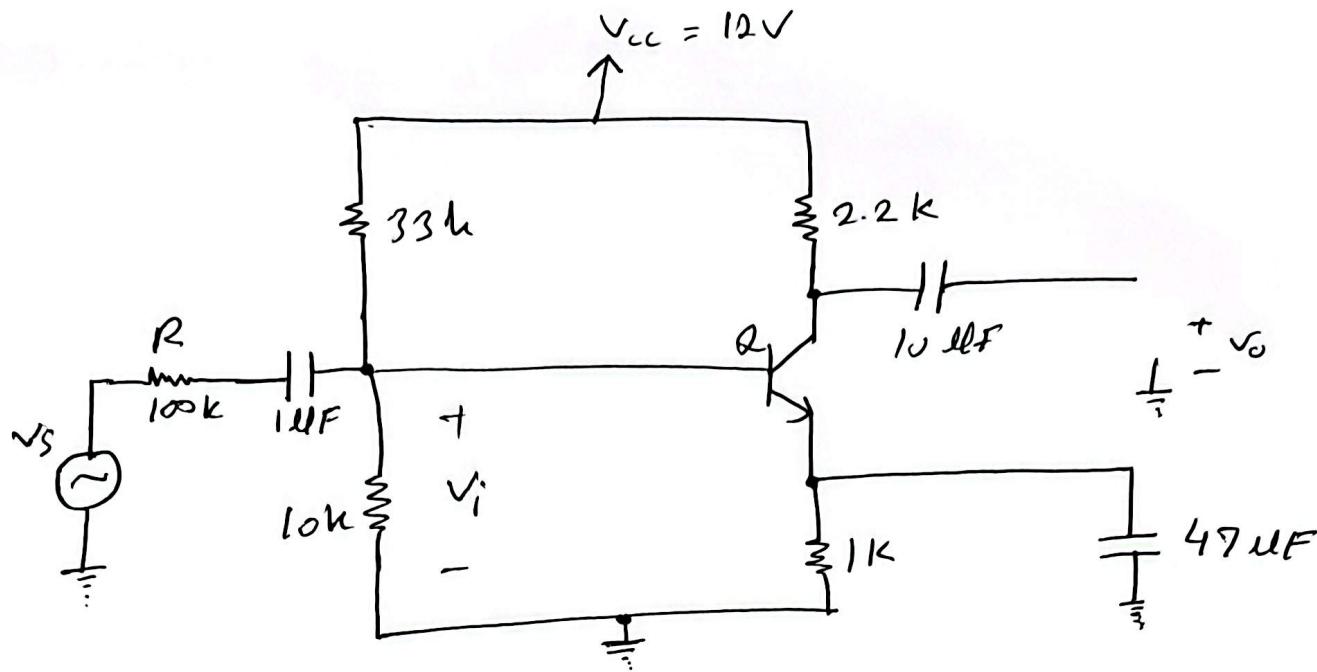
Objective:

To study the common emitter amplifier and measure its gain,

input impedance and output impedance.

Equipments:

1. Breadboard
2. Jumper wires
3. Multimeter
4. DC voltage source
5. Resistors (1K, 2.2 K, 10K, 33K Ω , 100k)
6. 100k Potentiometer
7. n-p-n Transistor (C828)
8. Capacitors (1 μ F, 10 μ F, 47 μ F)



1. Amplification :

$$v_i = \boxed{15.2} \text{ mV} \quad v_o = \boxed{2040} \text{ mV}$$

2. Input Impedance, R_i :

$$v_i = \boxed{15.2} \text{ mV} \quad v_s = \boxed{500} \text{ mV}$$

$$R_i = \boxed{100} \text{ k}\Omega$$

$$R_i = \boxed{3.14} \text{ k}\Omega$$

3. Output Impedance, R_o :

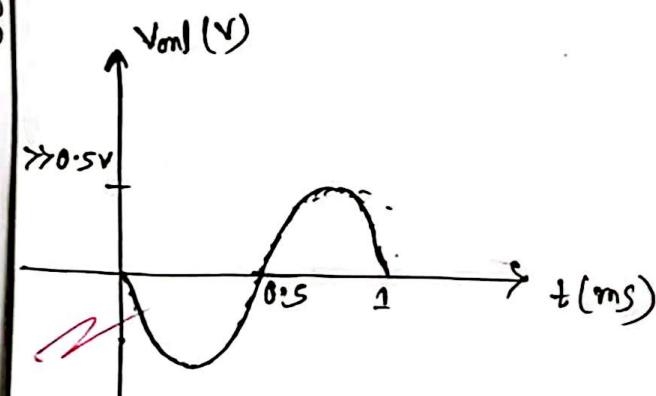
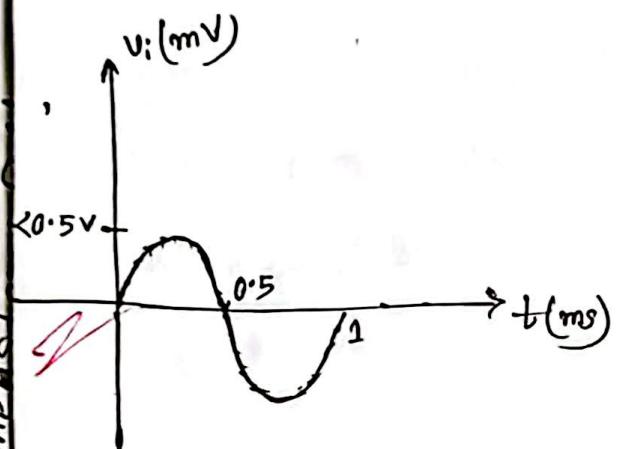
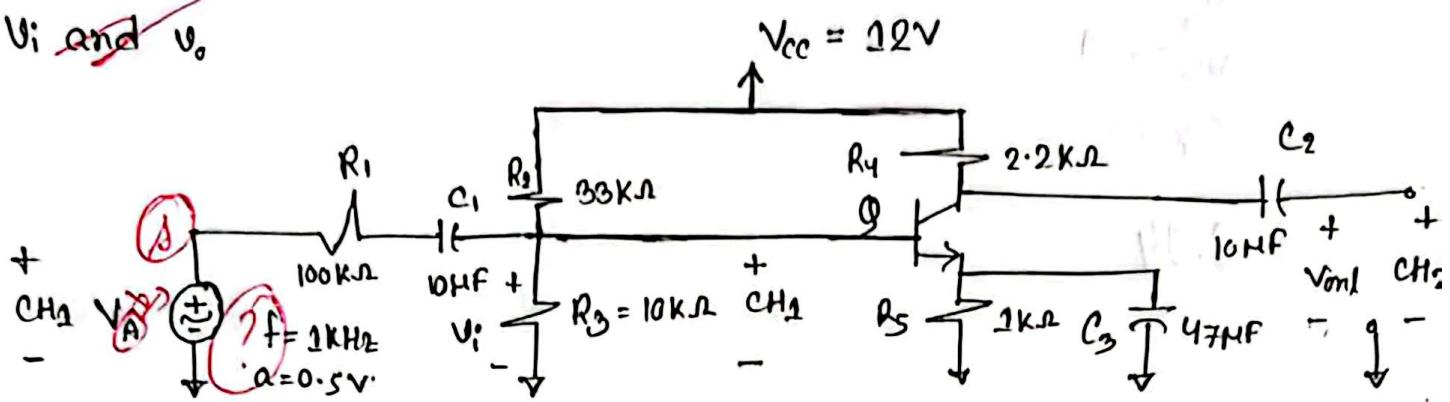
$$R_o = \boxed{2.18} \text{ k}\Omega$$

Experiment: 07

Common Emitter Amplifier

Group 1

V_i and V_o



R_2 ($k\Omega$)	$V_{S(\alpha)}$ (V)	V_{in} (mV)	V_{out} (V)	$A_v = \frac{V_{out}}{V_{in}}$
100.2	0.5	15.2	2.04	134.2 V

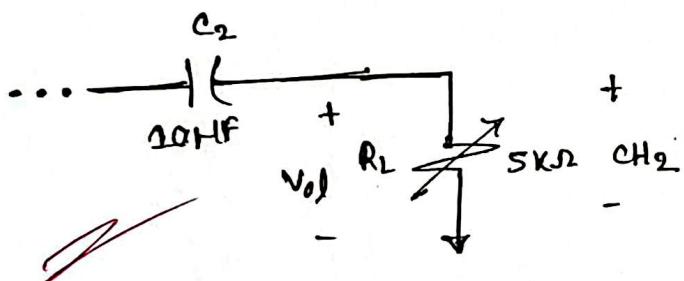
~~R_i~~

$$R_i = \frac{R_2}{\frac{V_{SA}}{V_{IA}} - 1}$$

~~(KR)~~

3.14

~~R_o~~



$$V_{oal} = \frac{V_{oaln}}{2}$$

~~(V)~~
R_o
(KR)

1.02

~~2.18~~

V 0.181

V 0.5

0.21

2.0

2.02

Oscilloscope settings

Probe switch	CH ₁ 9X	CH ₂ 1X
Zero time	XAxis	XAxis
AC / DC	AC	AC
Volts / DIV V	0.5V	-20m
Time / DIV	0.5ms	
Trigger Mode	ATO	
TRIGGER SOURCE	CH1.	
TRIGGER COUPLING	AC	
TRIGGER LEVEL	0V	

Incomplete

$\frac{7}{10}$

Rise

2015 - December - 18