

Department of Electrical & Electrical Engineering
BRAC University
Semester- Fall 2025



EEE205

Electronic Circuits I

Section: 04

Design Project : BJT Amplifier Design

Group Number: 4

Group Members:

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Submitted to

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EEE205 Design Project:

$$A_v = 35 + 4 + 4 \\ = 43$$

Section: 04

$$R_i \geq 20 + \frac{4}{2} \\ \approx 22$$

Group: 04

Objectives: To design and analyze a small-signal amplifier with a voltage gain of $A_v = 43$ and input resistance $R_i > 22\text{k}\Omega$ using either an npn BJT or NMOS transistor.

Methodology: For this project, we will initially select a pre built configuration and find out the large and small signal parameters through large signal (DC) analysis and small signal (AC) analysis. Then we will derive expressions for those parameters, (I_C , V_{CE} , V_{TH} , R_{TH}) and approximate further for our convenience. And we have to used a fixed universal value of B for our BJT analysis corresponding to the model, then we will assume values of the resistors, I_C and others through a trial and error basis where we will run simulations till we find our desired output.

Technical Specifications:

We will use 3 different configurations for 3 members. For the transistors, we will use the npn BJT Q2N760 which has a β value of 393.6. Our design requires 2 voltage sources, one AC and one DC. The DC source can either be a 10V single or double 5V source. Our circuit will consist of multiple resistors and capacitors depending on the configuration. We will use the PSpice simulation software to design and simulate our amplifier.

For constraints, our simulation will be run using the closest standard values of our components, this may yield slight errors.

Configuration 2

Attempted By : Mahdi Zaman

Student ID : 24121307

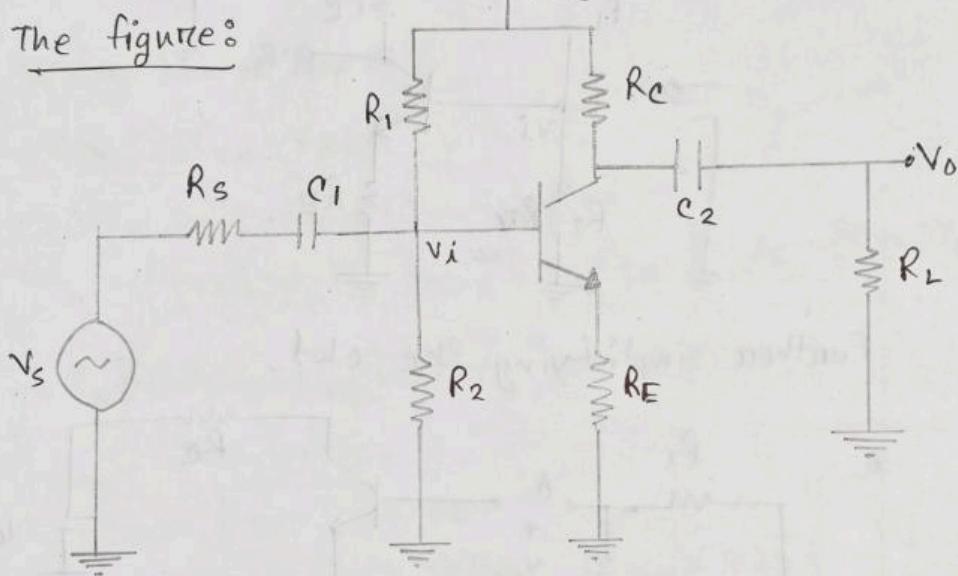
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Name : Mahdi Zaman

ID : 24121307

For our Common Emitter Amplifier
we will use - Configuration 02 ,
 V_{CC}

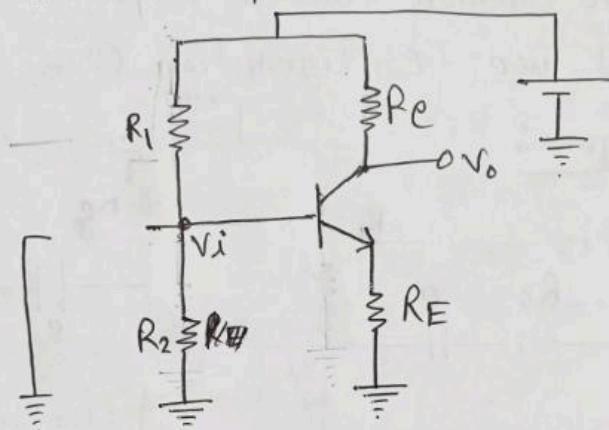
The figure :



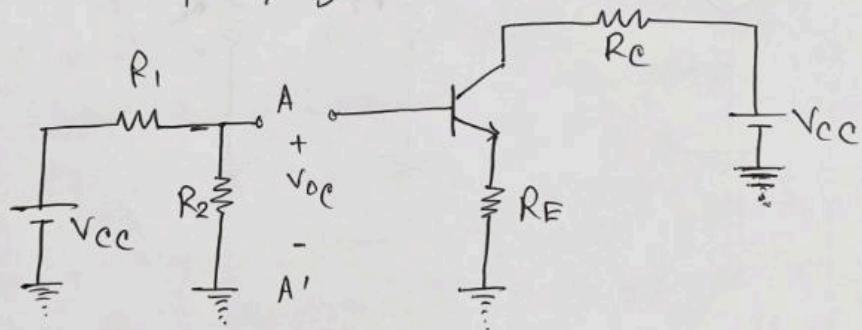
(2)

Large signal analysis:

Turning off all AC sources,
Open all the capacitors,



Further simplifying the ckt.



$$\text{Hence, } V_{TH} = V_{OC} = \dots$$

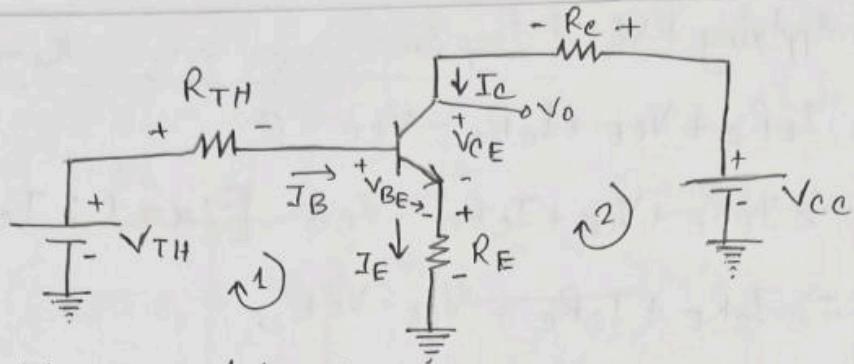
where,

$$R_{TH} = R_1 \parallel R_2 = \frac{R_1 \times R_2}{R_1 + R_2}$$

$$\therefore V_{TH} = \frac{R_2}{R_1 + R_2} \cdot V_{CC}$$

As it is an amplifier the BJT is in
Active region and α/β is allowed.

(3)



Applying KVL at loop 1,

$$+V_{TH} - I_B R_{TH} - V_{BE} - I_E R_E = 0$$

$$\Rightarrow V_{TH} = I_E R_E + V_{BE} + I_B R_{TH}$$

$$= (1+\beta) I_B R_E + V_{BE} + I_B R_{TH} \quad [\because I_E = (\beta+1) I_B]$$

$$\Rightarrow I_B [(1+\beta) R_E + R_{TH}] = V_{TH} - V_{BE}$$

$$\Rightarrow I_B = \frac{V_{TH} - V_{BE}}{R_{TH} + (1+\beta) R_E}$$

$$\Rightarrow I_C = \beta \times \frac{V_{TH} - V_{BE}}{R_{TH} + (1+\beta) R_E} \quad [\because I_C = \beta I_B]$$

As, $R_{TH} \ll (1+\beta) R_E$, so, $R_{TH} + (1+\beta) R_E \rightarrow (1+\beta) R_E$ [\because Approximation]

$$\therefore I_C = \frac{\beta (V_{TH} - V_{BE})}{(1+\beta) R_E}$$

as, $\beta \approx (1+\beta)$, we can write, [\therefore Approximation]

$$I_C = \frac{\beta (V_{TH} - V_{BE})}{\beta (R_E)}$$

$$\therefore I_C = \frac{V_{TH} - V_{BE}}{R_E} \quad \text{--- (1)}$$

(1)

Applying KVL at loop 2,

$$I_E R_E + V_{CE} + I_C R_C - V_{CC} = 0$$

$$\Rightarrow I_E R_E + V_{CE} + I_C R_C = V_{CC} \quad [\because \alpha \approx 1; I_C \approx I_E]$$

$$\Rightarrow I_C R_E + I_C R_C = V_{CC} - V_{CE}$$

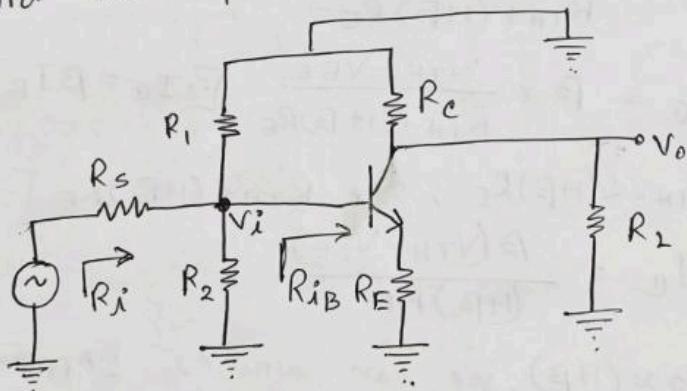
$$\Rightarrow I_C (R_E + R_C) + V_{CE} = V_{CC}$$

$$\Rightarrow V_{CE} = V_{CC} - I_C (R_E + R_C) \quad \text{--- (1)}$$

Now,

Small signal analysis:

Turn off DC sources,
short the capacitors,



To draw the small signal equivalent ckt

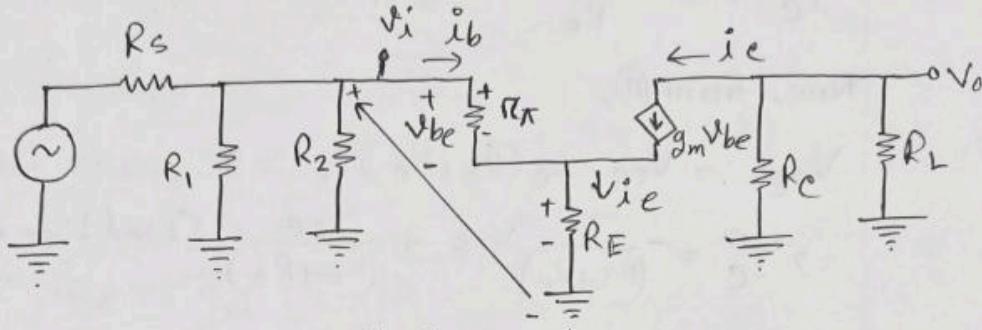
We will use Hybrid π model.

Hence, ~~$R_i = R_{iB} \parallel R_1 \parallel R_2$~~

(5)

Small signal equivalent ckt : Hybrid π model

$$[i_b = \frac{v_{be}}{r_\pi}]$$



Determining voltage, v_i [Input]

$$[i_e = (1+\beta)i_b]$$

$$v_i - i_b r_\pi - i_e R_E = 0$$

$$\Rightarrow i_b r_\pi + i_e R_E = v_i \Rightarrow i_b r_\pi + (1+\beta)i_b R_E = v_i$$

$$\Rightarrow i_b [r_\pi + (1+\beta)R_E] = v_i$$

We know,

$$R_{iB} = \frac{v_i}{i_b} = \frac{i_b [r_\pi + (1+\beta)R_E]}{i_b} = r_\pi + (1+\beta)R_E$$

$$\text{And, } R_i = R_{iB} \| R_1 \| R_2$$

Determining output, v_o

$$v_o = (R_C \| R_L) \times (-i_c) \quad [\text{using ohm's law}]$$

$$\Rightarrow v_o = -g_m v_{be} (R_C \| R_L) \quad [i_c = g_m v_{be}]$$

$$\therefore A_v = \frac{v_o}{v_i} = \frac{-g_m v_{be} (R_C \| R_L)}{i_b [r_\pi + (1+\beta)R_E]} \quad [i_b = \frac{v_{be}}{r_\pi}]$$

$$= \frac{-g_m r_\pi (R_C \| R_L)}{r_\pi + (1+\beta)R_E} = \frac{-\beta (R_C \| R_L)}{r_\pi + (1+\beta)R_E} \quad \text{--- (1)}$$

(6)

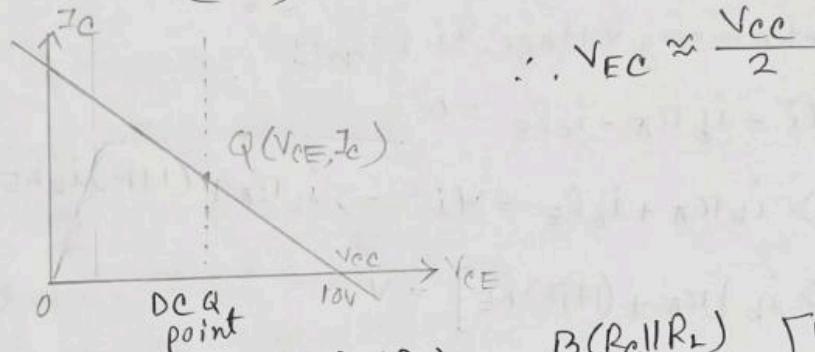
∴ Approximations,

$$I_C = \frac{V_{TH} - V_{BE}}{R_E} \quad [\text{Approximated}]$$

Now, from (1),

$$V_{CE} = V_{CC} - I_C(R_E + R_C)$$

$$\Rightarrow I_C = -\frac{1}{(R_E + R_C)} V_{CE} + \frac{V_{CC}}{(R_E + R_C)} \quad [\text{Load Line eq}]$$



$$\therefore V_{CE} \approx \frac{V_{CC}}{2}$$

from (11) $A_V = \frac{-\beta(R_C || R_L)}{r_\pi + (1+\beta)R_E} = \frac{\beta(R_C || R_L)}{r_\pi + (1+\beta)R_E} \quad \begin{array}{l} \text{[Here (-) indicates} \\ \text{the direction} \\ \text{of the output]} \end{array}$

$$= \frac{\beta(R_C || R_L)}{\beta(R_E)} \quad \begin{array}{l} \text{[Here, } (1+\beta)R_E \gg r_\pi \therefore r_\pi \approx 0 \\ \text{and } (1+\beta) \approx \beta \end{array}$$

$$= \frac{R_C || R_L}{R_E} \quad \therefore A_V = \frac{R_C || R_L}{R_E}$$

Again, $R_C || R_L = \frac{R_C \times R_L}{R_C + R_L}$ [As R_L is not given, assuming $R_L = \infty$]

$$= \frac{R_C \times R_L}{R_L} \quad \begin{array}{l} R_L \gg R_C \\ \therefore R_C + R_L = R_L \end{array}$$

$$= R_C$$

$$\therefore A_V = \frac{R_C}{R_E}$$

(7)

We know,

$$r_{\pi} = \frac{\beta}{g_m} = \frac{V_T}{I_B}$$

and $r_e = \frac{\alpha}{g_m} = \frac{V_T}{I_E}$

We know, $\alpha = \frac{\beta}{1+\beta} \approx 1$ [$\because \beta \gg (1+\beta)$]

$$\therefore r_e = \frac{1}{g_m} = \frac{r_{\pi}}{\beta}$$

$$\therefore r_e = \frac{r_{\pi}}{\beta} \Rightarrow r_{\pi} = \beta r_e$$

$$R_i = (20 + \frac{4}{2}) = 22$$

$$\text{gain} = (35 + 4 + 4) = 43$$

Transistor: We used BJT Q2N760 $\beta = 393.6$

$$\therefore A_V = \frac{V_o}{V_i} \Rightarrow 43 = \frac{R_c}{R_E} \quad [A_V = \frac{R_c}{R_E}] \quad | V_{CC} = 10V$$

$$\text{Assuming } R_E = 280\Omega = 0.24k\Omega$$

$$\therefore R_c = 43 \times 0.24 = 10.32k\Omega \quad [V_{CE} \approx \frac{V_{CC}}{2}]$$

$$\text{Finding } I_c, \quad V_{CE} = V_{CC} - I_c(R_c + R_E)$$

$$\Rightarrow S = 10 - I_c(10.32 + 0.24)$$

$$\Rightarrow I_c = \frac{10 - S}{10.56} = 0.473 \text{ mA}$$

$$\therefore \text{Finding } V_{TH} \Rightarrow I_c = \frac{V_{TH} - V_{BE}}{R_E} \Rightarrow V_{TH} = I_c R_E + V_{BE}$$

$$\therefore V_{TH} = (0.473 \times 0.24) + 0.7 = 6.8135V$$

$$\text{Now, } V_{TH} = \frac{R_2}{R_1 + R_2} \times V_{CC} \quad [\text{Assuming } R_2 = 20k\Omega]$$

$$\Rightarrow 6.8135 = \frac{20}{R_1 + 20} \times 10 \Rightarrow R_1 = 225.85k\Omega$$

$$\text{finding } r_e, \quad r_e = \frac{V_T}{I_c} \quad [V_T = 25 \text{ mV } @ T = 300K]$$

$$= \frac{25}{0.473} = 52.85\Omega$$

$$\therefore r_\pi = \beta r_e = 20801.76 = 20.8k\Omega$$

$$\therefore R_{iB} = r_\pi + (1 + \beta)R_E = [20.8 + (393.6 + 1)0.24] k\Omega$$

$$= 115.504k\Omega$$

$$\therefore R_i = R_{iB} \parallel R_1 \parallel R_2 = \left(\frac{1}{115.5} + \frac{1}{225.85} + \frac{1}{20} \right)^{-1} = 15.85k\Omega$$

$$A_V R_i = 15.85k\Omega \cancel{< 22}$$

\therefore condition not satisfied.

BJT won't ~~not~~ meet expectations.

Simulation gain, $A_V = 150.3$

Trial #2.

$$A_V = 43 = \frac{R_C}{R_E} \left[\text{Assuming } R_E = 1k\Omega \right] \quad \begin{cases} \beta = 393.6 \\ V_{CC} = 10V \end{cases}$$

$$\Rightarrow R_C = 43 \times 1 = 43k\Omega$$

$$\therefore \underline{I_{CE} V_{CE}} = V_{CC} - I_C (R_C + R_E)$$

$$\Rightarrow I_C = \frac{V_{CC} - V_{CE}}{R_C + R_E} = \frac{10 - 5}{1 + 43} = 0.114mA$$

$$\therefore V_{TH} = I_C R_E + V_{BE} \left[\cancel{I_C} = I_C = \frac{V_{TH} - V_{BE}}{R_E} \right]$$

$$= 0.114 \times 1 + 0.7 = 0.814V$$

$$\text{Now, } V_{TH} = \frac{R_2}{R_1 + R_2} \times V_{CC} \quad \left[\text{Assuming } R_2 = 39k\Omega \right]$$

$$\Rightarrow 0.814 = \frac{39}{R_1 + 39} \times 10; \quad R_1 = 490.115k\Omega$$

$$\therefore r_e = \frac{V_T}{I_C} = \frac{25}{0.114} = 211.86 = 0.212k\Omega$$

$$\therefore r_{\pi} = \beta r_e = 393.6 \times 0.212 = 83.44 = 83.44k\Omega$$

$$\therefore R_{IB} = r_{\pi} + (1 + \beta) R_E = [83.44 + (393.6 \times 1)] = 478.04k\Omega$$

$$\therefore R_i = R_{IB} \parallel R_1 \parallel R_2 = \left(\frac{1}{478.04} + \frac{1}{440.1} + \frac{1}{39} \right)^{-1} = 33.33k\Omega$$

$$\text{As, } R_i = 33.33 > 22$$

\therefore Condition is satisfied.

But after simulation was done,

gain, $A_V = 79.34$ which doesn't meet the expectations. Hence, this trial is Rejected.

Trial #3

$$A_V = 43 = \frac{R_C}{R_E} \quad [\text{Assuming } R_E = 10\text{k}\Omega] \quad | \quad \beta = 393.6 \\ V_{CC} = 10V$$

$$\Rightarrow R_C = 43 \times 10 = 430\text{k}\Omega$$

Now,
 $V_{CE} = V_{CC} - I_C(R_C + R_E)$

$$\Rightarrow I_C = \frac{V_{CC} - V_{CE}}{R_C + R_E} = \frac{10 - 5}{430 + 10} = 0.0114\text{mA}$$

Now, $V_{TH} = \frac{R_2}{R_1 + R_2} \times V_{CC} - I_C R_E + V_{BE}$
 $= (0.0114 \times 10) + 0.7 = 0.814V$

Again,
 $V_{TH} = \frac{R_2}{R_1 + R_2} \times V_{CC} \quad [\text{Assuming } R_2 = 56\text{k}]$

$$\Rightarrow 0.814 = \frac{56}{R_1 + 56} \times 10 \quad \cancel{R_1 = 631.96\text{k}\Omega}$$

$$\Rightarrow R_1 = 631.96\text{k}\Omega$$

$$\therefore r_e = \frac{V_T}{I_C} = \frac{25\text{mV}}{0.0114\text{mA}} = 2192.98\text{k}\Omega = 2.193\text{k}\Omega$$

$$\therefore r_{\pi} = \beta r_e = 393.6 \times 2.193 = 863.16\text{k}\Omega$$

$$\therefore R_{IB} = r_{\pi} + (1 + \beta)R_E = 863.16 + (393.6 \times 10) \text{ k}\Omega$$

$$\therefore R_i = R_{IB} \parallel R_1 \parallel R_2 = \left(\frac{1}{4809.16} + \frac{1}{631.96} + \frac{1}{56} \right)^{-1}$$

$$= 50.897\text{k}\Omega$$

$$\therefore R_i = 50.897\text{k}\Omega < 22\text{k}\Omega$$

\therefore condition is satisfied.

Simulation shows a gain of $A_V = 41.94$

which is very close to expectation.

PSpice Circuit Setup

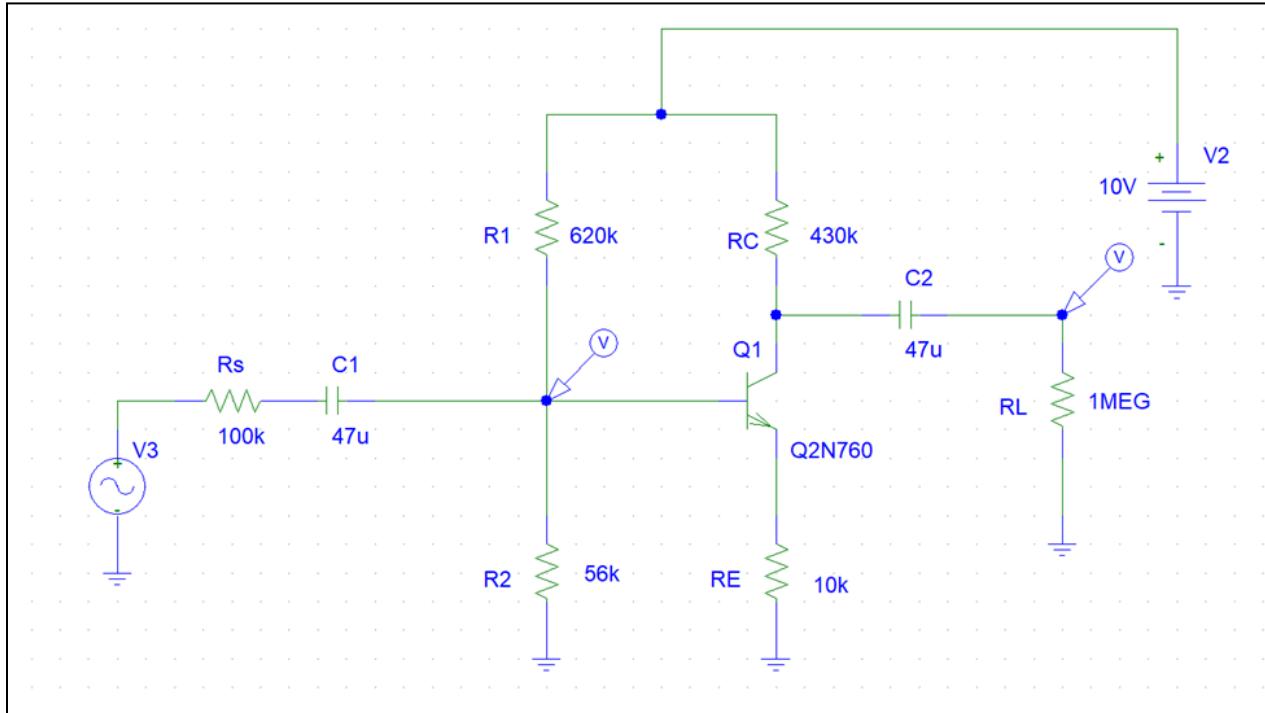
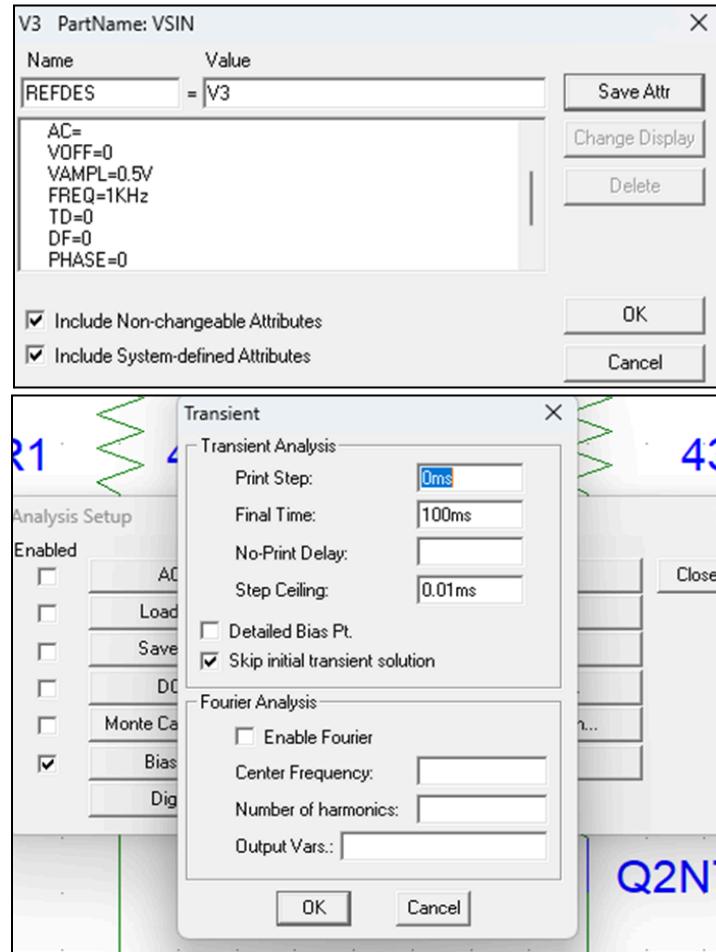


Figure of Circuit running on 3rd Trial parameters

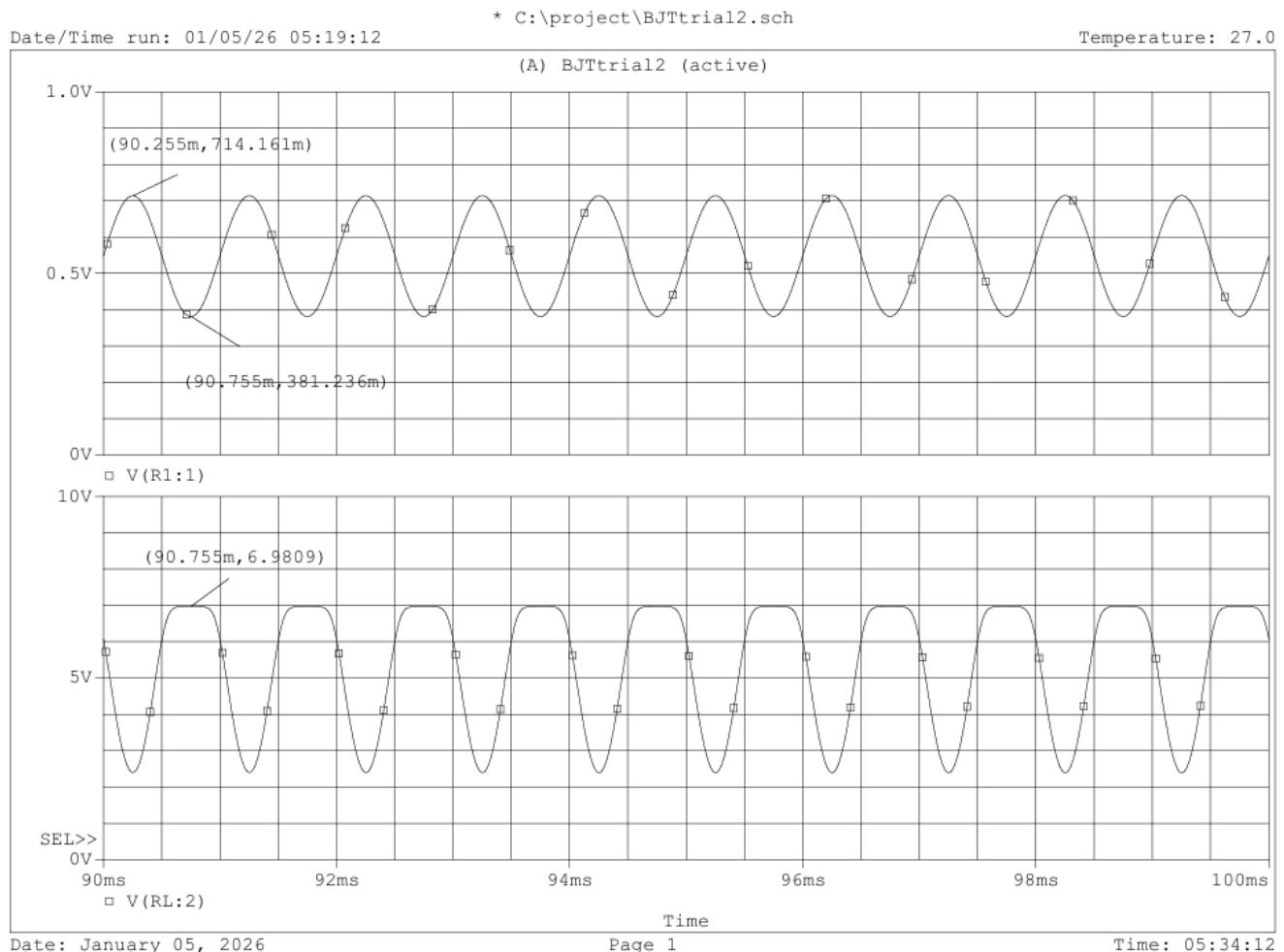
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Q2N760
NPN
IS      5.911000E-15
BF      393.6
NF      1
VAF     62.37
IKF     .01243
ISE     5.911000E-15
NE      1.271
BR      1.372
NR      1
RB      10
RC      1.61
CJE    4.973000E-12
MJE    .4146
CJC    4.017000E-12
MJC    .3174
TF     818.200000E-12
XTF    7
VTFF   4
ITFF   .35
TR     4.761000E-09
XTB    1.5
CN     2.42
D      .87

```



Simulation Graph : V_{input} vs V_{output}



Graph after running simulation on trial 3
Plot 1 showing V(input) and Plot 2 showing V(output)

Table of calculations and used parameters

<i>Trial no.</i>		<i>Trial 1</i>	<i>Trial 2</i>	<i>Trial 3</i>
Found Values	$R_1(k\Omega)$	225.8	440	631.96
	$R_2(k\Omega)$	20	39	56
	$R_E(k\Omega)$	0.24	1	10
	$R_C(k\Omega)$	10.32	43	430
Corresponding Standard Values	$R_1(k\Omega)$	220	430	620
	$R_2(k\Omega)$	20	39	56
	$R_E(k\Omega)$	0.24	1	10
	$R_C(k\Omega)$	10	43	430
Input Impedance, $R_i(k\Omega)$		15.85	33.33	50.897
Gain, A_v		150.3	79.34	41.94
Result Acceptance		Rejected	Rejected	Accepted

Table of Results:

Result Analysis:

Through Trial #3, we found the best possible values, adhering to our expectations.

We found, $R_E = 10\text{k}\Omega$

$$R_C = 430\text{k}\Omega$$

$$R_1 = 631.96\text{k}\Omega$$

$$R_2 = 56\text{k}\Omega$$

For, R_1 , we used the ^{closest} corresponding standard value of $620\text{k}\Omega$. The rest are already standard values

To calculate gain, A_V , [From the simulation graph]

$$\text{Find } V_i = \frac{714.161 - 381.236}{2 \times 1000} \text{ V} = 0.1664625 \text{ V}$$

$$V_o = 6.9809$$

$$\therefore A_V = \frac{V_o}{V_i} = \frac{6.9809}{0.1664625} = 41.936 \approx 41.94$$

$$\therefore \text{Error \%} = \frac{41.94 - 43}{43} \times 100\% = 2.46\%$$

The error is quite small but we have to keep in mind that, for our calculation purposes we have done a lot of approximations which range from neglecting small impedances to changing the values of β slightly for our convenience. Therefore, the output is not extremely precise, but quite close.

Comments and Conclusion:

Using standard values slightly affect the desired output. We also used the generic NPN BJT Model Q2N760 from PSpice Library which has a fixed $B = 393.6$.

Overall, the results have been satisfactory as the designed common emitter amplifier has yielded the expected gain with less than $\pm 5\%$ errors while satisfying other circuit conditions. Therefore, the design is successful in amplifying.

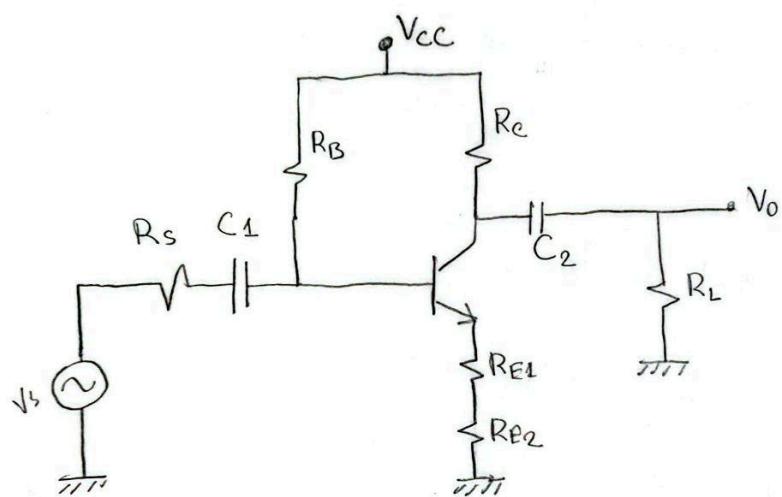
Configuration 6

Attempted By : Oshiul Alim Ornob
Student ID : 24121268

(Name): Oshul Alim Ornob

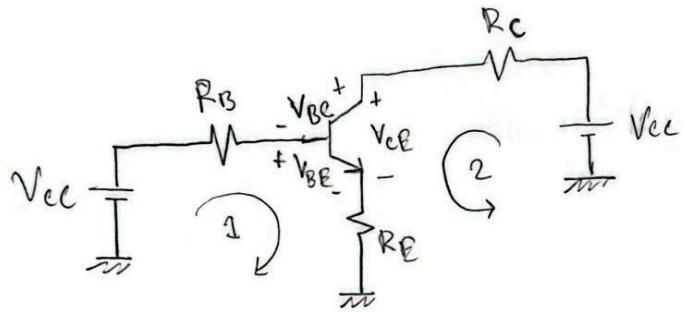
(ID): 24121268

Configuration 6



we can write $R_{E1} + R_{E2} = R_E$ as they're in series.

DC equivalent ckt. (Large signal equivalent ckt.)



Applying KVL @ loop 1,

$$-V_{cc} + I_B R_B + V_{BE} + I_E R_E = 0$$

$$\Rightarrow I_B R_B - V_{cc} + V_{BE} - I_E R_E = 0$$

$$\Rightarrow I_B R_B - V_{cc} + V_{BE} - (1+\beta) I_B R_E = 0$$

$$\Rightarrow I_B \left[R_B - (1+\beta) R_E \right] = V_{cc} - V_{BE}$$

$$\Rightarrow I_B = \frac{V_{cc} - V_{BE}}{R_B - (1+\beta) R_E} \quad \dots \quad (1)$$

$$I_E = (1+\beta) I_B$$

Applying KVL @ Loop 2,

$$-V_{CC} + I_C R_C + V_{CE} + I_E R_E = 0$$

$$\Rightarrow V_{CE} = V_{CC} - I_C R_C - I_E R_E \quad | \quad I_C \approx I_E$$

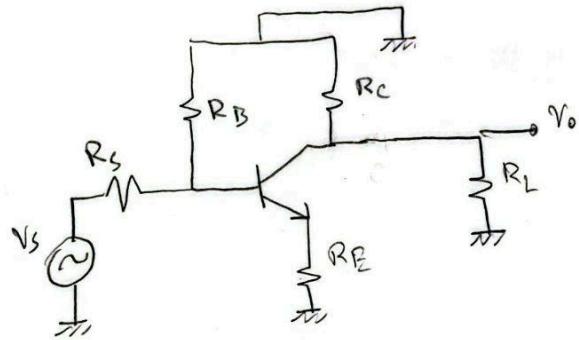
$$\Rightarrow V_{CE} = V_{CC} - I_C (R_C + R_E)$$

.... (ii)

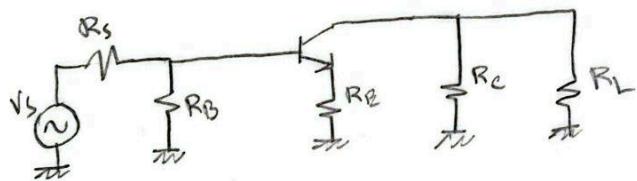
and, $I_C = \beta I_B$

$$\text{so, } I_C = \frac{\beta (V_{CC} - V_{BE})}{R_B + (1+\beta) R_E} \quad \dots \quad (\text{iii})$$

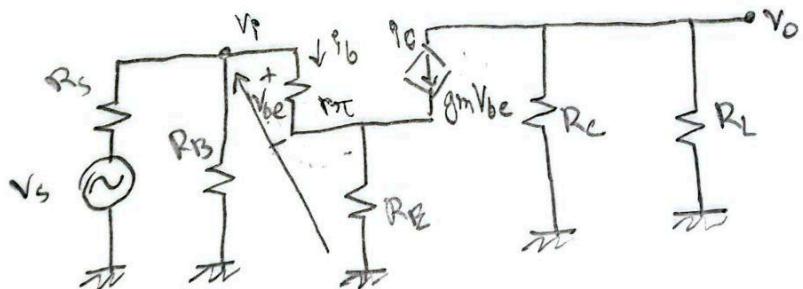
Small signal equivalent ckt.



or,



as Hybrid π model,



here,

$$V_i = i_e R_E + i_b r_\pi$$

$$\text{and, } V_o = i_o (R_c \parallel R_L)$$

$$= -i_e (R_c \parallel R_L)$$

$$= g_m V_{be} (R_c \parallel R_L)$$

$$\begin{aligned} R_{ib} &= \frac{V_i}{i_b} = \frac{i_e R_E + i_b r_\pi}{i_b} = \frac{i_b r_\pi + i_e R_E}{i_b} \\ &= \frac{i_b (1+\beta) R_E + i_b r_\pi}{i_b} \\ &= \frac{i_b \{(1+\beta) R_E + r_\pi\}}{i_b} \\ &= (1+\beta) R_E + r_\pi \end{aligned}$$

$$\text{or, } (1+\beta) R_E \dots (\text{iv})$$

since $(1+\beta) R_E \gg r_\pi$

$$\begin{aligned} A_V &= \frac{V_o}{V_i} = \frac{-g_m V_{be} (R_c \parallel R_L)}{i_e R_E + i_b r_\pi} = \frac{-g_m V_{be} (R_c \parallel R_L)}{i_b (1+\beta) R_E + i_b r_\pi} \\ &= \frac{-g_m r_\pi j_b (R_c \parallel R_L)}{[r_\pi + (1+\beta) R_E] j_b} [V_{be} = i_b r_\pi] \\ &= \frac{-g_m r_\pi (R_c \parallel R_L)}{(1+\beta) R_E} \end{aligned}$$

$[(1+\beta) R_E \gg r_\pi]$

$$= \frac{-\beta (R_c \parallel R_L)}{(1+\beta) R_E} \quad \dots \text{ (v)}$$

The BJT we will be using will have a large β value. $\therefore 1 + \beta \approx \beta$

$$\therefore \text{from eq. (iv)} \rightarrow (1 + \beta) R_E \approx \beta R_E = R_{ib}$$

$$\text{and eq. (v)} \rightarrow \frac{-\beta (R_c \parallel R_L)}{(1 + \beta) R_E}$$

$$\approx \frac{-\beta (R_c \parallel R_L)}{\beta R_E}$$

$$= \frac{- (R_c \parallel R_L)}{R_E} = A_V$$

[Trial - 1]

Setting R_E to $4.7\text{k}\Omega$, a standard resistor. And R_L has to be big; $R_L = 1\text{M}\Omega$

$$A_V = \frac{-(R_E \parallel R_L)}{R_E} \Rightarrow R_E = \frac{(R_E \parallel R_L)}{A_V}$$

eliminating the (-) sign as it only indicates the direction of the output.

$$\text{Now, } R_E = \frac{4.7\text{k} \parallel 1\text{M}}{43} = 99.57\ \Omega \approx 100\ \Omega$$

Since,

$$V_{CE} = \frac{V_{cc}}{2} = \frac{10}{2} = 5\text{ V}$$

$$\text{and, } V_{CE} = V_{cc} - I_C (R_E + R_{E1} + R_{E2})$$

Since R_E has to be around 100 and it is $(R_{E1} + R_{E2})$, picking 2 $51\ \Omega$ resistors will be better than 2 $47\ \Omega$. (Both are standard Resistors)

We will be using BJT \rightarrow Q2N760

running a simple circuit in PSpice, we
get its β value to be 393.6

$$\therefore \underline{\beta = 393.6}$$

for our Section $\rightarrow 4$ & Group $\rightarrow 4$

Requirements for $A_v = 35 + 5 + 0.1 = 43$

$$\text{and } R_i > 20 + \frac{C_1}{2} = 22 \Omega$$

We will be using a single 10V DC power source.
which is V_{cc} in our circuit.

$$\therefore V_{cc} = 10V$$

Q

$$\therefore V_{CE} = V_{CC} - I_C (R_C + R_{E1} + R_{E2})$$

$$\Rightarrow 5 = 10 - I_C (4700 + 51 + 51)$$

$$\Rightarrow I_C = \frac{(4700 + 51 + 51)}{10 - 5} = \frac{10 - 5}{4700 + 51 + 51}$$

$$= 1.04 \text{ mA.}$$

Here,

$$I_C = \frac{\beta (V_{CC} - V_{BE})}{R_B + (\beta + 1) (R_{E1} + R_{E2})}$$

$$\Rightarrow I_C R_B + I_C (\beta + 1) (R_{E1} + R_{E2}) = \beta (V_{CC} - V_{BE})$$

$$\Rightarrow I_C R_B = \beta (V_{CC} - V_{BE}) - I_C (\beta + 1) (R_{E1} + R_{E2})$$

$$\Rightarrow R_B = \frac{1}{I_C} \left[\beta (V_{CC} - V_{BE}) - I_C (\beta + 1) (R_{E1} + R_{E2}) \right]$$

$$= \frac{1}{1.04 \times 10^{-3}} \left[393.6 (10 - 0.7) - 1.04 \left(\frac{10^2}{\cancel{102}} \right) (393.6 + 1) \left(\frac{10^2}{\cancel{102}} \right) \right]$$

$$= 3479.44 \quad \text{or} \quad 3.479 \times 10^6 \text{ V}$$

$$\approx 3.5 \text{ M}\Omega \quad \text{or} \quad 3.5 \text{ M}\Omega$$

We can opt for a ~~3.5 MΩ~~ resistor for R_B .
~~3.6 MΩ~~

$$\text{Now, } R_{ib} = \beta \cdot R_E \\ = 3936 \Omega$$

$$\text{and, } R_i = R_B \parallel R_{ib}$$

$$= \frac{R_B \times R_{ib}}{R_B + R_{ib}}$$

$$= 3931.55 \Omega$$

but, $R_i \neq 92k$.

there fore, this trial didn't fulfill all the conditions.

$$A_V = \frac{-5.5562 - (-6.4501)}{+0.49 - (-0.49)}$$

$$= 0.91$$

[Trial 2]

assuming $R_e = 22k\Omega$

$$\therefore R_E A_V = \frac{R_C}{R_E} \quad \text{for } R_L = \infty$$

$$R_E = \frac{R_C}{A_V} = \frac{22k}{43} = 511.6279 \Omega$$

and $V_{CE} = V_{ac} - I_C (R_C + R_E + R_{B2})$

$$5 = 10 - I_C (22k + 511.6279)$$

$$\Rightarrow I_C < \frac{5}{22511.63} = 0.22 \text{ mA.}$$

$$R_B = \frac{1}{I_C} \left[\beta (V_{ac} - V_{BE}) - I_C (\beta + 1) (R_E + R_{B2}) \right]$$

$$+ \cancel{2952} \quad 16.28 \text{ M}\Omega$$

$$\therefore R_i = \beta R_E = 201.37 k\Omega$$

$$\therefore R_{ib} = R_B \parallel R_{ib} = 198.91 \text{ k}$$

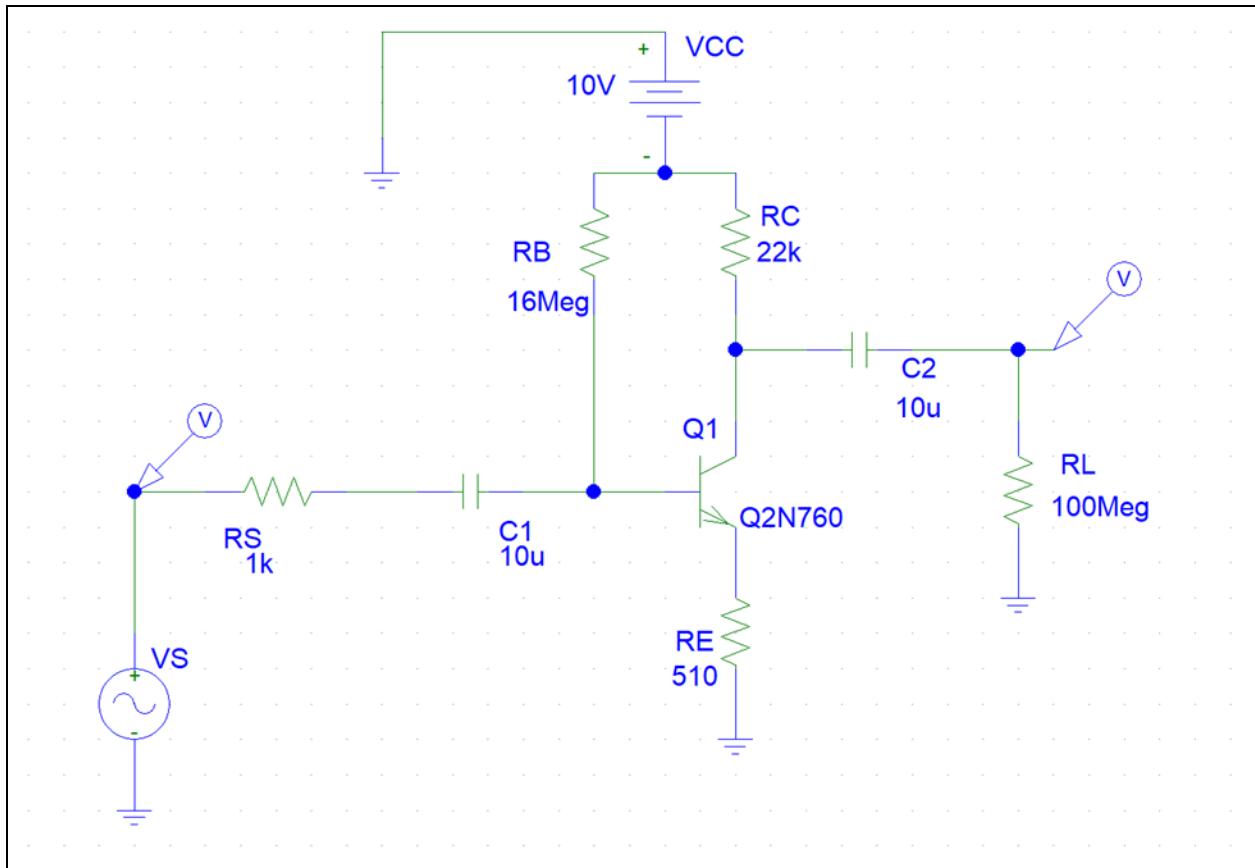
$$R_{ib} > 22 \text{ k.}$$

But after running it through PSpice, it doesn't seem to maintain the average gain.

$$A_v = \frac{\cancel{5.007 - (-5.00)} \quad \cancel{0.51 - (-0.8)}}{\cancel{-1.873} \cancel{(-12.843)}} \\ = \frac{-1.873 - (-2.843)}{-5 - (-5)} \\ = \cancel{+0.97} \quad 0.97$$

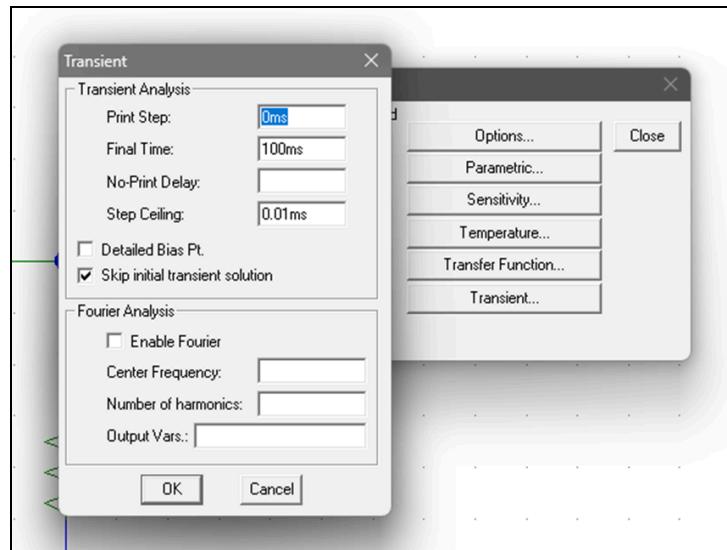
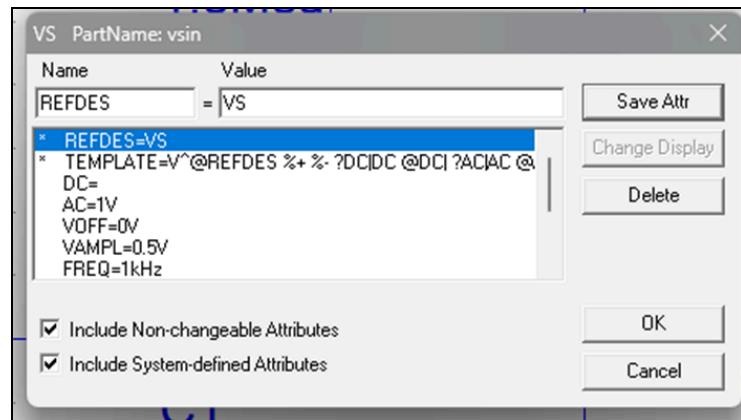
where it was supposed to be 43.

Therefore this trial was also a failure.

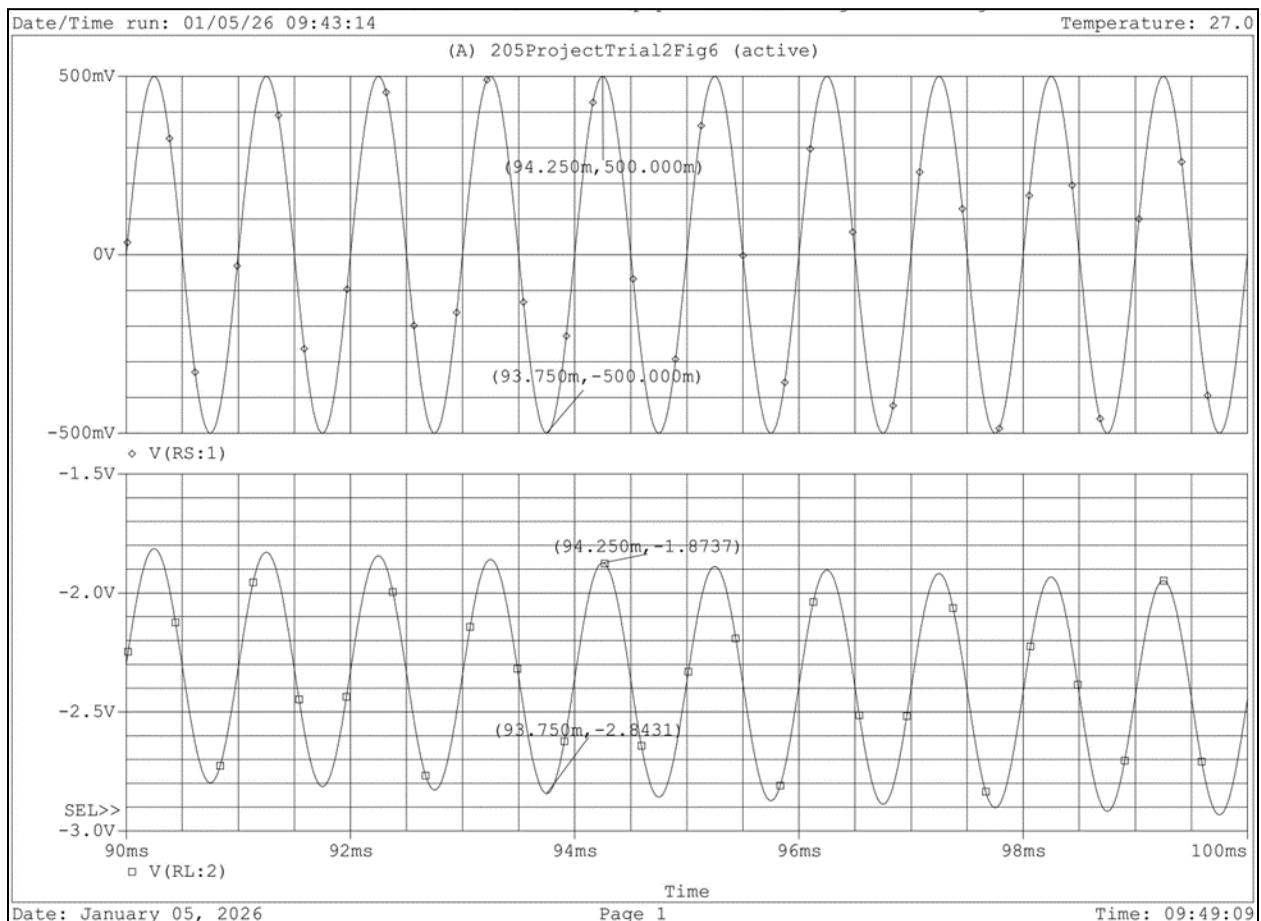


Config 6 done in PSpice

	Q2N760
	NPN
IS	5.911000E-15
BF	393.6
NF	1
VAF	62.37
IKF	.01243
ISE	5.911000E-15
NE	1.271
BR	1.372
NR	1
RB	10
RC	1.61
CJE	4.973000E-12
MJE	.4146
CJC	4.017000E-12
MJC	.3174
TF	818.200000E-12
XTF	7
VTI	4
ITF	.35
TR	4.761000E-09
XTB	1.5
CN	2.42
D	.87



Setup



Simulation Results

		Trial 1	Trial 2
Found Values	RC(kΩ)	4.7	22
	RE(kΩ)	0.1	510
	RB(kΩ)	3479	16280
Corresponding Standard Values	RC(kΩ)	4.7	22
	RE1(kΩ)	0.051	510
	RE2(kΩ)	0.051	
	RB(kΩ)	3600	16000
Input Impedance, Ri(kΩ)		3.9	198.91
Gain, Av		0.91	0.97
Result Acceptance		Rejected	Rejected

Comments :

Having voltage gain set to be around medium range may have caused some degree of discrepancy as the condition of configuration b was designed around high voltage gain.

While R_E and R_C weren't in proximity of each other as their values were far apart, but R_B was far greater; which was also mentioned in the description of this configuration.

Even after a long time in simulations (around 0 to 1 seconds), the graph of the output had a slope which was affected when capacitors were changed but the slope was not removed fully.

Configuration 5

Attempted By : Souvik Barman Ratul
Student ID : 24121205

EEE205

Design Assignment

Name: Souvik Barman Ratul

ID : 24121205

Group: 04

Section: 04

Hence,

$$S=4 \text{ [section number]}$$

$$G_L=4 \text{ [Group number]}$$

Now,

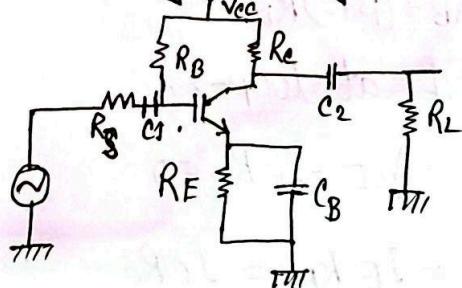
$$\begin{aligned} \text{Voltage gain } |Av| &= \frac{V_o}{V_i} = (35 + S + G_L) \\ &= (35 + 4 + 4) V \\ &\leq 43 V \end{aligned}$$

$$\text{Input resistance } R_i > \left(20 + \frac{G_L}{2}\right) k\Omega$$

$$R_i > \left(20 + \frac{4}{2}\right) k\Omega$$

$$R_i > 22 k\Omega$$

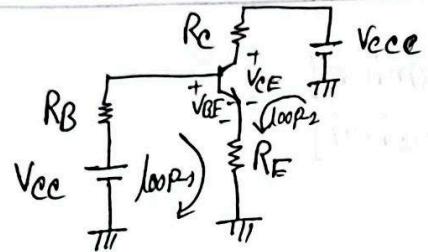
Hence we using configuration-5,

Step-1

DC equivalent circuit (open all capacitor)

Step-2

AC equivalent circuit (short all capacitor)



now,

Applying KVL at loop-1,

$$-V_{CC} + I_B R_B + V_{BE} + I_E R_E = 0.$$

$$\Rightarrow -V_{CC} + I_B R_B + V_{BE} + I_B (\beta+1) R_E = 0$$

$$\Rightarrow I_B R_B + I_B (\beta+1) R_E = V_{CC} - V_{BE}$$

$$\Rightarrow I_B = \frac{V_{CC} - V_{BE}}{R_B + (\beta+1) R_E} \dots \textcircled{i}$$

Applying KVL at loop-2,

$$V_{CC} - I_C R_C - V_{CE} - I_E R_E = 0$$

$$\Rightarrow V_{CC} - V_{CE} - I_E R_E = I_C R_C$$

$$\Rightarrow R_C = \frac{V_{CC} - V_{CE} - I_E R_E}{I_C} \dots \textcircled{ii}$$

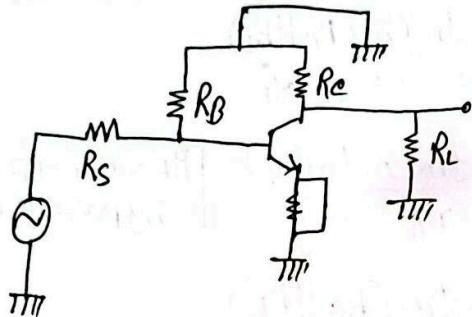
now,

$$I_C = \beta I_B$$

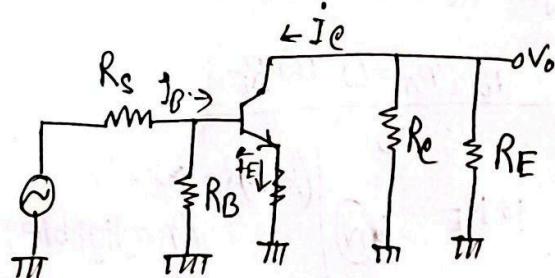
$$= \beta \left(\frac{V_{CC} - V_{BE}}{R_B + (\beta+1) R_E} \right) \dots \textcircled{iii}$$

now,

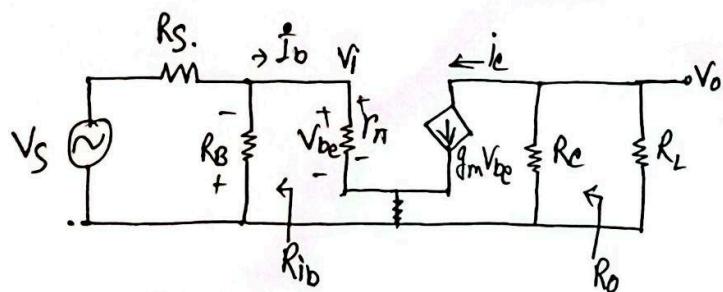
The AC equivalent circuit,



Redraw the circuit



now we replace this circuit with π -model.



$$V_{in} = i_b R_{\pi} + i_e R_E$$

$$\Rightarrow V_{in} = i_b [r_{\pi} + (1 + \beta) R_E]$$

and,

$$V_o = -(\beta i_b)(R_C \parallel R_L)$$

$$\begin{aligned}
 A_v = \frac{V_o}{V_{in}} &= \frac{-\beta(i_b)(R_C || R_L)}{i_b[r_\pi + (1+\beta)R_E]} \\
 &= \frac{-g_m r_\pi (R_C || R_L)}{r_\pi + (1+\beta)R_E} \\
 &= \frac{-g_m r_\pi (R_C || R_L)}{r_\pi} \quad [R_E = 0; \text{for the bypass capacitor}] \\
 &= -g_m (R_C || R_L)
 \end{aligned}$$

$$|AV| = g_m (R_C || R_L) \dots \textcircled{V}$$

$$\begin{aligned}
 \therefore R_{ib} &= \frac{V_{in}}{i_b} = \frac{i_b [r_\pi + (1+\beta)R_E]}{i_b} \\
 R_{ib} &= \beta R_E \dots \textcircled{N} \quad [(1+\beta)R_E \gg r_\pi \text{ (negligible)}]
 \end{aligned}$$

Trial-1Let, $\beta = 393.6$

$$V_{CE} = \frac{V_{CC}}{2} = \frac{10}{2} = 5V$$

$$V_T = 0.026V, I_C = 35mA \approx I_E$$

$$R_B = \frac{100}{100} k\Omega, R_L = 100k\Omega$$

(iii) $\Rightarrow I_C = \beta \left(\frac{V_{CC} - V_{BE}}{R_B + (\beta + 1)R_E} \right)$

$$\Rightarrow 35 = 393.6 \left(\frac{10 - 0.7}{\frac{100}{100} + 393.6 \times R_E} \right)$$

$$\Rightarrow 35 = \frac{3660.48}{100 + (393.6 R_E)}$$

$$\Rightarrow 3660.48 = 3500 + 33776 R_E$$

$$\Rightarrow R_E = 0.012 k\Omega$$

(ii) $\Rightarrow R_C = \frac{V_{CC} - V_{CE} - I_E R_E}{I_C}$

$$= \frac{10 - 5 - (35 \times 0.012)}{35}$$

$$= 0.131 k\Omega$$

now,

$$g_m = \frac{I_c}{V_T} = \frac{35}{0.026} = 1346.16 \text{ mS.}$$

$$R_{\pi} = \frac{\beta}{g_m} = \frac{393.6}{1346.16} = 0.292 \text{ k}\Omega$$

$$\therefore R_{iB} = R_{\pi} = 0.292 \text{ k}\Omega$$

$$R_I = (R_{iB} || R_B) = (0.292^{-1} + 100^{-1}) \\ = 3.43 \text{ k}\Omega$$

now,

$$A_v = g_m (R_C || R_L) \\ = 1346.16 (0.131^{-1} + 100^{-1}) \\ = 102.8$$

now,

~~$$A_v > 43 = 102.8 > 43$$~~

$$\text{and } R_I = 3.43 < 22 \text{ k}\Omega$$

\therefore The trial is not successful.

Trial-2

Let, $\beta = 393.6$

$$V_{CE} = \frac{V_{CC}}{2} = \frac{10}{2} = 5V.$$

$$V_T = 0.026V, I_C = 15mA \approx I_E$$

$$R_B = 10k\Omega, R_L = 100k\Omega$$

$$\textcircled{i} \Rightarrow I_C = \beta \left(\frac{V_{CC} - V_{BE}}{R_B + (\beta + 1)R_E} \right)$$

$$\Rightarrow 15 = 393.6 \left(\frac{10 - 0.7}{10 + 393.6 \times R_E} \right)$$

$$\Rightarrow 3660.48 = 150 + 1919 R_E$$

$$\Rightarrow R_E = 1.83k\Omega$$

$$\textcircled{i} R_E = \frac{V_{CC} - V_{CE} - I_C R_E}{I_C}$$

$$= \frac{10 - 5 - (15 \times 1.83)}{15}$$

$$> -1.497k\Omega$$

now,

$$g_m = \frac{I_C}{V_T} = \frac{15}{0.026} = 576.92 \text{ mS.}$$

$$R_\pi = \frac{\beta}{g_m} = \frac{393.6}{576.92} = 0.68 \text{ k}\Omega$$

$$R_{i\beta} = R_\pi = 0.68 \text{ k}\Omega$$

$$\cancel{R_i} R_i = (R_{iB} \parallel R_B)$$

$$= (0.68^{-1} + 10^{-1})$$

$$= 1.58 \text{ k}\Omega$$

$$A_V = g_m (R_C \parallel R_L)$$

$$= 576.92 \left((-1.49)^{-1} + (100)^{-1} \right)$$

$$= 391.15$$

now,

$$A_V = 391.15 > 43 V$$

$$R_i = 1.58 < 2.2 \text{ k}\Omega$$

This trial is not successful.