

Department of Electrical & Electrical Engineering

BRAC University  
Semester- Fall 2025



**EEE205L**

**ELECTRONIC CIRCUITS I LABORATORY**

**Section: 01**

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**Experiment:7 - BJT AMPLIFIER**

**Group Number: 1**

**Group Members:**

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**Submitted by :**

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**Name : Abir Chowdhury Ratul**

**Aldrin Nippon Bobby**

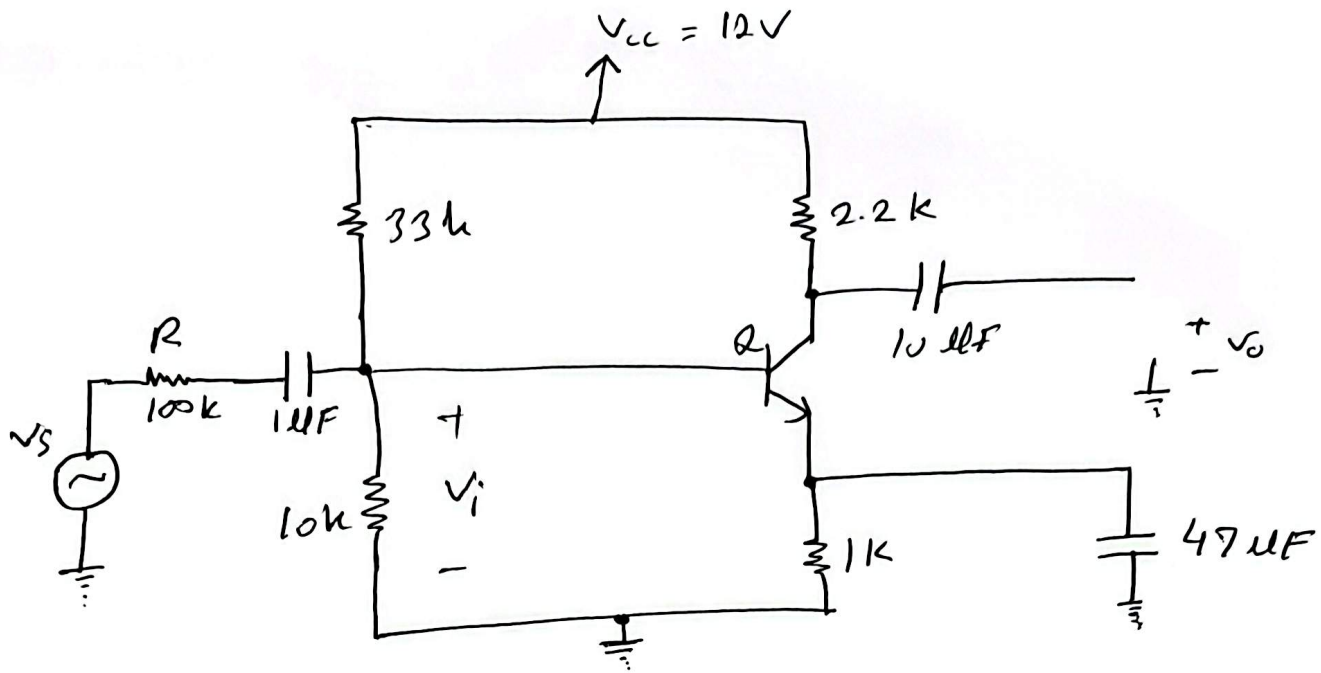
**ID : 24121204**

Objective:

To study the common emitter amplifier and measure its gain,  
input impedance and output impedance.

Equipments:

1. Breadboard
2. Jumper wires
3. Multimeter
4. DC voltage source
5. Resistors (1K, 2.2 K, 10K, 33K $\Omega$ , 100k)
6. 100k Potentiometer
7. n-p-n Transistor (C828)
8. Capacitors (1 $\mu$ F, 10 $\mu$ F, 47 $\mu$ F)



1. Amplification:

$$V_i = \boxed{15.2} \text{ mV}$$

$$V_o = \boxed{2040} \text{ mV}$$

2. Input Impedance,  $R_i$ :

$$V_i = \boxed{15.2} \text{ mV}$$

$$V_s = \boxed{500} \text{ mV}$$

$$R = \boxed{100} \text{ k}\Omega$$

$$R_i = \boxed{2.14} \text{ k}\Omega$$

3. Output Impedance,  $R_o$  :

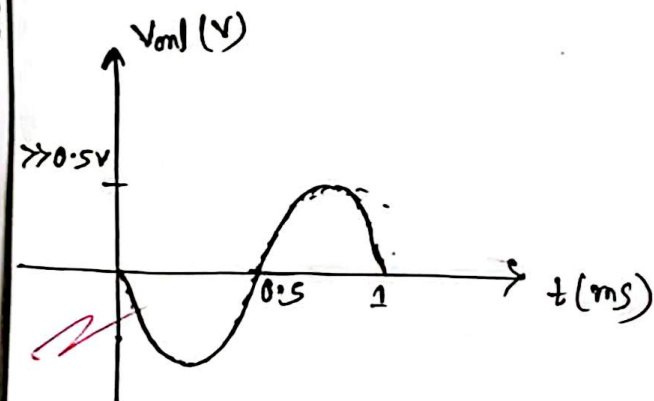
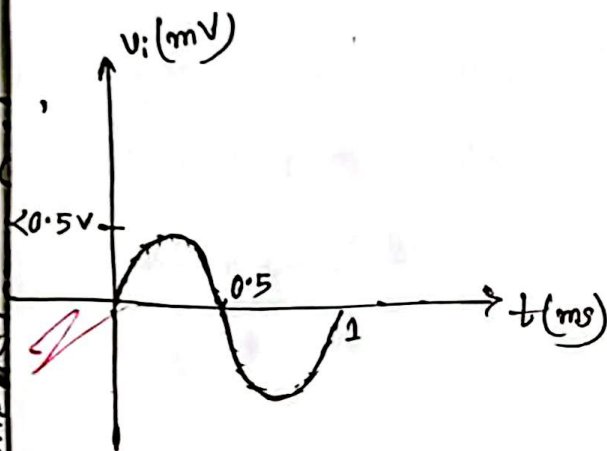
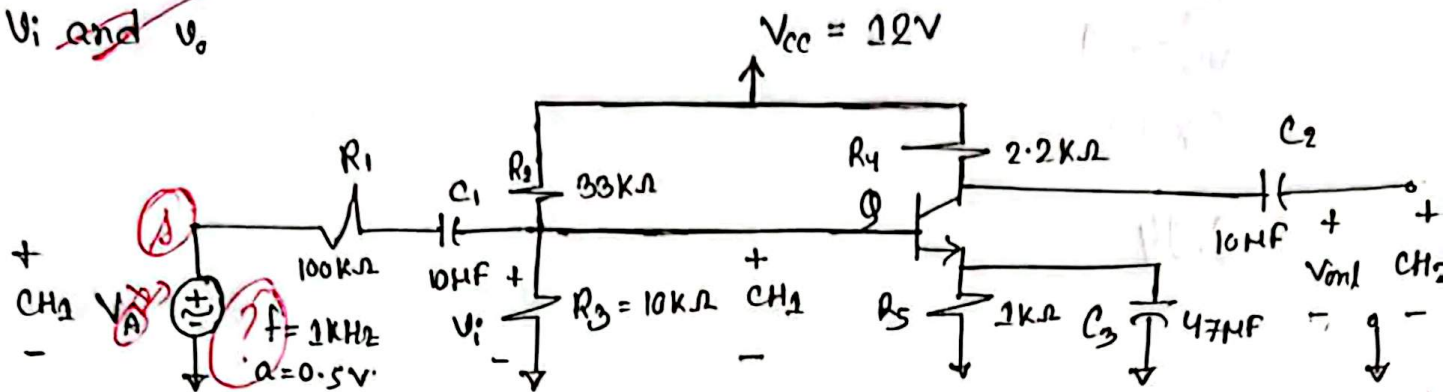
$$R_o = \boxed{2.18} \text{ k}\Omega$$

# Experiment: 07

## Common Emitter Amplifier

Group 1

$V_i$  and  $V_o$



$R_2$ ( $k\Omega$ )	$V_{SQ}$ (V)	$V_{iQ}$ (mV)	$V_{oQ}$ (V)	$A_v = \frac{V_{oQ}}{V_{iQ}}$
100.2	0.5	15.2	2.04	134.2 V

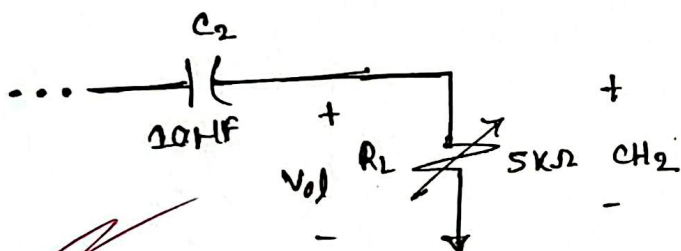
~~Q1 Ri~~

$$R_i = \frac{R_2}{\frac{V_{sa}}{V_{ia}} - 1}$$

(kΩ)

3.14

~~Q2 Ro~~



$$V_{out} = \frac{V_{in}}{2}$$

(V)

1.02

$R_o$   
(kΩ)

~~2.18~~  
2.18

break  
point

131.5 V

break  
(V)

5.04

break  
(V)

12.5

break  
(V)

2.0

break  
(V)

100.5

Oscilloscope settings

	CH1	CH2
Probe Switch	1X	1X
Zero time	XAxis	XAxis
AC/DC	DC	AC
Volts/DIV	0.5V	200m
Time/DIV	0.5ms	
Trigger Mode	ATO	
TRIGGER SOURCE	CH1.	
TRIGGER COUPLING	AC	
TRIGGER LEVEL	0V	

Incomplete

$$\frac{7}{10}$$

Rye

2025 - December - 18