

Lab 6 – Quadrature Encoder Decoding

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March 7, 2021

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Code:

```
/*
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    Purpose: With the use of a quadrature encoder, this code will count up
    on the LED display when turning clockwise, and count down when turning
    counter-clockwise.
*/

module qdecoder
( input logic a, b,
  input logic clock,
  output logic up, down ) ;

// my code is here
    logic [3:0] state;

    always_ff@(posedge clock) state[0] = a;

    always_ff@(posedge clock) state[1] = b;

    always_ff@(posedge clock) state[2] = state[0];

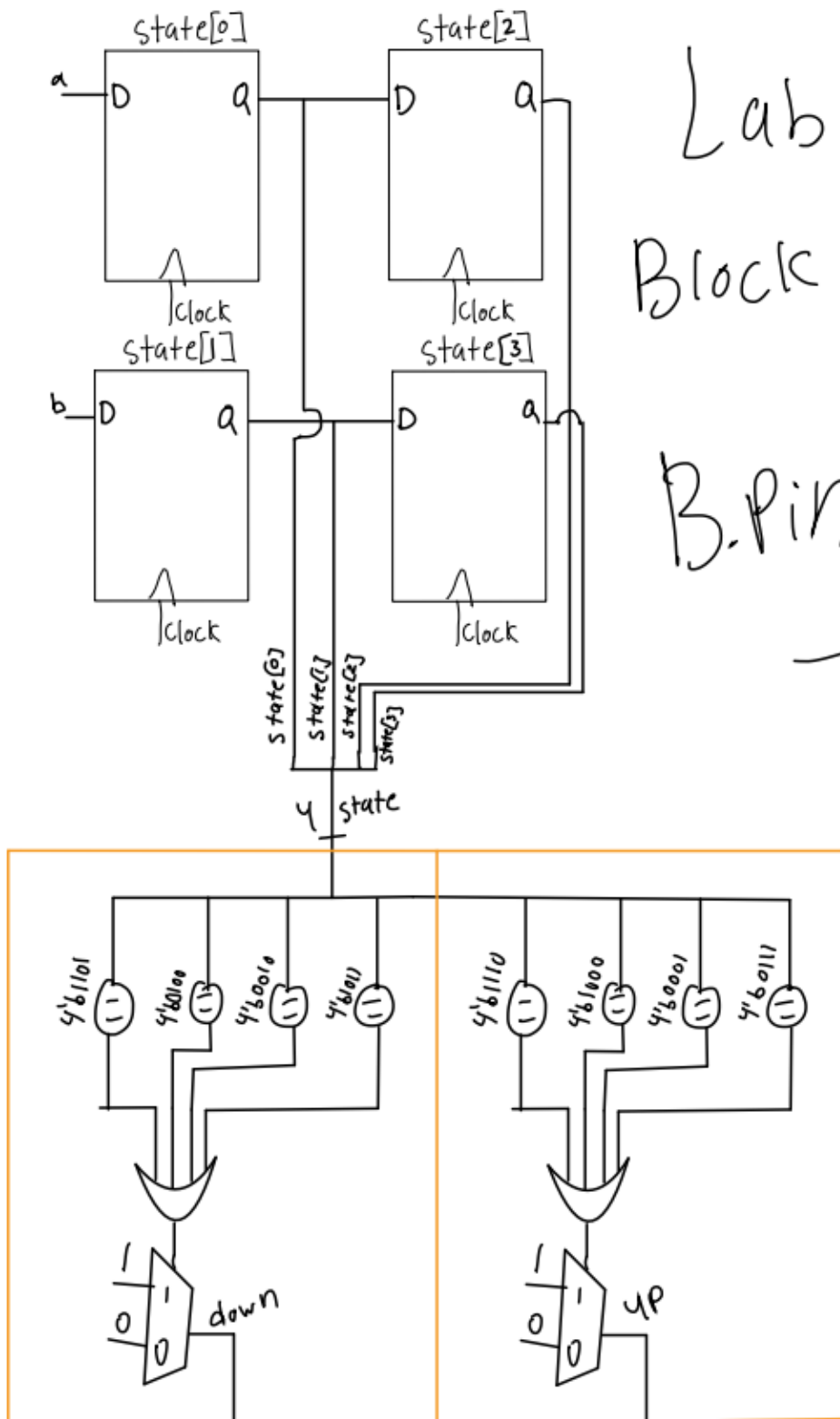
    always_ff@(posedge clock) state[3] = state[1];

    assign down =
        state == 4'b1101 ||
        state == 4'b0100 ||
        state == 4'b0010 ||
        state == 4'b1011 ? 1 :
        0;

    assign up =
        state == 4'b1110 ||
        state == 4'b1000 ||
        state == 4'b0001 ||
        state == 4'b0111 ? 1 :
        0;

endmodule
```

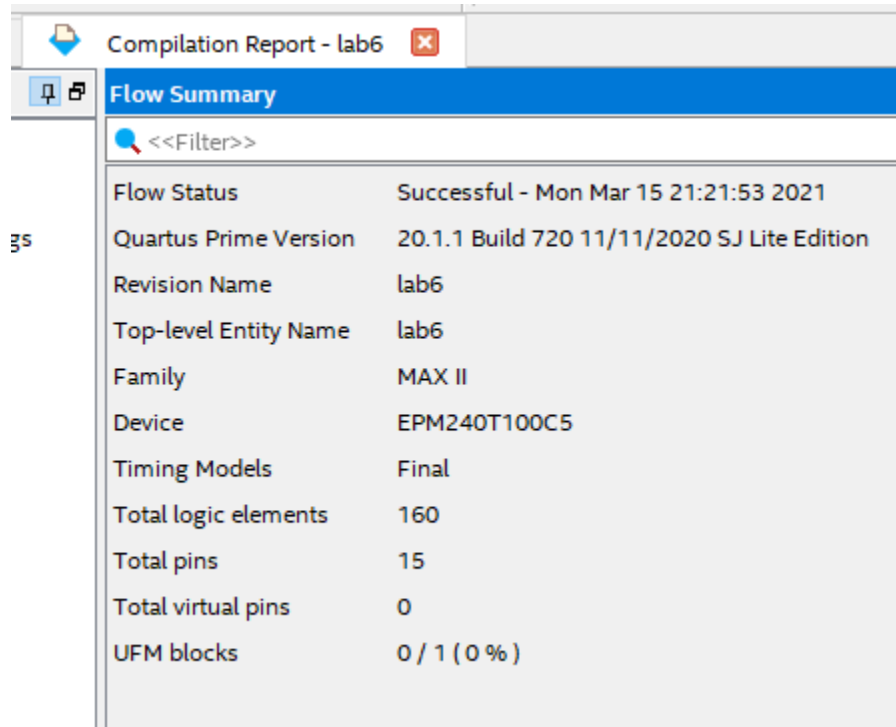
Block Diagram:



Lab 6
Block Diagram

B. Pint

Compilation Report:



Flow Summary	
<<Filter>>	
Flow Status	Successful - Mon Mar 15 21:21:53 2021
Quartus Prime Version	20.1.1 Build 720 11/11/2020 SJ Lite Edition
Revision Name	lab6
Top-level Entity Name	lab6
Family	MAX II
Device	EPM240T100C5
Timing Models	Final
Total logic elements	160
Total pins	15
Total virtual pins	0
UFM blocks	0 / 1 (0 %)

RTL Viewer:

