G. MADEGOWDA INSTITUTE OF TECHNOLOGY

Bharathinagara (K.M.Doddi), Maddur Taluk, Mandya Dist. – 571422

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

BIG DATA AND ANALYTICS (17CS82)

IA	2nd	**Instructions:**	Sem	8th
Faculty	Leena Shruthi	Answer at least one full	**Date**	16-08-2021
Max. Marks	**30**	question from each part	**Time**	10 AM -12:30 PM

Q. No.		Marks	CO'S	Blooms Level
	PART- A			
1.	Explain the architecture of SIC machine.	**10**		
	OR			
2.	Explain the architecture of SIC/XE machine.	**10**		
	PART- B			
3.	Define Assembler. Explain the functions of assembler.	**10**		
	OR			
4.	Write a note on Assembler Directive.	**10**		
	PART- C			
5.	Write and Explain Pass2 Algorithm of two Pass Assembler.	**10**		
	OR			
6.	Write a note on load and go Assembler.	**10**		