Arietta A5 - System On Module

Linux embedded system on module designed around the Microchip AT91SAMG25 ARM9@400MHz MPU

Overview (www/arietta/#config)   Documentation (www/arietta/#documentation)

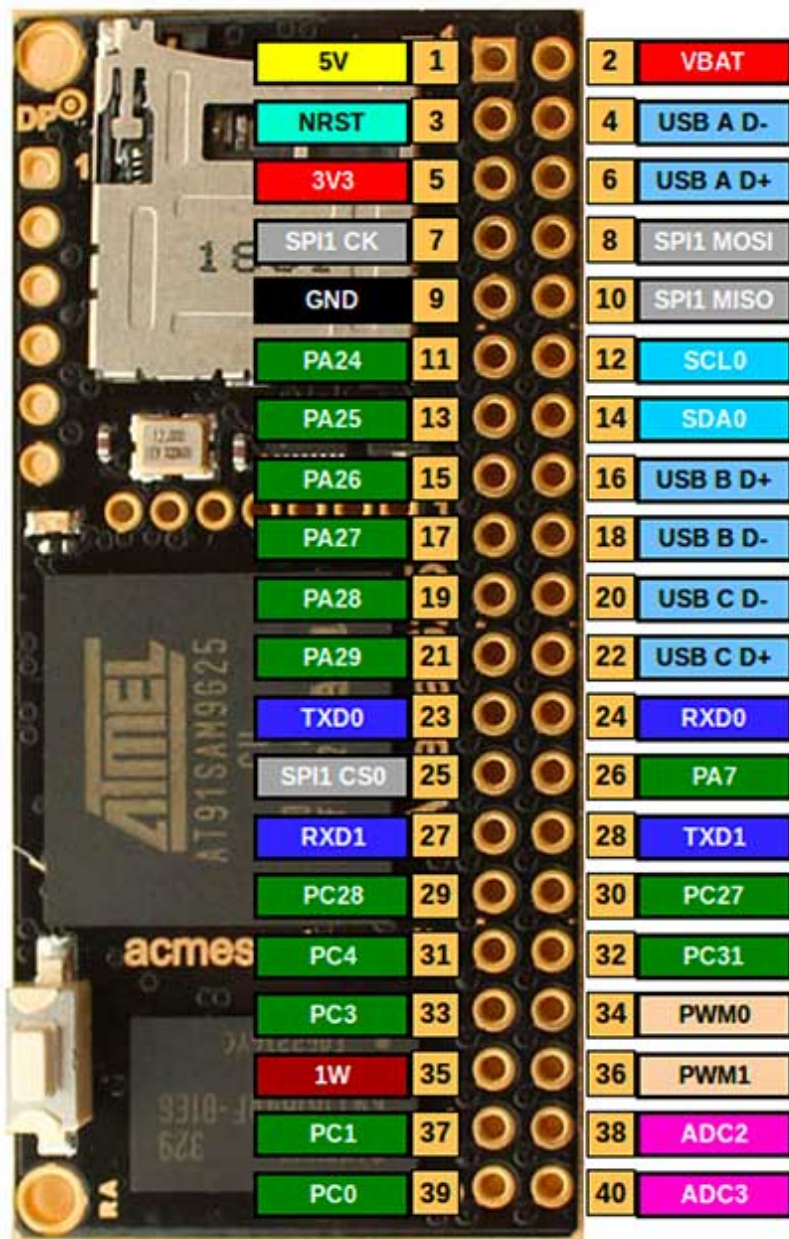Pinout (www/arietta/#pinout)   Download (www/arietta/#download)

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Arietta pinout

This is the default pinout of Arietta G25 when you use the latest default image available in the image repository (/download_arietta).

It is possible to change it by using the Device tree compiler and dynamic Arietta G25 pinout page (http://pinout.acmesystems.it)

5V	1			2	VBAT
NRST	3			4	USB A D-
3V3	5			6	USB A D+
SPI1 CK	7			8	SPI1 MOSI
GND	9			10	SPI1 MISO
PA24	11			12	SCL0
PA25	13			14	SDA0
PA26	15			16	USB B D+
PA27	17			18	USB B D-
PA28	19			20	USB C D-
PA29	21			22	USB C D+
TXD0	23			24	RXD0
SPI1 CS0	25			26	PA7
RXD1	27			28	TXD1
PC28	29			30	PC27
PC4	31			32	PC31
PC3	33			34	PWM0
1W	35			36	PWM1
PC1	37			38	ADC2
PC0	39			40	ADC3

Debug port pinout

This port is designed to be used with the Debug Port Interface (/DPI) to have access to the debug messages sent by the Atmel MPU at startup (RomBOOT), during AT91Bootstrap second level boot loader and the Linux Kernel bootstrap.

From this port is is possible to Login directly to the Linux console using a simple serial terminal emulator on your PC.

Pin #	Signal	Dir	Description
DP.1	3V3 power out	OUT	Output from the internal regulator from 5 to 3.3 volt
DP.2	EN5V input	INP	If low turn off internal regulator from 5 to 3.3 volt
DP.3	DTXD	OUT	Debug port serial output at 115200,N,8,1 3V3 level
DP.4	DRXD	INP	Debug port serial input
DP.5	WKUP	INP	Atmel MPU wakeup signal
DP.6	GND		Signal ground

WiFi module placement pinout

This port is designed to place this WiFi module (/WIFI-2).
To know hot to configure it: (/arietta_wifi)

Pin #	Signal	Dir	Description
7	WPS	OUT	WSP function. Low level activated. Wired to PC16
6	LED	N.C.	WiFi module activity led. Not used
5	GND		Signal ground GND
4	USB B D+		USB Host B D+
3	USB B D-		USB Host B D-
2	VCC 3.3V DC	OUT	Output from the internal regulator from 5 to 3.3 volt
1	WiFi TXEN	OUT	RF on/off control. Low level activated to off. Wired to PC30

Schematic

To know more about how are routed each signal inside the Arietta board please take a look to the Arietta schematic
(http://terzo.acmesystems.it/download/schematics/schematic_arietta.pdf).

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