Sum-Product Network and Its Application to Image Completion

A thesis defense

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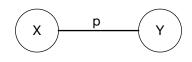
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Probabilistic Graphical Model

Probabilistic graphical model(PGM):

- Node(vertex): random variables
- Edge: condition(association)
- Weight: probability
- Network polynomial: computation

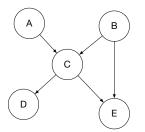




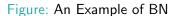
Traditional PGM: Bayesian Network

Bayesian Network(BN):

- Graph type: directed acyclic graph(DAG)
- Representation: conditional dependence
- Network polynomial: $p_{\mathcal{B}}(X) = \prod_{X \in \mathbf{X}} p(X|\mathbf{parent}(X))$



$$p_{\mathcal{B}}(A, B, C, D, E) = p(A)p(B)p(C|A, B)p(D|C)p(E|B, C)$$





Traditional PGM: Markov Random Field

Markov Random Field(MRF):

- Graph type: undirected graph
- Representation: Markov properties
- Network polynomial: $p_{\mathcal{M}}(\mathbf{X}) = \frac{1}{Z_{\mathcal{M}}} \prod_{l=1}^{L} \Psi_{\mathbf{C}_{l}}(\mathbf{X}_{\mathbf{C}_{l}})$

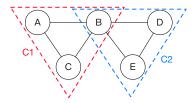
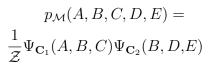


Figure: An Example of MRF



Learning

Learning:

- Object:
 - Structure learning
 - Parameters learning
- 2 Approach:
 - Generative learning
 - Discriminative learning



Inference

Inference:

- Marginalization
- Conditions
- Most probable explanation
- Maximum a-posterior



Weaknesses

Weaknesses:

- Complexity scales unproportionally
- Approximate learning
- Intractability
- 4 Separation of learning and inference



Motivation

Why SPN:

- Complexity scales up linearly
- Exact learning
- Tractability
- 4 Combination of learning and inference



Target

Targets:

- Implement an SPN
- Reproduce the application to image completion



Arithmetic Circuit(AC)

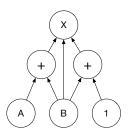


Figure: An Example of AC

Network polynomial:

$$f(A, B, 1) = (A + B)B(B + 1)$$



What is SPN

Sum-product network(SPN):

- Graph type: DAG
- Leaves: random variables
- Internal node: sum node, product node
- Root: sum node
- Network polynomial: $f_{\mathcal{S}}(\mathbf{X}) = \sum_{X \in \mathbf{X}} \Phi(X) \prod_{X \in \mathbf{X}} (X)$



Example

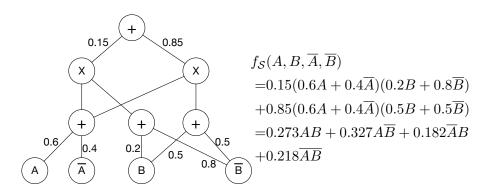


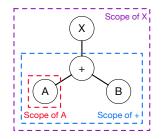
Figure: An Example of SPN



Scope

Definition (Scope of a Node(sc))

$$\mathbf{sc}(N) \begin{cases} \{X\} & \text{if } N \text{ is a leaf} \\ \cup_{\mathbf{C} \in \mathbf{chi}(N)} \mathbf{sc}(\mathbf{C}) & \text{if } N \text{ is an internal node} \end{cases}$$





Properties

Properties:

- Completeness: a sum node's children have the same scope
- Consistency: a variable and its negation are in the same scope of a product node
- Validity: completeness and consistency
- Decomposability: no variable appears in more than one child of a product node node

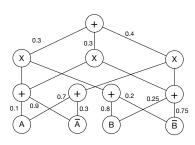


Figure: An Example of Valid SPN



Learning Methods

Learning:

- Structure learning:
 - Poon-Domingos Architecture: Rectangle regions
 - Dennis-Venture Architecture: Any shape regions
- Parameter learning:
 - Gradient method: maximize log-likelihood
 - EM algorithm: introduce latent variables, inference in E-step, update weights in M-step



Inference Methods

Differential approach:

- Compute the marginal distribution via network polynomials
- Interpret the probability distribution through derivatives



Poon-Domingos Architecture

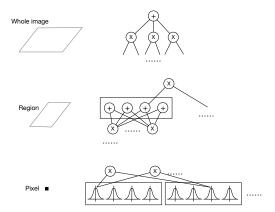


Figure: Poon-Domingos Architecture



Program

Code:

- **common**: helper functions for time management, messaging between progress(MPI), parameter settings, and some utilities.
- **evaluation**: dataset processing, model generation, evaluation.
- **spn**: SPN architecture, including node definition, computation functions, the learning, and inference.



Callgraph

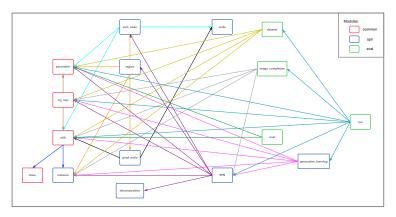


Figure: Callgraph of Program



Enviroment

Environment:

- Platform: TaiYi
- Library: OpenMPI C++
- Dataset:
 - Caltech:
 - \blacksquare 101 categories, from 40 to 800 images per category
 - \blacksquare about 300×200 pixels, rescaled to 100×64 pixels
 - Olivetti:
 - face images taken between Apr. 1992 and Apr. 1994 at AT&T Laboratories Cambridge
 - size 64×64 pixels



Experiments

Experiments:

- **Caltech**: 80 cores. **Olivetti**: 40 cores. size 64×64
- **Caltech**: 120 cores, **Olivetti**: 80 cores, size 64×64
- **3 Caltech**: 80 cores, size 100×64
- **Caltech**: 120 cores, size 100×64

Poon's experiments: 102 cores, **Olivetti**: 51 cores, size

 64×64



Comparison on MSE(1)

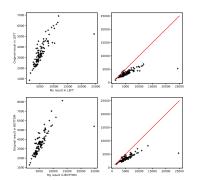


Figure: Exp. #1 vs Poon's(Caltech)

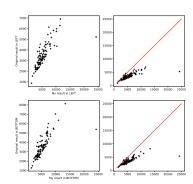


Figure: Exp. #2 vs Poon's(Caltech)



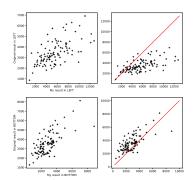


Figure: Exp. #3 vs Poon's(Caltech)

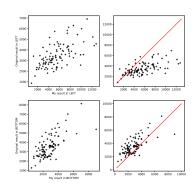


Figure: Exp. #4 vs Poon's(Caltech)



Comparison on Number of Cores

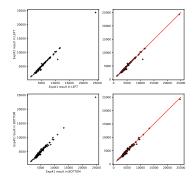
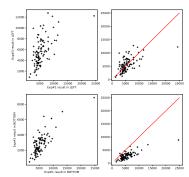


Figure: Exp. #1 vs Exp. #2(Caltech)



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Comparison on Input Size



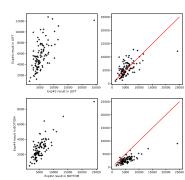


Figure: Exp. #1 vs Exp. #3(Caltech) Figure: Exp. #2 vs Exp. #4(Caltech)



Comparison on Image(1)



Figure: Airplanes-bottom(Poon's, Exp. #2, Exp. #4)



Figure: Yin_yang-left(Poon's, Exp. #2, Exp. #4)



Comparison on Image(2)

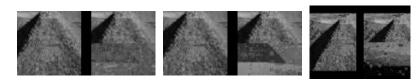


Figure: Pyramid-bottom(Poon's, Exp. #2, Exp. #4)



Figure: Sunflower-bottom(Poon's, Exp. #2, Exp. #4)



SUSTe

Comparison on Time

Time Cost	Poon's	Exp. #1	Exp. #2	Exp. #3	Exp. #4
Caltech-101 Olivetti	$\leq 2 \ \text{hours}$ within a few minutes	_	$\leq 7 \text{ hours}$ $\geq 72 \text{ hours}$	$\leq 11 \; \mathrm{hours}$ Nan	$\leq 19 \; \mathrm{hours}$ Nan

Table: Time Cost of Poon's Experiments and My Experiments



Analysis

Conclusion:

- Number of cores makes no influence
- Larger input size leads to lower MSE
- My implementation is valid

Why:

- Randomness in architecture
- Difference between implementation
- Complexity of model



Conclusion

Conclusion:

- My implementation is valid and successful
- Reproduction is not easy



Future Work

Future work:

- Architecture improvement
- More applications
- New algorithms for learning and inference



Acknowledgement

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Prof. Jialin Liu (Committee member)



Thanks

Thanks for listening!



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Q & A

Questions?



Timi Zheng