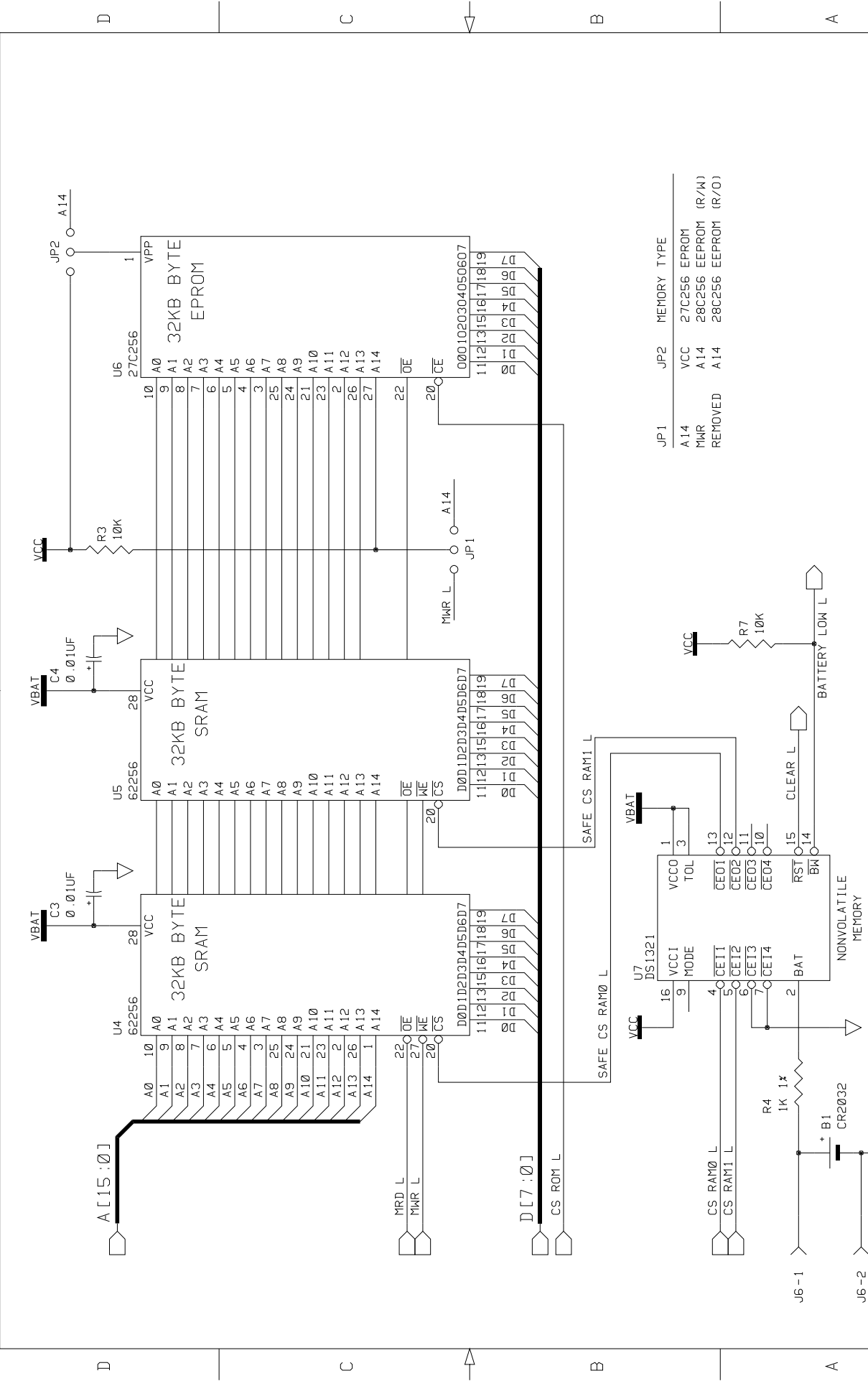


SBC1802 CPU		COPYRIGHT (C) 2021 BY SPARE TIME GIZMOS ALL RIGHTS RESERVED. REFER TO LICENSE FILE FOR DETAILS.		SIZE C	
18:34:34	20-Mar-24	CPU	SHEET 1 OF 12		

8 7 6 5 4 3 2 1



JP1	JP2	MEMORY TYPE
A14	VCC	27C256 EPROM
MWR	A14	28C256 EPROM (R/W)
REMOVED	A14	28C256 EPROM (R/O)

SBC1802  
MEMORY

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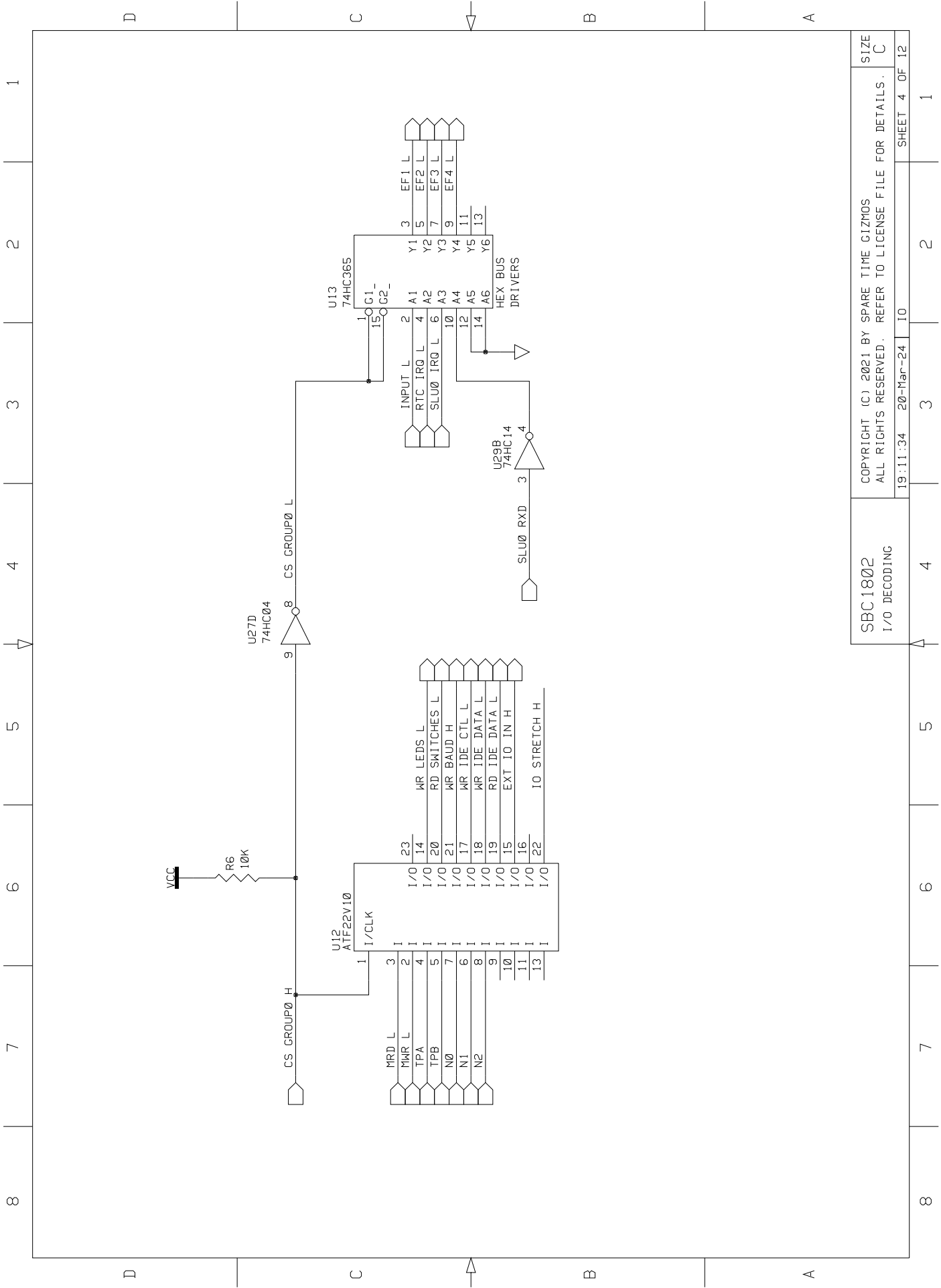
SIZE  
C

18:34:4520-Mar-24MEMORY

SHEET 2 OF 12

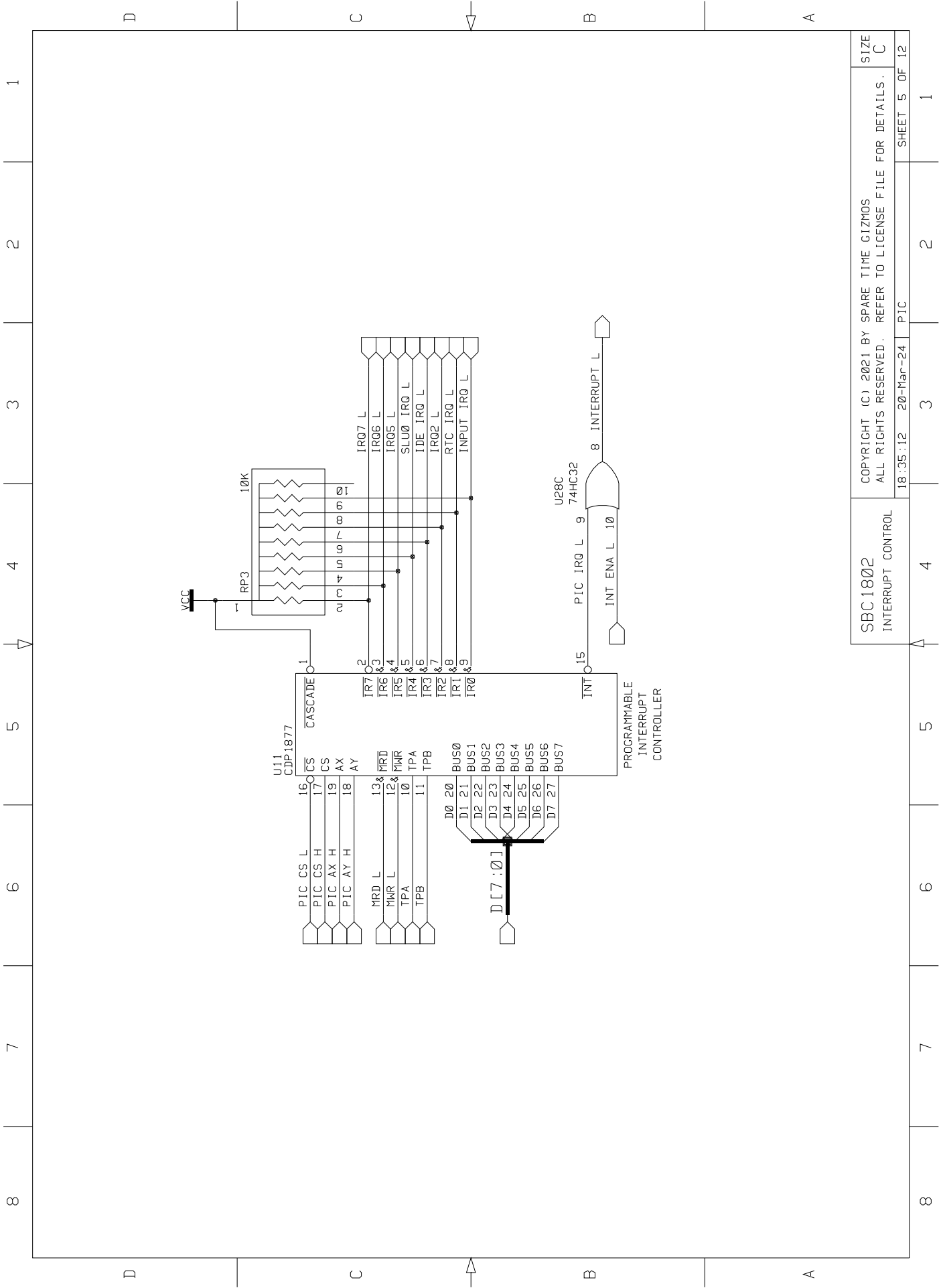
8 7 6 5 4 3 2 1





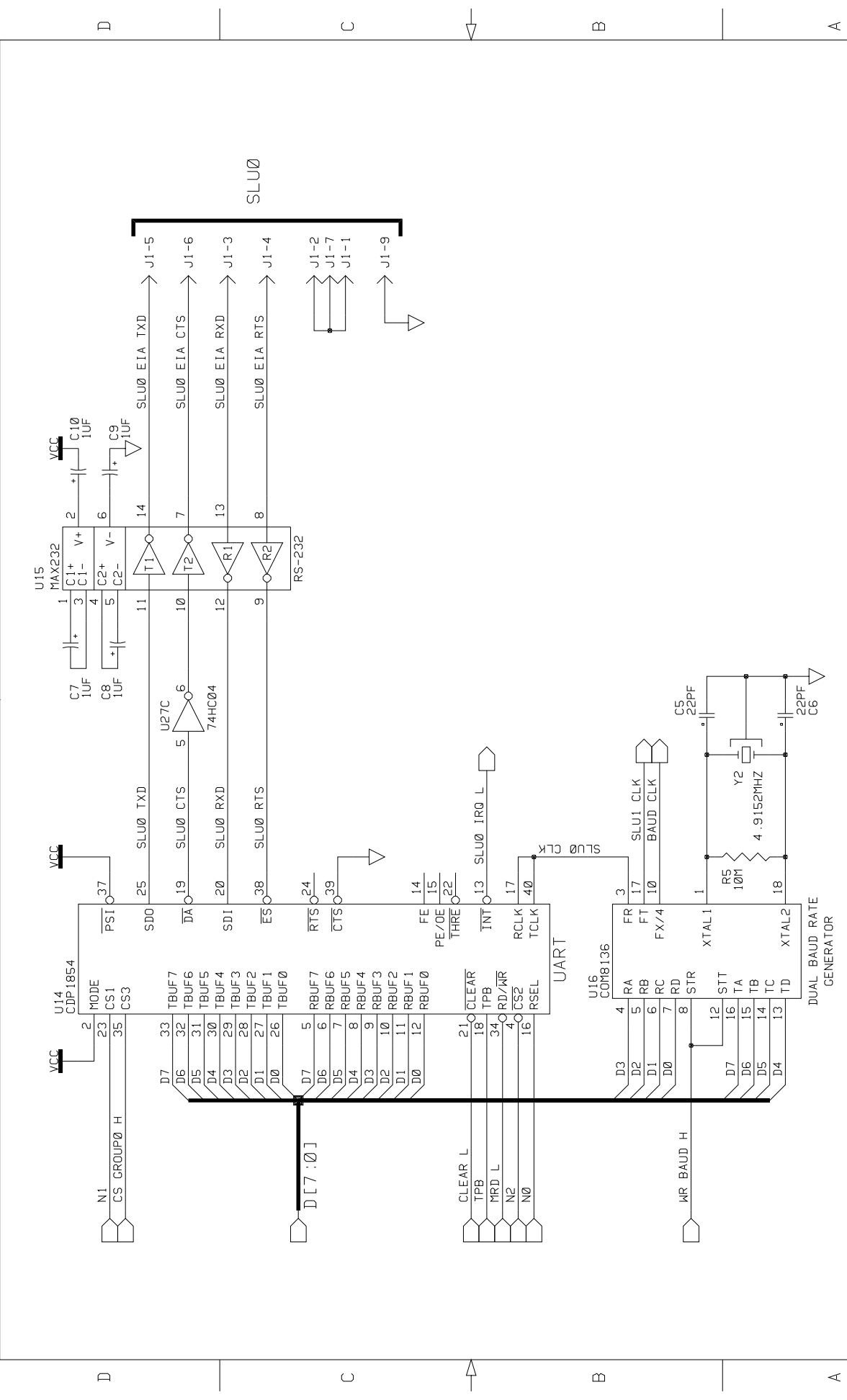
SBC1802 I/O DECODING	COPYRIGHT (C) 2021 BY SPARE TIME GIZMOS ALL RIGHTS RESERVED. REFER TO LICENSE FILE FOR DETAILS.				SIZE C	
	19:11:34	20-Mar-24	10		SHEET 4 OF 12	

8	7	6	5	4	3	2	1
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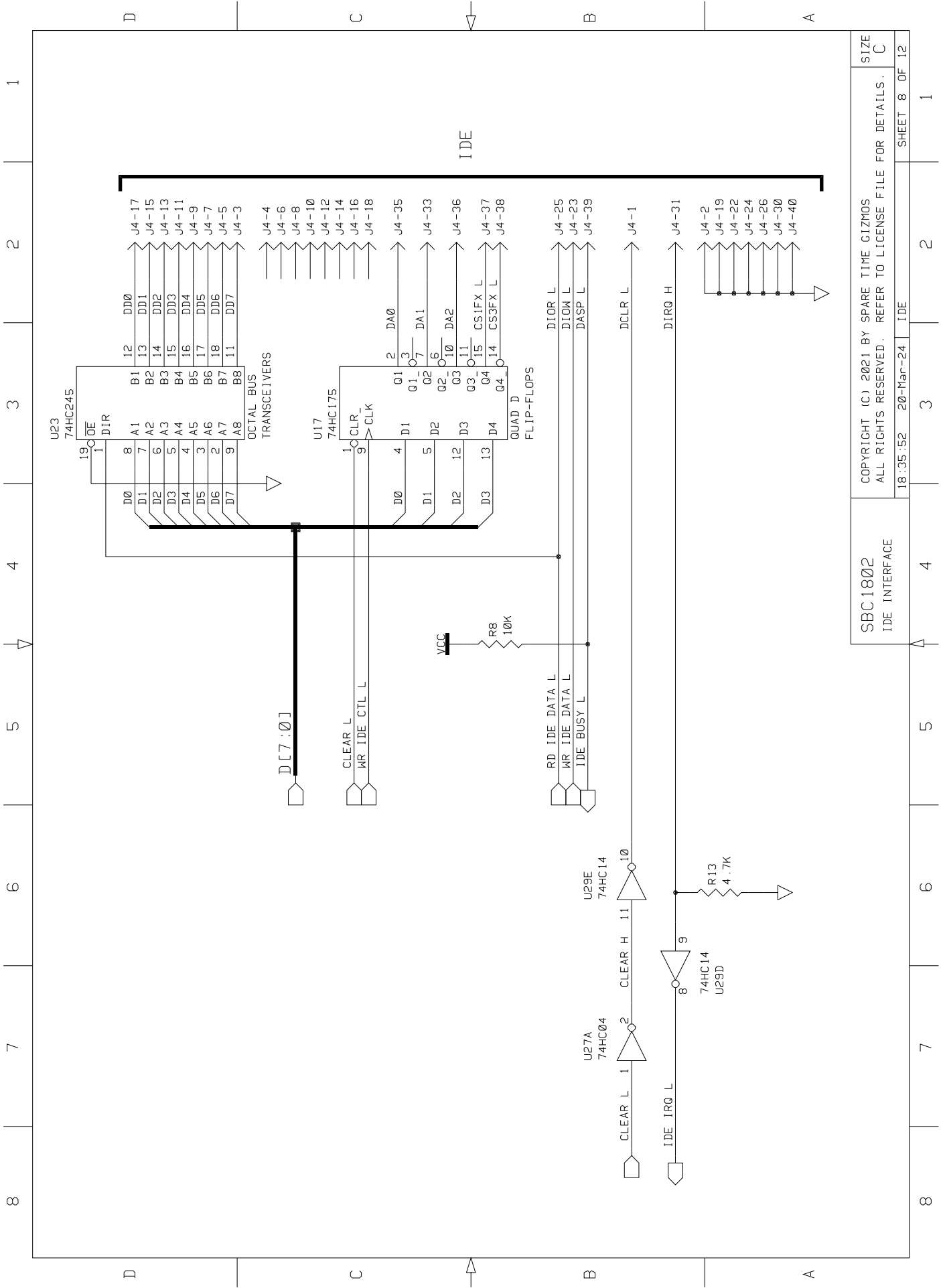


SBC1802 INTERRUPT CONTROL		COPYRIGHT (C) 2021 BY SPARE TIME GIZMOS ALL RIGHTS RESERVED. REFER TO LICENSE FILE FOR DETAILS.		SIZE C	
18:35:12	20-Mar-24	PIC	5	OF	12

1 2 3 4 5 6 7 8

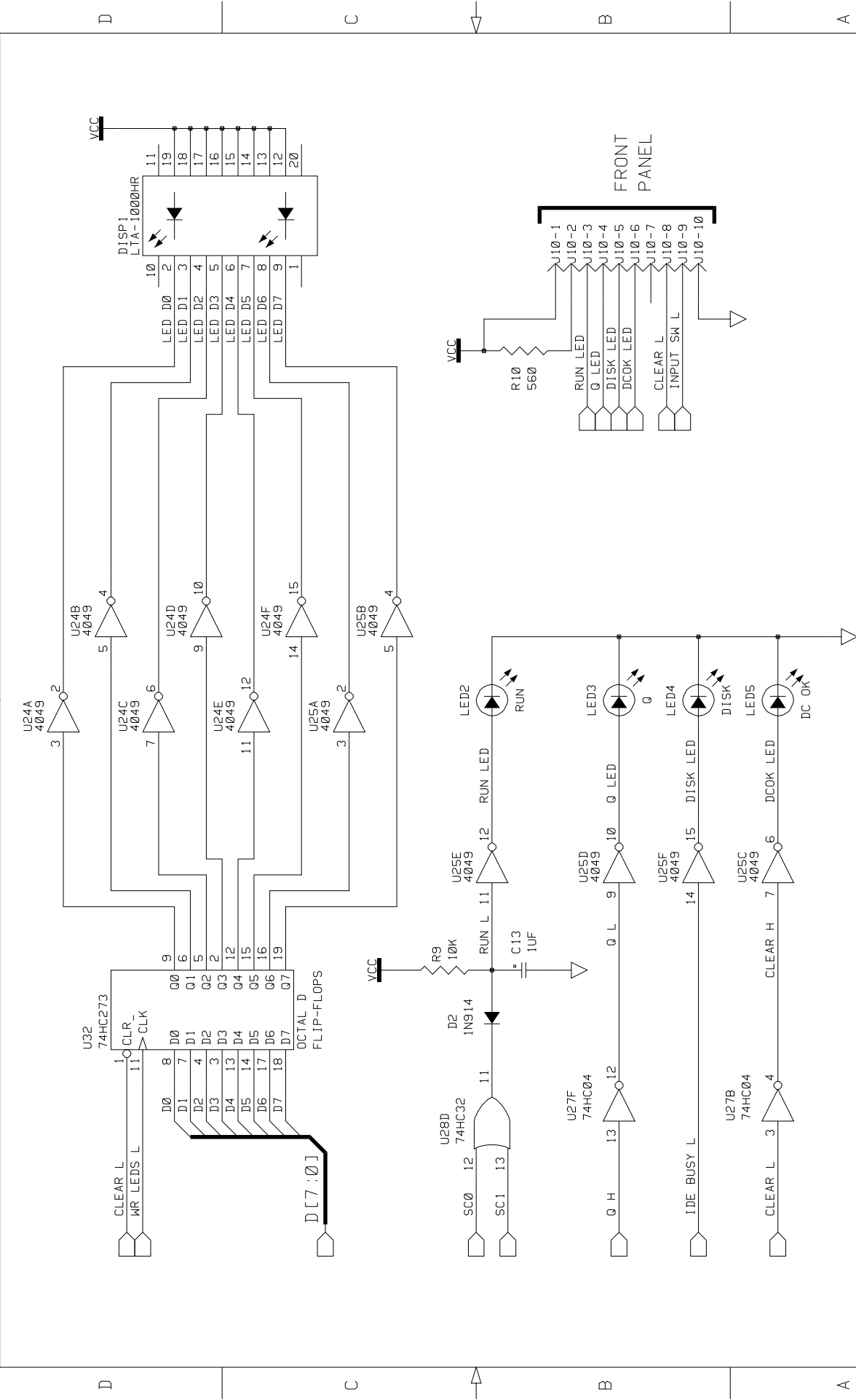






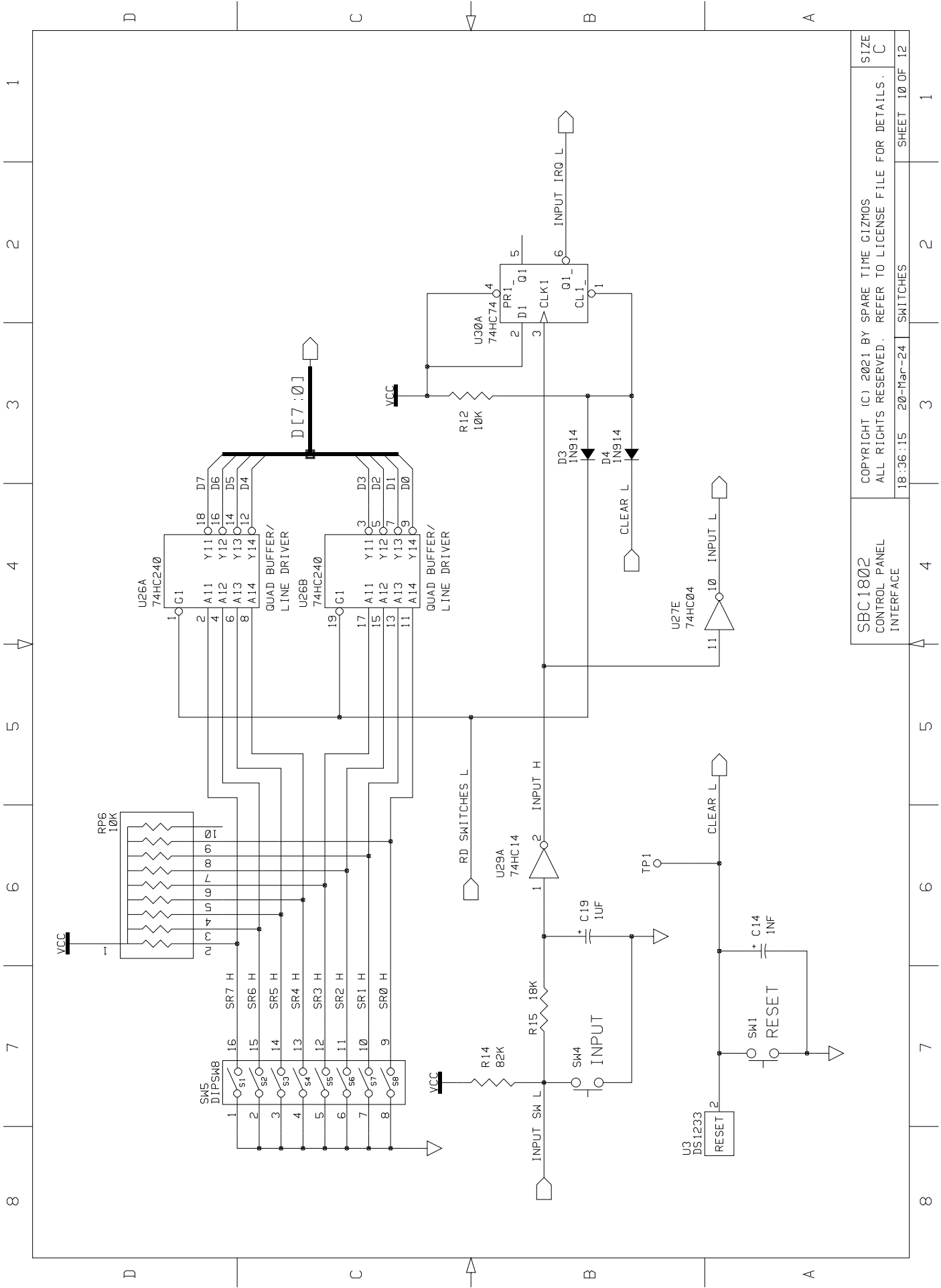


8 7 6 5 4 3 2 1



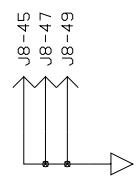
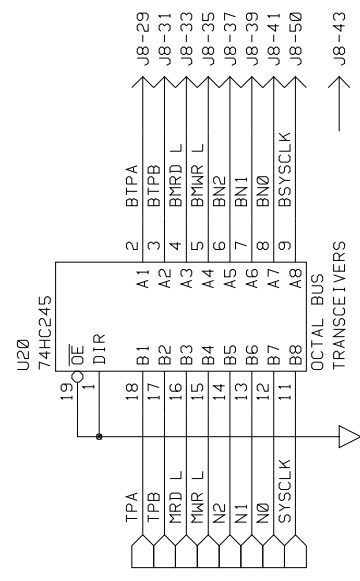
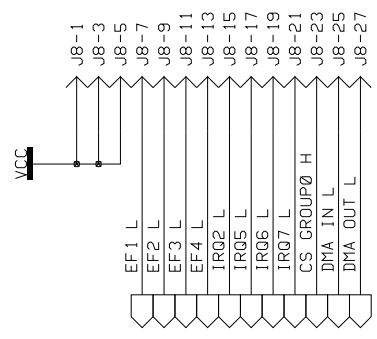
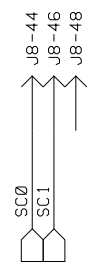
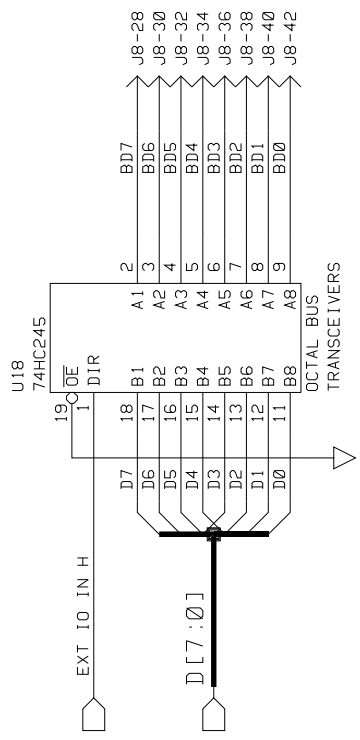
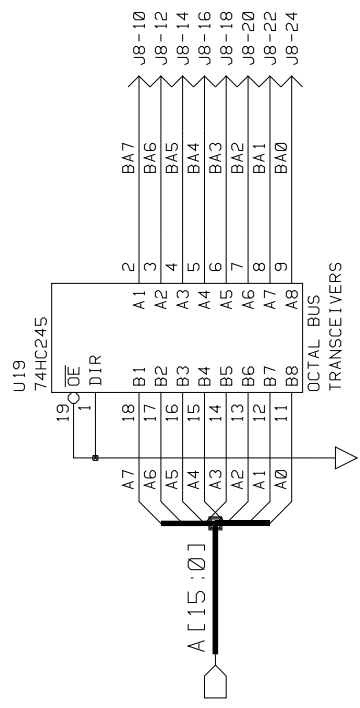
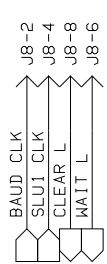
SBC1802 SWITCHES & LEDs				COPYRIGHT (C) 2021 BY SPARE TIME GIZMOS ALL RIGHTS RESERVED. REFER TO LICENSE FILE FOR DETAILS.				SIZE C	
08:12:10				21-Mar-24				SHEET 9 OF 12	

8 7 6 5 4 3 2 1



1 2 3 4 5 6 7 8

D C B A



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	18:36:26	20-Mar-24	CONN	3	2	1	SHEET	11	OF	12		

