ES204 Digital Systems
LAB Assignment - 1

Indian Institute of Technology, Gandhinagar
January 10, 2024
Time & Venue: Tuesday 8:30-9:50am [7/108]

Submission deadline: Jan 10, 2024 (9:50 am)
Marks : 20

For each of the questions, write a Verilog code. You also need to create a **testbench** and show the simulation results.

1. **[20 Marks]** Implement a function with finds the Prime numbers. The input is 4-bit.

   (a) Structural code
   (b) continuous assignment