

Cycle	Instruction Retired	Reason
1	-	Reset asserted
2	-	Reset asserted
3	-	Instructions still in pipeline
4	-	Instructions still in pipeline
5	-	Instructions still in pipeline
6	-	Instructions still in pipeline
7	0x0800 NOP	When reset deasserted, no instruction in EX so NOP is passed
8	0xC000 LBI R0, 0	
9	0xC52B LBI R5, 43	
10	0xC62B LBI R6, 43	
11	0xC72B LBI R7, 43	
12	0x8820 LD R1, R0, 0	
13	0x0800 NOP	ST reading from R1 which has not been written to by LD (stall)
14	0x0800 NOP	ST reading from R1 which has not been written to by LD (stall)
15	0x0800 NOP	ST reading from R1 which has not been written to by LD (stall)
16	0x81A0 ST R5, R1, 0	
17	0x8822 LD R1, R0, 2	
18	0x0800 NOP	ST reading from R1 which has not been written to by LD (stall)
19	0x0800 NOP	ST reading from R1 which has not been written to by LD (stall)
20	0x0800 NOP	ST reading from R1 which has not been written to by LD (stall)
21	0x81C1 ST R6, R1, 1	
22	0x8824 LD R1, R0, 4	
23	0x0800 NOP	ST reading from R1 which has not been written to by LD (stall)
24	0x0800 NOP	ST reading from R1 which has not been written to by LD (stall)
25	0x0800 NOP	ST reading from R1 which has not been written to by LD (stall)
26	0x81E1 ST R7, R1, 1	
27	0x0000 HALT	