



SPARSH AGARWAL  
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COMP R0  $\rightarrow$  compare read  
COMP R1R  $\rightarrow$  compare write

Aceess unde = idle  $\rightarrow$  menard  
Aceess read = idle  $\rightarrow$  PRE WRITEN }

redundant  
can be  
achieved  
without  
these  
paths)