

# Error Correction Coding in Passive UHF Gen2 Communications

CSE 4344

Charlie A. Albright  
Computer Science and  
Engineering Department  
Southern Methodist University  
Dallas, Texas USA  
calbright@smu.edu

Spencer A. Kaiser  
Computer Science and  
Engineering Department  
Southern Methodist University  
Dallas, Texas USA  
skaiser@smu.edu

Luke W. Oglesbee  
Computer Science and  
Engineering Department  
Southern Methodist University  
Dallas, Texas USA  
loglesbee@smu.edu

## 1. INTRODUCTION

The purpose of this study is to explore possible error-correcting alternatives to the simple Cyclic Redundancy Check (CRC) codes used in Ultra-High Frequency (UHF) Gen2 RFID communications. We will evaluate the complexity added to the RFID system when using various types of error-correcting codes and analyze their effects on the performance of the system. We will also compute the additional circuitry needed to implement our alternatives to CRC, and then weigh the benefits and cost of implementing this particular Error-Correcting Code (ECC) into RFID communications.

As of current, RFID uses Cyclic Redundancy Checks (CRC) to detect errors that happen when the information is transmitted wirelessly from Tag to Reader and from Reader to Tag. However, even if these errors are detected, the system is not able to correct the error, so the result is thrown out and another request for the Tag's ID is made. We propose that if the RFID system was able to correct one-bit or even multiple-bit errors, RFID's reliability and overall success would benefit significantly. By having an error-correcting system implemented, the Reader would be able to identify the Tag in less reads on average, and it would potentially improve the range at which the Reader could identify the Tag.

## 2. METHODOLOGY

### SHOULD WE KEEP SOME OR ALL OF THIS?

The first major milestone in approaching our research problem will be to develop a firm understanding of the implementation and execution of Cyclic Redundancy Check (CRC) in the context of RFID interrogators. This will be accomplished through an analysis of the Class-1 Generation-2 UHF RFID documentation provided by EPCglobal and by examining past work in the RFID communications field, specifically with regards to the use of CRC.

After establishing a thorough understanding of CRC, our next step will be to delve into the execution of error-correcting code. We will do this by reviewing past work that identifies the key differentiations and variations in performance between the two codes. We will then identify one or more specific error-correcting codes that we believe to be most applicable for use in RFID systems and conduct further research regarding these specific codes.

Finally, once we have developed a deep understanding of

CRC and identified one or more error-correcting codes for use in an RFID system, we will transmission error rate metrics for RFID systems to simulate the effect of these error correcting codes in terms of tag reading efficiency, added complexity to the tag and interrogator, and overall performance increase for the system.

## 3. PREVIOUS WORK

In their article, A New Ultralightweight RFID Authentication Protocol with Permutation, authors Tian, Chen, and Li propose implementing a strong but relatively simple protocol that implements permutation in order to establish authenticity between Reader and Tag [4]. The last messages exchanged in their protocol are sent again by the reader to resist against desynchronization attacks.

Authors Grzegorz Smietanka, Jan Geldmacher and Jurgen Gotze proposed the implementation of Forward Error Correction (FEC) in their article, Error Detection Based on Correlation Analysis for BCH Encoded UHF RFID Communication [3]. They say the FEC could be implemented easily because of BCH code's similarity to CRC.

A Secure RFID Authentication Protocol Adopting Error Correction Code talks about using a one-way hash to establish authenticity and prevent intentional transmission manipulation, which CRC is not capable of detecting. By hashing the Tag's ID, a secret key, and random challenge numbers, the authors claim that mutual authentication can be achieved [1].

Another lightweight protocol for de-synchronization is proposed by authors Zhou, Zhang, Luo, and Wong in their article A lightweight anti-desynchronization RFID authentication protocol. They claim that using the one-way hash and XOR functions while the backend server keeps a history of all shared secret keys prevents desynchronization and is capable of finding intentional errors [5].

Finally, On Error Performance Improvements of Passive UHF RFID Systems via Syndrome Decoding discusses using the existing CRC error detection implementation to correct single bit errors. Using a lookup table with various CRC codes and other information stored in it, CRC becomes capable of correcting single bit errors [2].

## 4. IMPLEMENTATION AND TESTING

In order to test the effects of adding an error-correcting code in place of the CRC that is already in place for RFID

communications, we decided to implement a virtual testing environment comprised of completely software. We put considerable time and thought into carefully choosing which language to implement our environment in, and after weighing the pros and cons, we chose to write our testing modules in Python. We felt that Python's unique functionality, ingenious string manipulation methods, and overall lightweight qualities would be well suited for rapid processing of simulated transmissions.

The general setup of our testing environment runs in a main file, with specific Python modules included for each Noise model and ECC implementation, which are written in separate files. Our main file simulates an RFID reader unit by opening a text file containing a sample packet on each line. The testing environment then runs each packet through a series of Error-Correcting Codes and Noise models and analyzes the results of the fake transmission, keeping track of variable such as the total number of transmissions, the number of times a retransmission occurred, and how often the ECC was able to correct the errors. the Following algorithm gives an overview of the overall process that a packet goes through:

**Algorithm 4.1:** PACKET ANALYSIS(*Parameters*)

```

for each Packet ∈ File
do {
  Encode Packet with CRC
  while Transmission is not successful
  do {
    Send Packet though Noise module
    Decode resulting Packet to check for errors
    if Errors Detected
    then { Retransmission occurs
    else if Errors exist but undetected
    then { Undetected Error occurs
    else Successful transmission
  }
  Encode Packet With Hamming
  while Transmission is not successful
  do {
    Send Packet though Noise module
    Decode resulting Packet to check for errors
    if Errors Detected and Not Fixed
    then { Retransmission occurs
    else if Errors exist but undetected
    then { Undetected Error occurs
    else Successful transmission
  }
}

```

*Print results to screen*

**Algorithm 4.2:** HAMMING(*Signal*)

```

procedure ENCODE(Signal)
  Insert empty parity bits into Signal
  for each parityBit in Signal
  do Signal[parityBit - 1] ← Parity(Signal, parityBit)
  return (Signal)

procedure DECODE(Signal)
  for each parityBit in Signal
  do { if Parity(Signal, parityBit) == 1
    then ParityErrors push parityBit
  }
  if ParityErrors
  then { badBit ← sum of ParityErrors
    if badBit ≥ length(Signal)
    then return (False)
    else Signal[badBit - 1] ← !Signal[badBit - 1]
  }
  Remove parity bits from Signal
  return (Signal)

procedure PARITY(Signal, parityBit)
  i ← parityBits
  comp ← Signal[parityBits - 1]
  while i < length(Signal)
  do {
    j ← i
    while j < i + parityBit and j < length(Signal)
    do { comp = comp ⊕ Signal[j]
      j ← j + 1
    }
    i ← i + 2 · parityBit
  }
  return (comp)

```

**Algorithm 4.3:** GAUSSIAN NOISE(*Signal*, *noiseRatio*)

```

Noise ← random value (0-1) for each element of Signal
for i ← 0 to length(Signal)
do { if Signal[i] < noiseRatio
  then Signal[i] ← !Signal[i]
  else Signal[i] ← Signal[i]
}
return (Signal)

```

**Algorithm 4.4:** BURST NOISE(*Signal*, *noiseRatio*, *maxBitFlip* = 16)

```

Noise ← random float (0-1) for each element of Signal
Length ← random int (0-maxBitFlip) for each element of Signal
i ← 0
while i < length(Signal)
do { if Signal[i] < noiseRatio
  then { for j ← i to i + Length[i]
    do { Signal[j] ← !Signal[j]
      i ← j + 1
    }
  }
  else i ← i + 1
}
return (Signal)

```

## 5. REVISED RESEARCH PLAN

11/11/14: Interim report

11/12 - 11/16: Implement missing Noise and ECC models

11/17 - 11/21: Perform final analysis on effectiveness on each Error Correcting Code for each Noise Model and collect data for comparison

11/22 - 11/27: FINAL WORK PERIOD - Make final additions to paper and revise

11/28: Assign presentation sub-sections to team members

11/28 - 12/1: Team members individually create presentation components

12/1 - TBD: Compile final presentation from components and practice delivery

**TBD (12/2/14 - 12/4/14): Final Presentation**

## 6. ROAD BLOCKS AND CONCERNS

The major road block we have is finding a quantitative method to measure the efficacy of a protocol in a theoretical environment. Most past work on efficacy of error-correcting protocols in RFID communication has been conducted in controlled laboratory environments. While a statistical analysis based on the chance of message bits being corrupted under various conditions is plausible, we have not yet found any pre-existing methods for conducting the analysis.

## 7. REFERENCES

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