Local Reasoning about the Presence of Bugs:Incorrectness Separation Logic

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Overview

- ► A brief review of the incorrectness logic.
- Introduction to separation logic.
- ► ISL (incorrectness separation logic).
- ▶ Investigation of the tool INFER.

Incorrectness Logic

Let ϵ range over a collection of exit conditions, to include at lease "ok" and "er". An *under-approximate triple* is of the form:

$$[p]C[\epsilon:q]$$

meaning q under-approximates the states when C exits via ϵ starting form states in p.

Sometimes, we write [p]C[ok:q][er:r] as shorthand for [p]C[ok:q] and [p]C[er:r]

An important point: the triple $[p]C[\epsilon:q]$ express the reachability property that involves termination. Every state in the result is reachable from some states in the presumption

Separation Logic

Separation logic is an extension of Hoare logic, which employs novel logical operators and able to handle heap-manipulating programs with aliasing.

More types of variables: x = 1 and $y \mapsto 1$

Therefore we introduce a new operator \ast read as "and seperately".

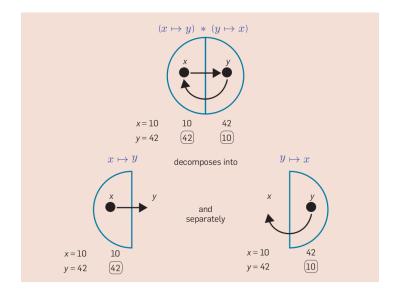
$$x1 \mapsto -* \cdots * xn \mapsto -$$

 $\forall x, v, v'.x \mapsto v * x \mapsto v' \Rightarrow$ false

We also use emp to represent the empty heap.



SL: Semantic



SL: Semantic

Let $s: Vars \rightarrow Ints$ and $h: Nats \rightarrow Ints$ be the "store" and "heap".

s is similar to the evaluation.

$$s, h \models E \mapsto F$$
 iff $\{\llbracket E \rrbracket s\} = dom(h) \text{ and } h(\llbracket E \rrbracket s) = \llbracket F \rrbracket s$
 $s, h \models \text{emp}$ iff $h = \llbracket 1 \rrbracket$ is the empty heap
 $s, h \models P * Q$ iff $\exists h_0, h_1. \ h_0 \bullet h_1 = h, \ s, h_0 \models P \text{ and } s, h_1 \models Q$
 $s, h \models P \twoheadrightarrow Q$ iff $\forall h'. \text{if } h' \# h \text{ and } s, h' \models P \text{ then } s, h \bullet h' \models Q$
 $s, h \models \text{false}$ never
 $s, h \models P \Rightarrow Q$ iff if $s, h \models P \text{ then } s, h \models Q$
 $s, h \models \forall x. P$ iff $\forall v \in \text{Ints.} [s \mid x \mapsto v], h \models P$

 $h \bullet h' =$ union when domains disjoint, undefined otherwise h# h' means domains disjoint

Boolean constructs $\neg P$, $P \land Q$, $P \lor Q$, $\exists x.P$ defined in usual way from false, \forall , \Rightarrow .



SL: Rules and Axioms

Frame Rule
$$\frac{\{p\} C \{q\}}{\{p*r\} C \{q*r\}}$$

$$x \mapsto v * x \mapsto v' \Leftrightarrow \text{false} \qquad x \mapsto v * \text{emp} \Leftrightarrow x \mapsto v$$

$$\text{Local Axioms} \qquad \{x \mapsto -\} [x] := v \{x \mapsto v\}$$

$$\{x \mapsto v\} \ y := [x] \{x \mapsto v \land y = v\}$$

$$\{\text{emp} \} \ x := \text{alloc}() \{\exists I. \ I \mapsto -\land x = I\}$$

$$\{x \mapsto -\} \ \text{free}(x) \{\text{emp} \}$$

The primitive statements of the language update or access only one memory cell each time. These local axioms are extremely concise and able to do local reasoning.

Frame rule allows to extend the reasoning from one cell to multiple cell.

SL: Proof Example

```
{p} C {q}
                                                              \{x \mapsto -\} [x] := v \{x \mapsto v\}
\{p*r\} C \{q*r\}
                      \uparrow \{ X \mapsto - * V \mapsto - * Z \mapsto - \}
                   \lim_{n \to \infty} \lim_{n \to \infty} \left\{ \begin{array}{l} x \mapsto - \\ x \mapsto 1 \end{array} \right\} 
                      \bigcup \{ X \mapsto 1 * V \mapsto - * Z \mapsto - \}
                                         [y] := 2;
                                         [z] := 3;
                        \{x \mapsto 1 * y \mapsto 2 * z \mapsto 3\}
```

SL: Soundness and Completeness

Theorem (Soundness)

SL is sound:

$$\{p\}C\{q\}$$
 is provable $\Rightarrow \{p\}C\{q\}$ is true

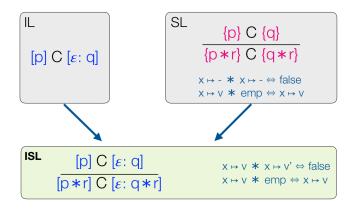
SL is not complete however.

Incompleteness: Example

Counter-example.

```
| lossy: forgets that x held a location | \{\text{emp}\}\ x:= \text{alloc()} \{\exists I.\ I \mapsto -\land x=I \} | \{x \mapsto -\} \text{ free(x) } \{\text{ emp }\}
```

ISL: Incorrectness Separation Logic



ISL: Problem of Combining

ISL: Problems of Combining

ISL: Solution

The solution for the frame rule is to remember that we allocated memory for the pointer even if we freed them.

Old free rule:

$$[x \mapsto v] free(x) [ok : emp]$$

New free rule:

$$[x \mapsto v] free(x) [ok : x \not\mapsto]$$

$$X \mapsto V * X \mapsto V' \Leftrightarrow \text{false}$$

 $X \mapsto V * \text{emp} \Leftrightarrow X \mapsto V$
 $X \mapsto V * X \not\mapsto \Leftrightarrow \text{false}$
 $X \not\mapsto * X \not\mapsto \Leftrightarrow \text{false}$

ISL: Target Programs and Rules

```
COMM \ni \mathbb{C} ::= skip \mid x := e \mid x := * \mid assume(B) \mid local x in \mathbb{C} \mid \mathbb{C}_1; \mathbb{C}_2 \mid \mathbb{C}_1 + \mathbb{C}_2 \mid \mathbb{C}^*
                          \mid x := \mathtt{alloc}() \mid \mathtt{L} : \mathtt{free}(x) \mid \mathtt{L} : x := [y] \mid \mathtt{L} : [x] := y \mid \mathtt{L} : \mathtt{error}
                 if B then \mathbb{C}_1 else \mathbb{C}_2 \triangleq (\operatorname{assume}(B); \mathbb{C}_1) + (\operatorname{assume}(!B); \mathbb{C}_2)
                                      while (B) \mathbb{C} \triangleq (assume(B); \mathbb{C})^*; assume(!B)
                                         assert(B) \triangleq (assume(!B); error) + assume(B)
                                    x := malloc() \triangleq x := alloc() + x := null
                                                                                FREENULL
FreeEr
\vdash [x \nleftrightarrow] \text{ L: free}(x) [er(L): x \nleftrightarrow]
                                                                                \vdash [x=\text{null}] \text{ L: free}(x) [er(L): x=\text{null}]
LOAD
                                                                                                  STORE
\vdash [x = x' * y \mapsto e] \text{ L: } x := [y] [ok : x = e[x'/x] * y \mapsto e[x'/x]] \quad \vdash [x \mapsto e] \text{ L: } [x] := y [ok : x \mapsto y]
Loader
                                                                                            STOREER
\vdash [y \not\mapsto ] \text{ L: } x := [y] [er(L) : y \not\mapsto ]
                                                                                           \vdash [x \nleftrightarrow] L: [x] := y [er(L): x \nleftrightarrow]
LoadNull
                                                                                  STORENULL
\vdash [y=\text{null}] \text{ L: } x := [y] [er(\text{L}): y=\text{null}]
                                                                                  \vdash [x=\text{null}] \text{ L: } [x] := y [er(L): x=\text{null}]
```

ISL: Semantic

Adapt original semantic incorrectness logic to ISL semantic

```
\llbracket . \rrbracket : \text{Comm} \to \text{Exit} \to \mathcal{P}(\text{State} \times \text{State}) \sigma \in \text{State} \triangleq \text{Store} \times \text{Heap}
s \in \text{Store} \triangleq \text{Var} \xrightarrow{\text{fin}} \text{Val} \qquad h \in \text{Heap} \triangleq \text{Loc} \xrightarrow{\text{fin}} \text{Val} \uplus \{\bot\} \qquad l \in \text{Loc} \subset \text{Val}
                            [skip]ok \triangleq \{(\sigma, \sigma) \mid \sigma \in STATE\}
                                                                                                                     \llbracket \mathtt{skip} 
rbracket er(-) 	riangleq \emptyset
                         [x := e] ok \triangleq \{((s, h), (s[x \mapsto s(e)], h))\}
[x := e] er(-) \triangleq \emptyset
                         \llbracket x := * \rrbracket ok \triangleq \{((s, h), (s[x \mapsto v], h)) \mid v \in VAL\} \llbracket x := * \rrbracket er(-) \triangleq \emptyset
            [\![\![ assume(B) ]\!]\!] ok \triangleq \{(\sigma,\sigma) \mid \sigma = (s,h) \land s(B) \neq 0\} \qquad [\![\![ assume(B) ]\!]\!] er(-) \triangleq \emptyset
                                                                                              [\![ L: \mathtt{error} ]\!] er(L') \triangleq \{(\sigma, \sigma) \mid L=L'\}
                    [L: error] ok \triangleq \emptyset
                           [\![\mathbb{C}_1; \mathbb{C}_2]\!] \epsilon \triangleq \left\{ (\sigma, \sigma') \middle| \begin{array}{l} \epsilon \neq ok \wedge (\sigma, \sigma') \in [\![\mathbb{C}_1]\!] \epsilon \\ \vee \exists \sigma'' \cdot (\sigma, \sigma'') \in [\![\mathbb{C}_1]\!] ok \wedge (\sigma'', \sigma') \in [\![\mathbb{C}_2]\!] \epsilon \end{array} \right\}
      [local x in \mathbb{C}] \epsilon \triangleq \{((s[x \mapsto v], h), (s'[x \mapsto v], h')) \mid ((s, h), (s', h')) \in [\mathbb{C}] \epsilon \}
                      [\mathbb{C}_1 + \mathbb{C}_2] \epsilon \triangleq [\mathbb{C}_1] \epsilon \cup [\mathbb{C}_2] \epsilon
                                    [\![\mathbb{C}^\star]\!]\epsilon \triangleq \bigcup_{i \in \mathbb{N}} [\![\mathbb{C}^i]\!]\epsilon \quad \text{with} \quad \mathbb{C}^0 \triangleq \mathtt{skip} \quad \text{and} \quad \mathbb{C}^{i+1} \triangleq \mathbb{C} \colon \mathbb{C}^i
```

ISL: Semantic

Semantic related to memory operations.

ISL: Soundness

Theorem (Soundness of ISL)

For all $p, \mathbb{C}, q, \epsilon$, if $\vdash [p]\mathbb{C}[\epsilon : q]$, the $\models [p]\mathbb{C}[\epsilon : q]$.

The definition of $\models [p]\mathbb{C}[\epsilon:q]$ is $\forall \sigma_q \in q. \exists \sigma_p \in p. (\sigma_p, \sigma_q) \in [[\mathbb{C}]]\epsilon$

Tool: Infer

Current Version: 0.17.0

Only able to do inference with separation logic.

./infer run -- javac test.java

- CONSTANT ADDRESS DEREFERENCE
- MEMORY_LEAK
- NULLPTR_DEREFERENCE
- STACK_VARIABLE_ADDRESS_ESCAPE
- USE_AFTER_DELETE
- USE_AFTER_FREE
- USE_AFTER_LIFETIME
- VECTOR_INVALIDATION

In the doc of next version:

./infer --pulse --pulse-intraprocedural-only