Progess Report 1

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Overview of the Progress

- ► Survey of Tools: CBMC, NuSMV and CPACHECKER
- Reading paper: things related to separation logic.
- Survey of Papers and Slides:
 - ► Slides from CBMC site.
 - Alessandro Cimatti et al. Integrating BDD-based and SAT-based Symbolic Model Checking.
 - E. Clarke et al. Symbolic Model Checking.
 - NUSMV 2.5 Tutorial and other related slides.
 - Slides from CPACHECKER site.
- Usage of the Tools and brief introduction to Algorithms.

Tool Survey: CBMC

CBMC is a bounded model checker for C and C++.

Functionalities:

```
Program instrumentation options:
 --bounds-check
                              enable array bounds checks
 --pointer-check
                              enable pointer checks (always enabled for Java)
 --memory-leak-check
                              enable memory leak checks
 --div-by-zero-check
                              enable division by zero checks
 --sianed-overflow-check
                              enable signed arithmetic over- and underflow check
 --unsigned-overflow-check
                              enable arithmetic over- and underflow checks
 --pointer-overflow-check
                              enable pointer arithmetic over- and underflow chec
 --conversion-check
                              check whether values can be represented after type
 cast
 --undefined-shift-check
                              check shift greater than bit-width
 --float-overflow-check
                              check floating-point for +/-Inf
                              check floating-point for NaN
 --nan-check
 --no-built-in-assertions
                              ignore assertions in built-in library
 --no-assertions
                              ignore user assertions
                              ignore user assumptions
 --no-assumptions
 --error-label label
                              check that label is unreachable
 --cover CC
                              create test-suite with coverage criterion CC
                              memory consistency model for concurrent programs
 --mm MM
 --reachability-slice
                              remove instructions that cannot appear on
                              a trace from entry point to a property
 --reachability-slice-fb
                              remove instructions that cannot appear on
                              a trace from entry point through a property
 --full-slice
                              run full slicer (experimental)
```

Insight of Algorithm

```
clexma@clexma-ThinkPad-P52s:~/Desktop/Disk_D/gitRepos/Tex/TexBak/llvm_discussion1/cbmc$_cbmc_buffer.c --bo
CBMC version 5.10 (cbmc-5.10) 64-bit x86 64 linux
Parsing buffer.c
Converting
Type-checking buffer
Generating GOTO Program
Adding CPROVER library (x86 64)
Removal of function pointers and virtual functions
Generic Property Instrumentation
Running with 8 object bits, 56 offset bits (default)
Starting Bounded Model Checking
size of program expression: 33 steps
simple slicing removed 6 assignments
Generated 1 VCC(s), 1 remaining after simplification
Passing problem to propositional reduction
converting SSA
Running propositional reduction
Post-processing
Solving with MiniSAT 2.2.1 with simplifier
328 variables, 11 clauses
SAT checker: instance is SATISFIABLE
Runtime decision procedure: 0.00503025s
** Results:
[main.array bounds.1] array `buffer' upper bound in buffer[(signed long int)20]: FAILURE
** 1 of 1 failed (1 iteration)
```

Slides from the official website of CBMC.

Tool Survey: NUSMV

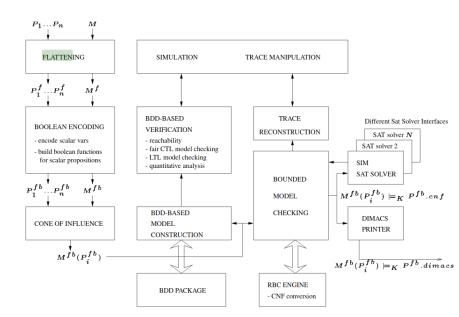
 NuSVM is a NEW tool of Symbolic model checker for finite state systems.

Features:

- Analysis of invariants.
- LTL model checking.
- PSL model checking.
- SAT-based bounded model checking.

The tool is given a SMV language file as input and

Overview of NuSMV



Input Format of NuSMV

```
1 MODULE main
 2 VAR
 3 bit0 : counter cell(TRUE);
4 bit1 : counter cell(bit0.carry out);
 5 bit2 : counter cell(bit1.carrv out):
 6 SPEC
   AG AF bit2.carry out
9 SPEC AG(!bit2.carrv out)
11 MODULE counter cell(carry in)
12 VAR
13 value : boolean:
14 ASSIGN
15 init(value) := FALSE;
    next(value) := value xor carry in:
17 DEFINE
   carry out := value & carry in;
```

./NuSMV couter.smv

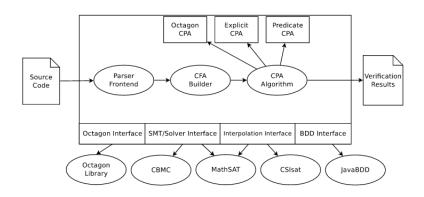
```
-- specification AG (AF bit2.carry_out) is true
-- specification AG !bit2.carry_out is false
-- as demonstrated by the following execution sequence
```

Tool Survey: CPACHECKER

CPACHECKER is a configurable software-verification platform that enables the parsing, analysing and verifying of the source program.

- ► Static Analysize: Data-Flow analysis etc.
- Invariant generation via over-approximation.
- Termination checking.
- **.**..

Architecture



Future Work

- ► A closer look into the source code of these tools especially CPACHECKER.
- Better understanding of the underlying algorithm and techniques.
- ▶ Other tool survey: SPIN, INFER, KLEE, etc.