

ELC 2137 Lab 0: 7-Segment Display with Time-Division Multiplexing

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Summary

Type the summary of your experiment and results here.

Q&A

- What are the three main "groups" of the RTL definition of sequential logic?
- If instead of a counter, you wanted to make a shift register that oved the input bits from right to left (low to high). What would you put on the line $Q_{next} = ???$

Q&A

Answer questions posed in the lab assignment here.

Results

In this section, put your simulation waveforms, results tables, pictures of hardware, and any other required items.

Code

Include all of the code you wrote or modified here.