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(21223)   Roll No. . ... _

B.C.A.-III Sem.

18013

B.C.A. Examination, Dec.-2023

COMPUTER ARCHITECTURE AND

ASSEMBLY LANGUAGE

(BCA-303)

Time : 3:00 Hours]   *[Maximum Marks : 75*

Note : Attempt question from **all** sections as

per Instruction.

Section-A

(Very Short Answer Question)

Note : Attempt **all** question.   5×3=15

1. What is Instruction? Explain.

2. Write an assembly language program to

add two numbers.

P.T.O.

3. What is Macros.

4. What is the full form of Risc.

5. Write about flag register in 8085.

Section-B

(Short Answer Question)

Note : Attempt any **two** question. 2×7½=15

6. What is serial communication? Explain.

7. What do you understand by DMA. Explain

Giving a diagram.

8. Define Interrupt. Explain its various

type.

Section-C

(Detail Answer Questions)

Note : Attempt any **three** question.

3×15=45

9. Discuss the following in brief.

    (a) General Register organization

    (b) Index Register

    (c) Input/output processor (IOP)

18013/2

10. Write a short notes on:

    (a) Memory Influfacing Memory

    (b) Architecture of 8085.

    (c) Floating point Representation.

11. (a) What is various data transfer schemes? Briefly discuss each scheme.

    (b) Explain the need of different addressing mode by taking suitable example.

    (c) Explain the role of register transfer in computer Architecture.

12. What is priority Interrupt? Explain polling and chaining periority.

13. (a) List the five important characteristics of Rise Architecture. *inst*

    (b) Explain Interrupt driven I/o in detail.

    (c) Differentiate between Hardwired control unit VS Microprogrammed control unit.