CS-E4340 Cryptography: Exercise Sheet 9

—Side-Channel Analysis, White-Box Cryptography—

Submission deadline: November 21, 2022, 11:30, via MyCourses

Each exercise can give up to two participation points, 2 for a mostly correct solution and 1 point for a good attempt. Overall, the exercise sheet gives at most 4 participation points. We encourage to **choose** exercises which seem **interesting** and/or adequately challenging to you.

Exercise Sheet 6 is intended to help...

- (a) ...learn to perform cryptanalysis (Ex. 1).
- (b) ...think about meaningful security definitions for white-box cryptography (Ex. 2-Ex.6). In Definition 1, we provide a general definition for a white-box compiler. This compiler takes as input the secret key of a symmetric encryption scheme, and outputs a white-box encryption program. The white-box encryption program takes as input a message m and a nonce nc, and outputs an encryption of m. The white-box program is functionally equivalent to $\operatorname{enc}(k, m, nc)$.

Exercise 1 (Side-channel analysis). Below we see a description of the Montgomery Ladder using projective coordinates. This algorithm is a popular choice for implementing the scalar multiplication in elliptic curve cryptosystems implemented in hardware. We recall that in such cryptosystems, the scalar k corresponds to the secret key and the multiplication kP is performed, e.g., for generating a signature or performing encryptions. The algorithm goes through an initialization phase (line 1), where 4 registers are set with some initial values. The algorithm then enters a loop (lines 2 to 10). The loop is executed once for each bit of the scalar k and depending on the bit value we execute either lines 4 and 5 or lines 7 and 8. Note that in both cases, the loop consists of the same arithmetic operations (multiplication, addition, squaring and overwriting the value of a register).

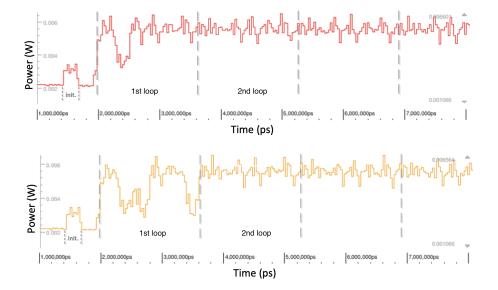
Figure 1 shows two power traces obtained during the execution of the algorithm (these power traces correspond actually to the initialization period and to the first 4 loop iterations of the algorithm). For the power trace above, a scalar $k = \kappa_1$ was used, while for the power trace below, a different scalar $k = \kappa_2$ was used. From observing these power traces, are you able to extract any bits from any of the two scalars?

Algorithm 1 Montgomery algorithm for the kP-operation using projective coordinates

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Input: k = (k_{l-1}, ..., k_1, k_0)_2 with k_{l-1} = 1, P = (x, y) \in E(GF(2^m)).
Output: kP = (x_1, y_1).
 1: X_1 \leftarrow x, Z_1 \leftarrow 1, X_2 \leftarrow x^4 + b, Z_2 \leftarrow x^2.
 2: for i from l-2 downto 0 do
          if k_i = 1 then
             T \leftarrow Z_1, Z_1 \leftarrow (X_1Z_2 + X_2Z_1)^2, X_1 \leftarrow xZ_1 + X_1X_2TZ_2, T \leftarrow X_2, X_2 \leftarrow X_2^4 + bZ_2^4, Z_2 \leftarrow T^2Z_2^2.
 5:
 6:
          else
              T \leftarrow Z_2, Z_2 \leftarrow (X_2Z_1 + X_1Z_2)^2, X_2 \leftarrow xZ_2 + X_1X_2TZ_1, T \leftarrow X_1, X_1 \leftarrow X_1^4 + bZ_1^4, Z_1 \leftarrow T^2Z_1^2.
 7:
 8:
 9:
          end if
10: end for
11: x_1 \leftarrow X_1/Z_1.
12: y_1 \leftarrow y + (x + x_1)[X_1 + xZ_1)(X_2 + xZ_2) + (x^2 + y)(Z_1Z_2)]/(xZ_1Z_2).
13: return ((x_1, y_1)).
```

Hints:

1. l is the length of the key, e.g. 233 bits. In this context, we say that the first bit of the key (k_{l-1}) is always a 1. The loop iterations start when we read the second bit (k_{l-2}) .



- 2. P = (x, y) is a point on the elliptic curve, the value of its coordinate x is used in line 1 to initialize some registers (e.g. register X_1). b is a coefficient used for defining the elliptic curve.
- 3. During the first loop iteration, the registers (or variables) X_1, X_2, Z_1 and Z_2 will be initialized with the values set on line 1.
- 4. Note that the Montgomery Ladder is expected to provide robustness against simple power analysis attacks. Thus, you are not really expected to extract *many* bits just from observing these two power traces. Note that most loop iterations look very similar and it is difficult to determine any key dependencies. However the first loop iteration looks different than the rest. Moreover, the first loop iteration for both power traces looks very different.
- 5. Recall that the power consumption of a device depends on the number of gates being switched during the computation. Thus, power consumption (per clock cycle) is highly dependent on the type of operations we are performing and also on the inputs we are processing. Do you think all multiplications demand equally high power? Or does it depend on the values we are multiplying?

Definition (White-box compiler). An algorithm Comp is called a white-box compiler for a symmetric encryption scheme se = (se.enc, se.dec) if for all key values $k \in \{0,1\}^{\lambda}$, all nonces $nc \in \{0,1\}^{\lambda}$ and all messages $m \in \{0,1\}^*$, we have $\Pr[se.enc(k, m, nc) = \mathsf{WB}(m, nc)] = 1$, where the probability is taken over $\mathsf{WB} \leftarrow \mathsf{sComp}(k)$.

Exercise 2 (Security against key extraction). In the lecture we discussed how white-box programs may be susceptible to key-extraction attacks. Achieving security against key extraction is crucial for a white-box program. However as we will see in this exercise, white-box programs need to achieve further properties in order to provide any meaningful security.

Below we provide a definition capturing the property of security against key extraction for white-box programs.¹ Explain that while this definition captures security against key extraction, it does not really capture useful properties we would want from a white-box (cryptographic) program. Give an example of a program which achieves security against key extraction (i.e. is secure

¹ In the literature, this definition might be referred to as *Unbreakability*. The name Unbreakability derives from the fact that when we manage to extract the key of a cryptographic implementation, we say that we *break* the implementation.

in the model below), yet it does not provide any further meaningful security (e.g. confidentiality of plaintexts). For this, you may design a correct but insecure symmetric-encryption scheme $se_{example}$ (assuming an existing secure symmetric-encryption scheme se) and a compiler comp for $se_{example}$ such that the compiler for $se_{example}$ outputs a white-box encryption program which is (1) functional equivalent to the encryption of $se_{example}$, (2) is secure against key extraction and (3) does not achieve any meaningful security in the presence of a white-box adversary.

Hint: Note that the definition below follows a notation style similar to the security definition for one-way functions (see Section 3.1 on the Cryptocompanion (Security definition 1)).

Definition (Security against key extraction). Let $\mathsf{Exp}^{\mathsf{Kextr}}_{\mathsf{WB},\mathcal{A}}(1^{\lambda})$ be a security experiment defined by

$$\frac{\mathsf{Exp}^{\mathsf{Kextr}}_{\mathsf{Comp},\mathcal{A}}(1^{\lambda})}{k \leftarrow \$ \left\{0,1\right\}^{\lambda}} \\ \mathsf{WB} \leftarrow \$ \, \mathsf{Comp}(k) \\ k^* \leftarrow \$ \, \mathcal{A}(\mathsf{WB}) \\ \mathsf{if} \, \, k^* = k \, \, \mathsf{then} \\ \mathsf{return} \, \, 1 \\ \mathsf{return} \, \, 0$$

A white-box compiler Comp for a symmetric encryption scheme se is secure against key extraction if for all PPT adversaries \mathcal{A} the winning probability $\mathsf{Win}^{\mathsf{Kextr}}_{\mathsf{Comp},\mathcal{A}}(1^{\lambda}) := \Pr\left[1 = \mathsf{Exp}^{\mathsf{Kextr}}_{\mathsf{Comp},\mathcal{A}}(1^{\lambda})\right]$ is negligible in λ . (See discussion of search games in the Cryptocompanion.)

Hints:

1. It may be useful to think of other primitives (besides encryption programs) which we may want to white-box. For instance, suppose you want to white-box a MAC program. As we recall from previous lectures, a good MAC should achieve an unforgeability property. Suppose we white-box a bad MAC program which does not achieve the unforgeability property. Suppose however, that the white-box version of this program manages to hide its key such that it is indeed very difficult for an adversary to recover it from the implementation. Would such a MAC program be considered secure in the definition above, even though we know that it is a bad MAC?

Exercise 3 (Leakage resilient cryptography). In wake of the evident threat of side-channel attacks, a research area known as *leakage resilient cryptography* was introduced. This area covers definitional studies considering an adversary who obtains some leakage of the secret key used within a cryptographic scheme. Then, the adversary tries to break security of the cryptographic scheme, given the leakage.

- a) Provide a definition for IND-CPA security of an encryption scheme under leakage. This definition should look similar to the traditional IND-CPA definition we have seen in previous lectures, with some additional steps where the adversary obtains leakage from the secret key.
- b) What do you think may be practical ways of achieving security under leakage?

Hints:

1. In definitions for cryptographic schemes secure under leakage, the adversary usually defines his own leakage function. However for the definition to make sense, the leakage function should have some restrictions. E.g. the leakage function should not be able to provide the adversary with the complete value of the secret key, else the adversary would always trivially win the security games. I.e. there may be some output length restrictions for the leakage

functions. Alternatively, there might not be length restrictions for the output of the leakage functions, but the adversary should not be able to trivially derive the value of the secret key from the leakage. How would you capture these properties of the leakage function within the definition? What would be a reasonable output length restriction?

Exercise 4 (Impossibility). AES has been shown to be tremendously difficult to white-box. As we will see via an example in this exercise, some symmetric encryption schemes are actually impossible to be securely white-boxed, and they might not even achieve the modest goal of security against key extraction. Let se be an IND-CPA secure symmetric encryption scheme, let f be a pseudorandom function and let se' be the symmetric encryption scheme as defined below. Let comp be a (correct) white-box compiler for se', i.e. WB \leftarrow \$ Comp(k'_{se}).

Give the code of an efficient attacker \mathcal{A} such that $\mathsf{Win}^{\mathsf{Kextr}}_{\mathsf{Comp},\mathcal{A}}(1^{\lambda}) := \Pr\left[1 = \mathsf{Exp}^{\mathsf{Kextr}}_{\mathsf{Comp},\mathcal{A}}(1^{\lambda})\right]$ is non-negligible. You only need to argue about the success probability informally.

Hint: A black-box program means that an adversary is able to obtain input/output pairs but not know the code/internals of the program. What happens when an adversary instead interacts with a white-boxed program, i.e. it knows the program's code/internals, although it cannot make sense of it?

Exercise 5 (White-box as PKE). One possible view on security of white-box cryptography (without hardware-binding) is to build public key encryption scheme from a white-box of symmetric encryption scheme and demand that the resulting PKE satisfies IND-CPA security of a PKE. Given a white-box symmetric encryption scheme, describe a pke based on it and argue intuitively why it satisfies IND-CPA of PKE.

Exercise 6 (Hardware-binding). In the lecture we discussed the issue that white-box programs might be susceptible to re-distribution attacks (or *code-lifting* attacks). In such attacks, an adversary copies the complete white-box program and simply uses it on a device of its choice. One method to mitigate such attacks is to compile the white-box program such that it can only be executed on one specific device. We refer to this property as hardware-binding. One way to implement hardware-binding is by having a program which tries to check on which device it is running before performing a computation. For instance, if the device running the white-box has a secure hardware module (which is not accessible to the white-box adversary), we could use one functionality of that module to check if we are running on the desired hardware.

Consider now a key derivation function (KDF) that we wish to white-box. Here, we consider a simple key derivation function which uses a secret key k_{kdf} to derive further keys from a context value e. The derived keys should be indistinguishable from random and thus, the syntax and security definition of this KDF are equal to those we have defined for PRFs². In this exercise we will study a security definition for a white-box KDF with hardware-binding. Below, we first introduce the syntax for our hardware module. Then we introduce the syntax for the white-box KDF. Finally, we provide a definition for the security of a white-box KDF.

We observe that the white-box KDF works in collaboration from a response coming from the hardware module. Without this response, the white-box KDF cannot compute anything. The security definition has some subtle flaws. Namely, currently, an adversary can win this game. Can you spot the flaws, explain them and correct them?

Definition (Hardware Module HWM). A *hardware module* HWM consists of four algorithms $(kgen_{HW}, SubKgen_{HW}, Resp_{HW}, Check_{SW})$, where $kgen_{HW}$ is a PPT algorithm, and the algorithms $SubKgen_{HW}$, $Resp_{HW}$ and $Check_{SW}$ are deterministic algorithms with the following syntax:

$$\begin{aligned} k_{\mathsf{HWm}} &\leftarrow \$ \, \mathsf{kgen}_{\mathsf{HW}}(1^n) & k_{\mathsf{HWs}} &\leftarrow \mathsf{SubKgen}_{\mathsf{HW}}(k_{\mathsf{HWm}}, label) \\ \sigma &\leftarrow \mathsf{Resp}_{\mathsf{HW}}(k_{\mathsf{HWm}}, label, x) & b &\leftarrow \mathsf{Check}_{\mathsf{SW}}(k_{\mathsf{HWs}}, x, \sigma), \end{aligned}$$

Correctness requires that for all security parameters $n \in \mathbb{N}$,

$$\Pr[\mathtt{Check_{SW}}(\mathtt{SubKgen_{HW}}(k_{\mathtt{HWm}}, label), x, \mathtt{Resp_{HW}}(k_{\mathtt{HWm}}, label, x)) = 1] = 1,$$

where the probability is over the sampling of $k_{\text{HWm}} \leftarrow \$ \text{kgen}_{\text{HW}}(1^n)$.

The purposes of the algorithms are:

- The algorithm $\mathsf{kgen}_{\mathsf{HW}}$ is used within the HWM to generate a main key k_{HWm} , which is located in the HWM.
- The algorithm $SubKgen_{HW}$ is used within the HWM to generate a subkey k_{HWs} , which is used when compiling the whitebox program (the whitebox program can later use the key k_{HWs} to verify that it is running in the correct hardware.)
- The algorithm $\mathsf{Resp}_{\mathsf{HW}}$ is run in the HWM. The whitebox program can ask the HWM to give a response on which hardware it is running, that invokes $\mathsf{Resp}_{\mathsf{HW}}$ algorithm which gives the answer
- The algorithm $Check_{SW}$ is used within the whitebox program to check that the certificate given by $Resp_{HW}$ was valid. To do this, $Check_{SW}$ uses the subkey k_{HWs} .

Definition (WBHW). A white-box key derivation scheme with hardware binding WBHW consists of a hardware module HWM, a key derivation function KDF, and a PPT compiling algorithm Comp:

WKDF
$$\leftarrow$$
\$ Comp (k_{kdf}, k_{HWs}) .

For all genuine k_{HWm} , for all k_{kdf} , for all label, for all e, for all $k_{\text{HWs}} = \text{SubKgen}_{\text{HW}}(k_{\text{HWm}}, label)$ and $\sigma = \text{Resp}_{\text{HW}}(k_{\text{HWm}}, label, e)$, we have

$$\Pr[\mathsf{KDF}(k_{\mathsf{kdf}}, e) = \mathsf{WKDF}(e, \sigma)] = 1$$

where the probability is taken over compiling WKDF \leftarrow s Comp (k_{kdf}, k_{HWs}) .

Definition. A hardware-bound white-box key derivation scheme WBHW is IND-WKDF-secure if the real and ideal games $Gind-wkdf_{WBHW}^b$ are computationally indistinguishable, that is, for all PPT adversaries \mathcal{A} the advantage

$$\mathbf{Adv}_{\mathcal{A}}^{\mathtt{Gind-wkdf}_{\mathsf{WBHW}}^{0},\mathtt{Gind-wkdf}_{\mathsf{WBHW}}^{1}}(\lambda) := \left| \Pr \left[1 = \mathcal{A} \circ \mathtt{Gind-wkdf}_{\mathsf{WBHW}}^{0} \right] - \Pr \left[1 = \mathcal{A} \circ \mathtt{Gind-wkdf}_{\mathsf{WBHW}}^{1} \right] \right|$$
 is negligible.

 $^{^2}$ We here use the term KDF, because it makes more sense conceptually, but if it is confusing, think PRF each time you read KDF.

${\tt Gind-wkdf}_{\sf WBHW}^0$

Parameters

 λ : security parameter WBHW: WKDF scheme

Package State

 $k_{\mathtt{kdf}}$: KDF key

 $k_{\tt HWm}$: hardware main key $k_{\tt HWs}$: hardware sub-key

$\mathsf{GETWB}(label)$

 \mathbf{assert} WKDF $\neq \bot$

$$k_{\mathtt{kdf}} \leftarrow \$ \left\{0,1\right\}^{\lambda}$$

 $k_{\mathtt{HWm}} \leftarrow \mathtt{s} \ \mathsf{kgen}_{\mathtt{HW}}(1^{\lambda})$

 $\begin{aligned} k_{\texttt{HWs}} \leftarrow \texttt{SubKgen}_{\texttt{HW}}(k_{\texttt{HWm}}, label) \\ \texttt{WKDF} \leftarrow & \texttt{Somp}(k_{\texttt{kdf}}, k_{\texttt{HWs}}) \end{aligned}$

return WKDF

KDF()

 $e \leftarrow \$ \{0,1\}^{\lambda}$

 $\hat{k} \leftarrow \mathsf{KDF}(k_{\mathtt{kdf}}, e)$

 $\textbf{return}\ (\hat{k},e)$

$\mathsf{Resp}(e, label)$

 $\sigma \leftarrow \mathtt{Resp}_{\mathtt{HW}}(k_{\mathtt{HWm}}, label, e)$

return σ

${\tt Gind-wkdf}^1_{{\tt WBHW}}$

Parameters

 λ : security parameter WBHW: WKDF scheme

Package State

 $k_{\mathtt{kdf}}$: KDF key

 k_{HWm} : hardware main key k_{HWs} : hardware sub-key

$\mathsf{GETWB}(label)$

 \mathbf{assert} WKDF $\neq \bot$

$$k_{\mathtt{kdf}} \leftarrow \$ \left\{0,1\right\}^{\lambda}$$

 $k_{\mathtt{HWm}} \leftarrow \!\! \mathtt{s} \ \mathsf{kgen}_{\mathtt{HW}}(1^{\lambda})$

 $k_{\texttt{HWs}} \leftarrow \texttt{SubKgen}_{\texttt{HW}}(k_{\texttt{HWm}}, label)$

 $\texttt{WKDF} \gets \$ \ \mathsf{Comp}(k_{\mathtt{kdf}}, k_{\mathtt{HWs}})$

return WKDF

KDF()

 $e \leftarrow \$ \{0,1\}^{\lambda}$

 $\hat{k} \leftarrow \$ \{0,1\}^{\lambda}$

return (\hat{k}, e)

$\mathsf{Resp}(e, label)$

 $\sigma \leftarrow \mathtt{Resp_{HW}}(k_{\mathtt{HWm}}, label, e)$

return σ