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Chapter 2: Case study

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Version 0: Assembly code [advanced]

In the later parts of this course it will be very helpful to be able to have a look at the assembly code produced by the compiler. This way we will get a much better understanding of how the CPU will actually see our program.

No worries, we will definitely not try to **program** in the assembly language, and we certainly do not expect you to understand everything that you see in the assembly code. For us, it is enough that you can find the most relevant part of the assembly code and get a rough understanding of what are the most relevant instructions there.

Seeing the assembly code

To see what the assembly code produced by the compiler looks like, we can simply add the directive -S when we invoke the compiler. For example, if our implementation of the step function is in the file v0.cc, we can run:

```
g++ -S -g -03 -march=native -std=c++17 v0.cc
```

This will produce a file called vo.s that you can open in a text editor, and it contains precisely the same code that the processor sees when it runs the program.

Finding the relevant place

Unfortunately, the assembly code that the compiler produces is often a bit hard to follow, or to even see which part of it corresponds to which part of the source code. With high optimization settings (-03), there is no straightforward one-to-one correspondence between the source code and the assembly code.

Here is a simple trick that often helps a lot. We simply surround the innermost loop in the C++ source code with instructions such as

```
asm("# foo");
```

This basically asks the compiler to add the assembly language instruction # foo as such in the assembly code it generates. But for the assembler anything that starts with a # symbol is a comment, so the assembler will ignore this. We can however find these comments easily in the assembly code.

There is still a lot of additional garbage, but we can delete comments, labels (e.g. LBB14:), and everything that is outside the innermost loop. With a bit of effort, we will see that the innermost loop consists of just 7 instructions:

How to interpret it

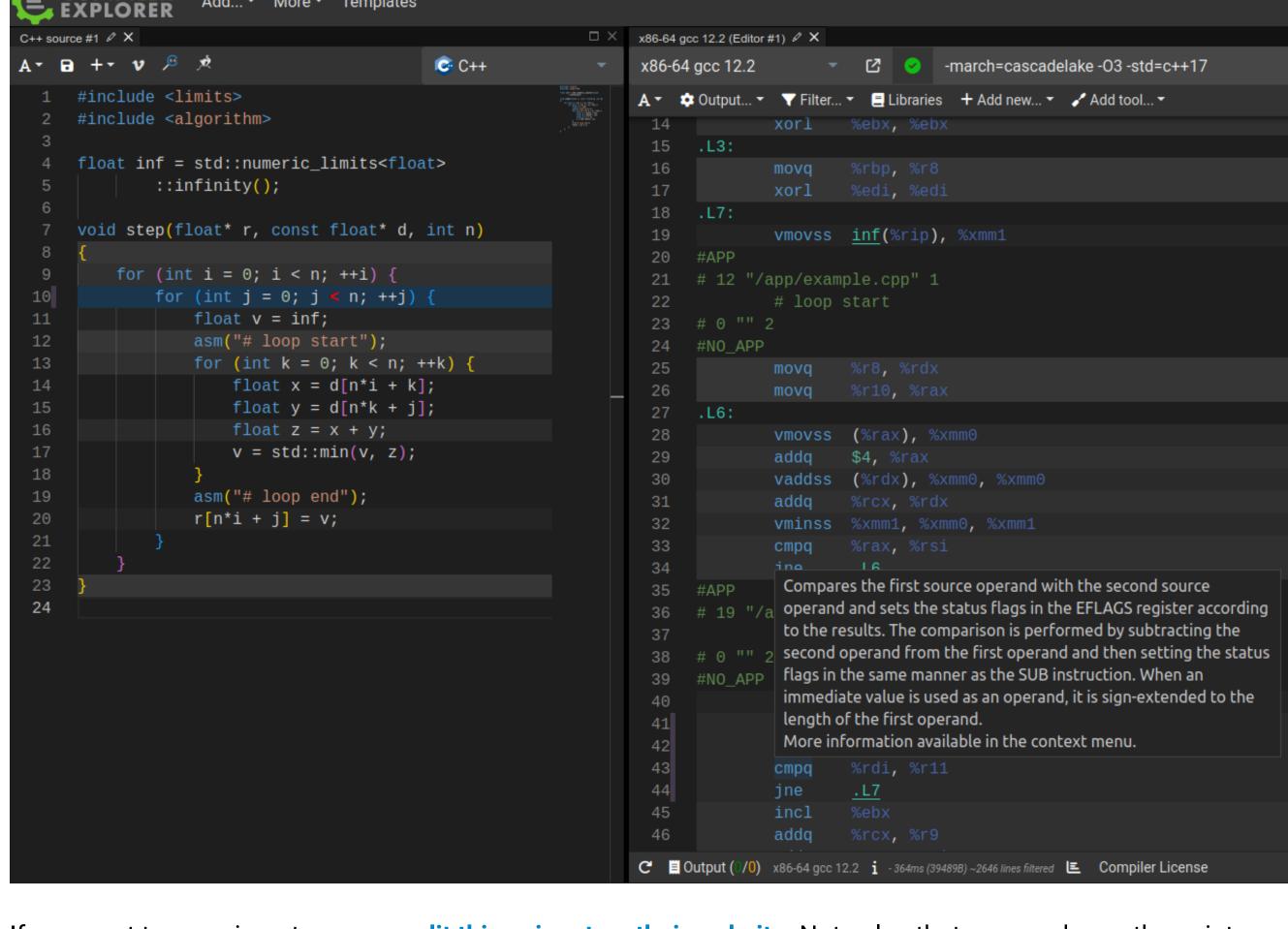
In the above code, <code>%rax</code>, <code>%rcx</code>, <code>%rdx</code>, <code>%rdi</code>, and <code>%r8</code> are normal <code>integer registers</code> that hold pointers and counters; also <code>%eax</code> and <code>%edx</code> refer to the same registers. The <code>addq</code> instructions increment the counters, <code>cmpl</code> sees if we have reached the end of the loop, and <code>jg</code> instruction jumps back to the beginning if this is not the case. Nothing particularly interesting here.

The interesting part is related to the registers %xmm0 and %xmm1. These are registers that can hold **floating-point numbers**. Let us first rewrite the original C++ code a bit so that it is closer to the assembly code, and then put it side by side with the relevant parts of the assembly code:

- vmovss (something), %xmm0: Read a single-precision floating point number from the memory address indicated by "something" and put it in register %xmm0. This is equivalent to the operation x = d[...] in the source code.
- vaddss (something), %xmm0, %xmm0: Read a single-precision floating point number from the memory address indicated by "something", add it to the value of register %xmm0, and store the result in register %xmm0. This is equivalent to the operation x = d[...] + x in the source code.
- vminss %xmm1, %xmm0, %xmm1: Calculate the minimum of the values in %xmm1 and %xmm0, and store the result in %xmm1. This is equivalent to the operation v = std::min(v, x) in the source code.

Interactive assembly

Compiler Explorer lets you interactively compare a C++ snippet and the corresponding assembly. For example, for the code presented here, Compiler Explorer shows the following:



If you want to experiment, you can **edit this snippet on their website**. Note also that you can hover the pointer over an assembly instruction and get a short description.

Compiler Explorer defaults to using the **Intel syntax** for its assembly, whereas we use the **AT&T** syntax. You can switch between the syntaxes by selecting **Output** and toggling **Intel asm syntax**.

```
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```