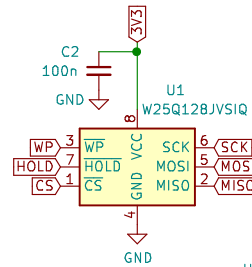
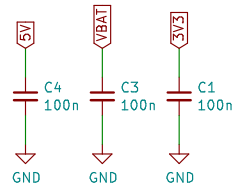


PWR_FLAG PWR_FLAG PWR_FLAG



Notes:

- U1 can be any serial flash chip compatible with the Lattice ECP5 FPGA series. Something with 4MiB >= size >= 16MiB is preferred.
- Max power draw from 3V3: 500mA
- Max power draw from 5V: 300mA
- Max draw from Vbat: whatever the batteries can supply
- Note that 5V is generated by a boost converter from the battery voltage, it is NOT the 5V USB line.
- CON1 connects to the badge, HDR1 is an empty header you can solder wires to

