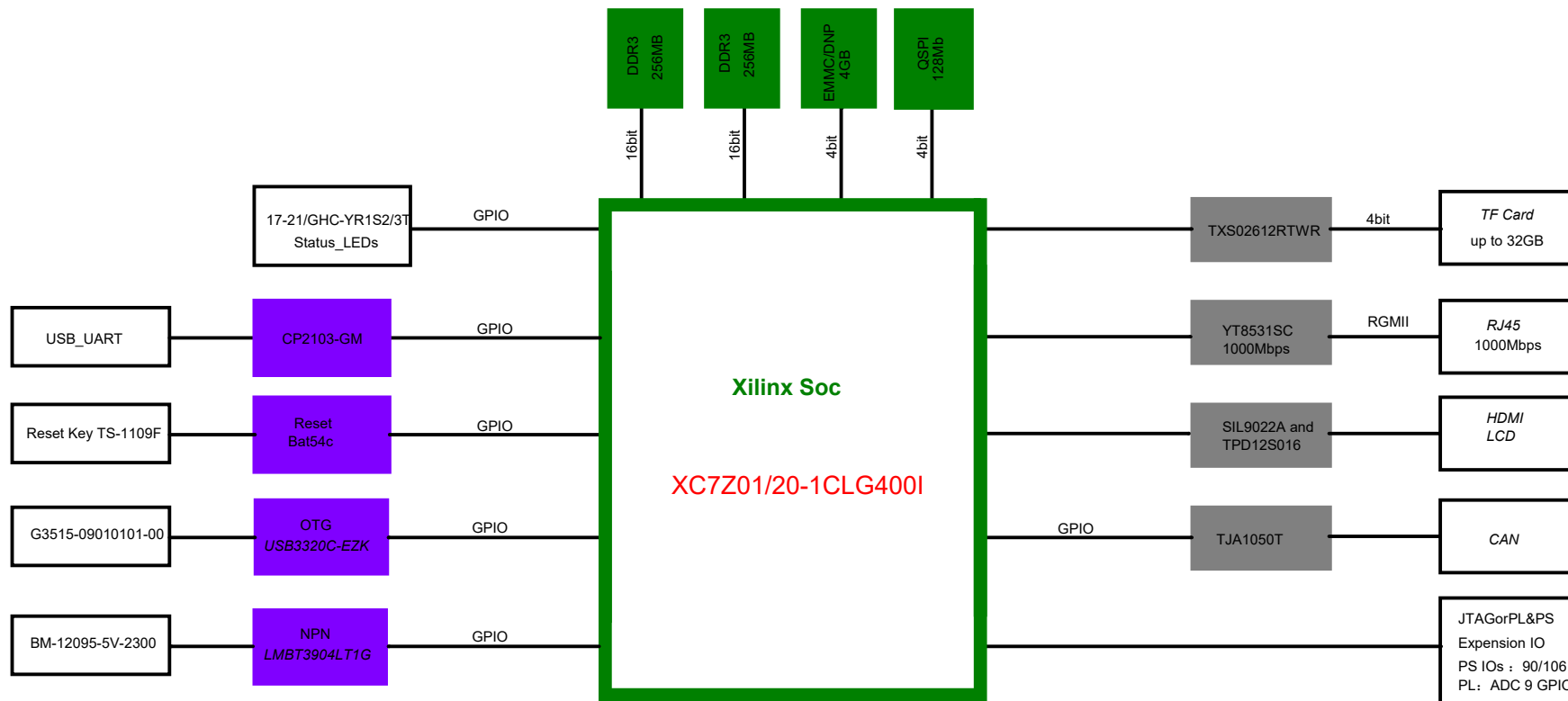
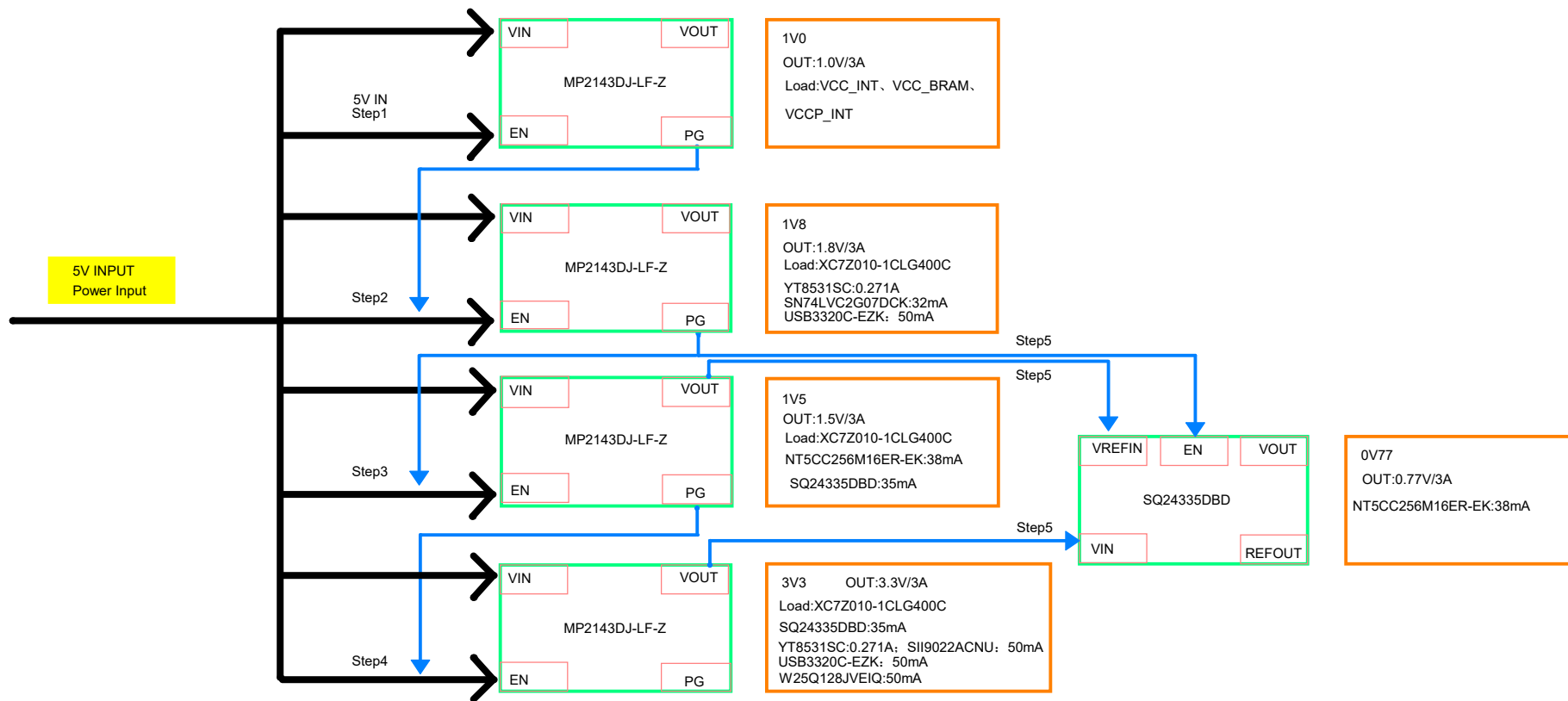
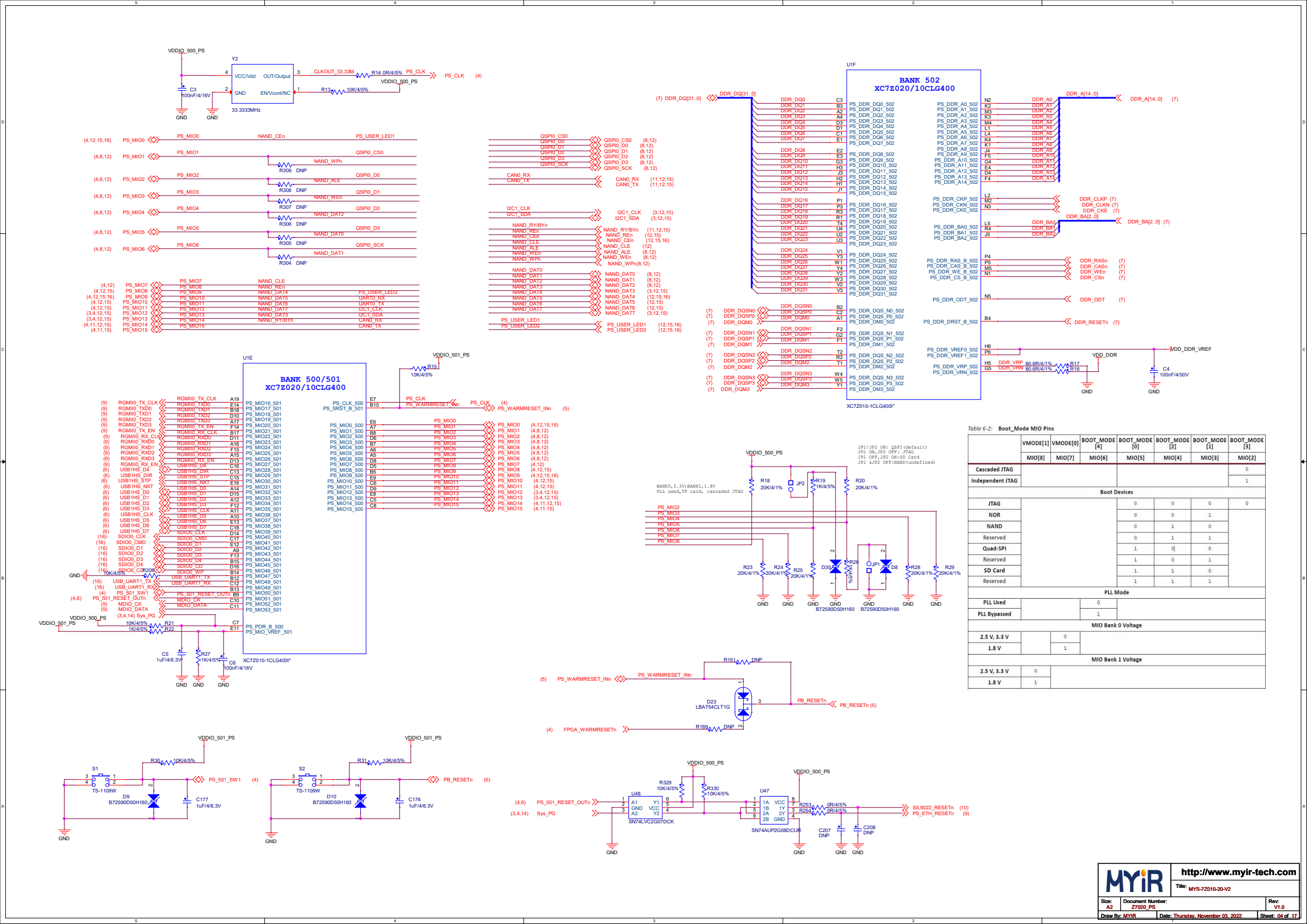


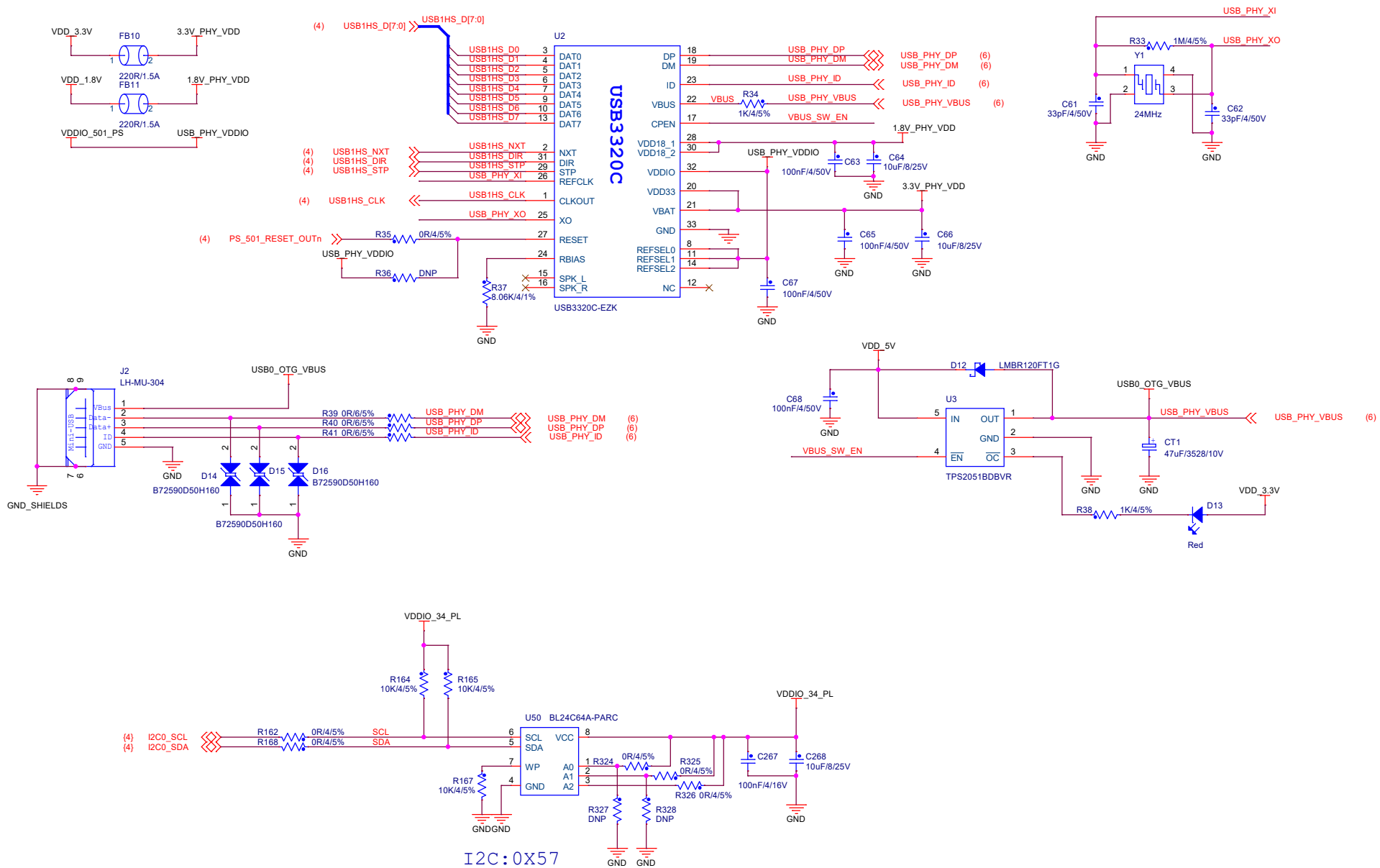
REV	Description	DATE	BY
Ver 1	Initial Release.		

PAGE NO.	SCHEMATIC PAGE
1	TITLE PAGE
2	BLOCK
3	Z7020_PL
4	POWER LOGIC
5	Z7020_PS
6	Z7020_POWER
7	USB OTG
8	DDR3 MEMORY
9	QSPI FLASH
10	10/100/1000 ETHERNET
11	HDMI
12	Sensor: G&T,CAN
13	NAND_FLASH
14	PWR:1.0V&1.5V,VTT
15	Power 1.8V,3.3V&DCin
16	EXP CONN
17	MISC

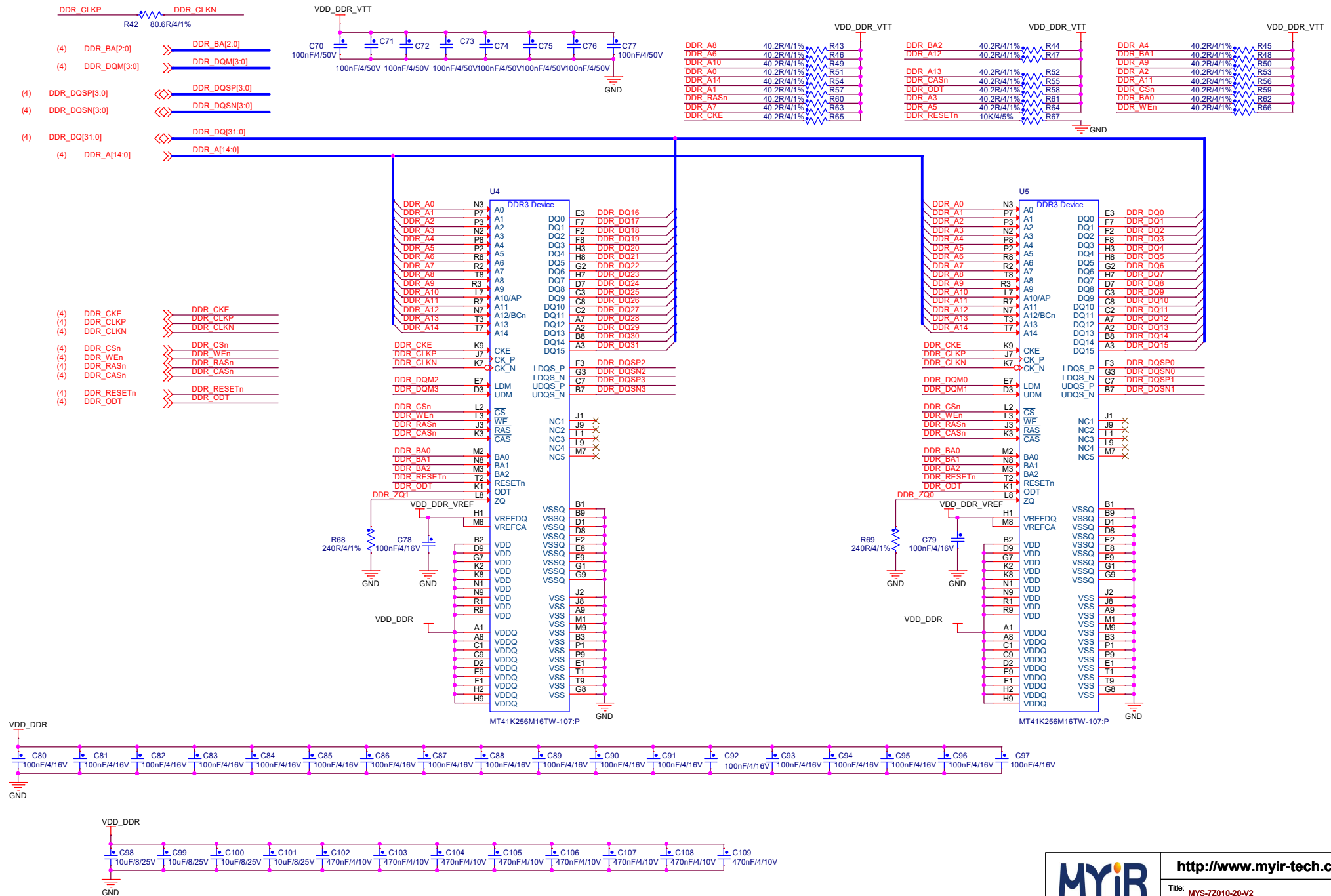


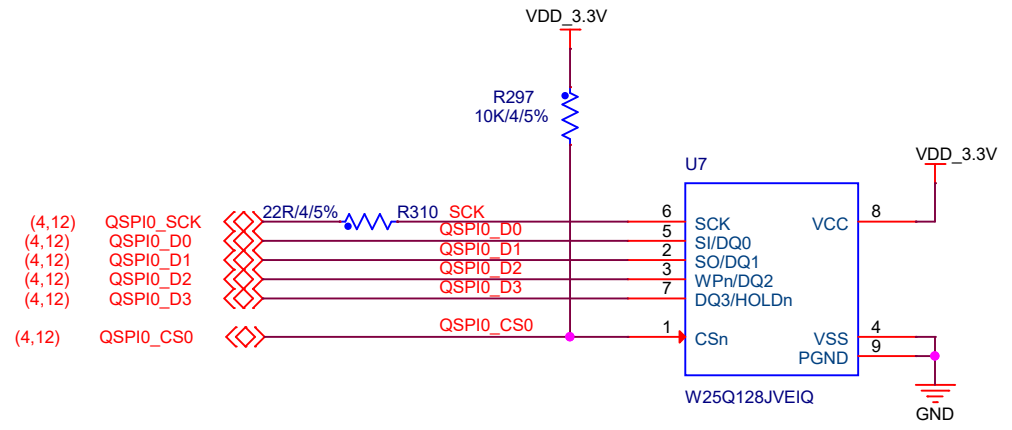
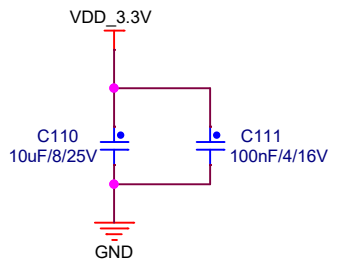




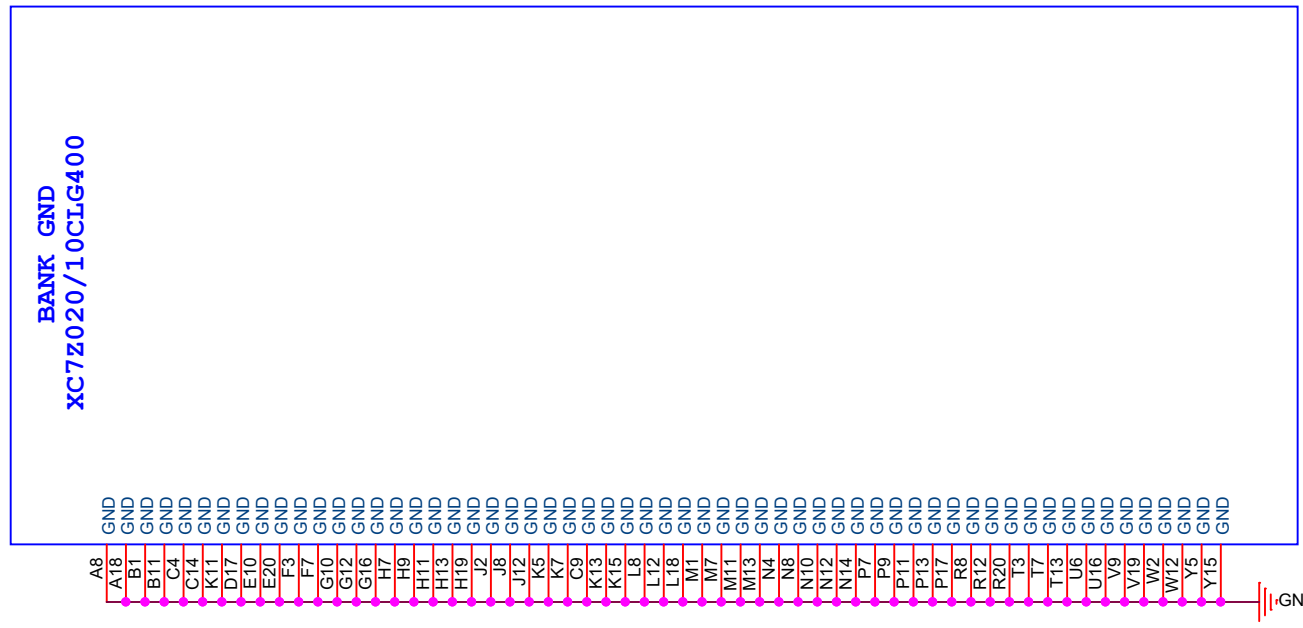


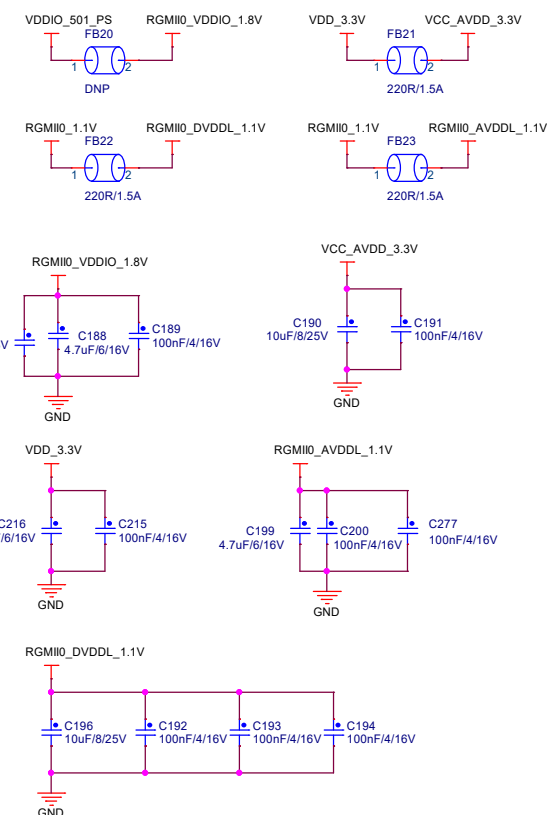
1024MB DDR3 (x32)





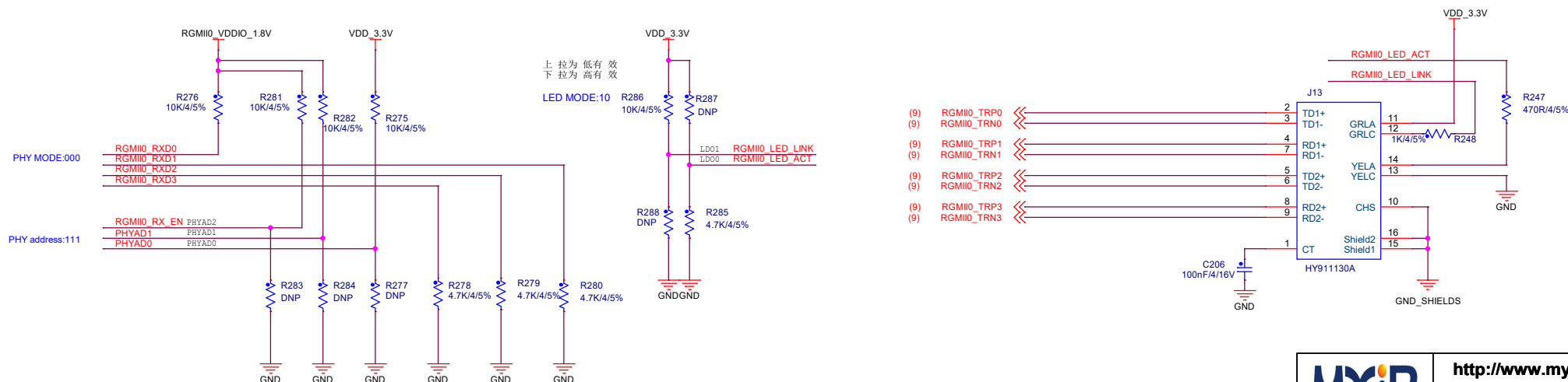
U1G
XC7Z010-1CLG400I*

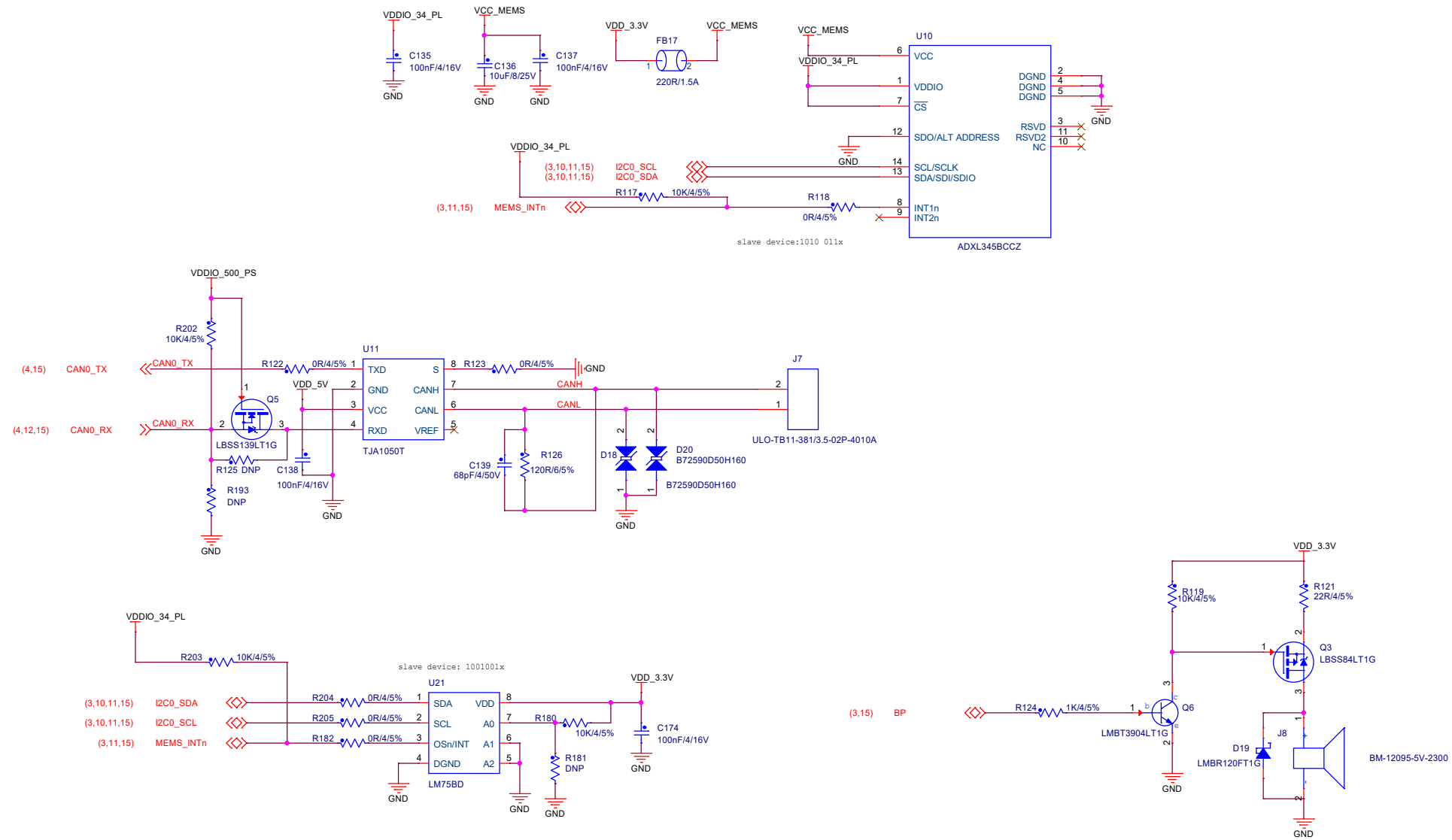


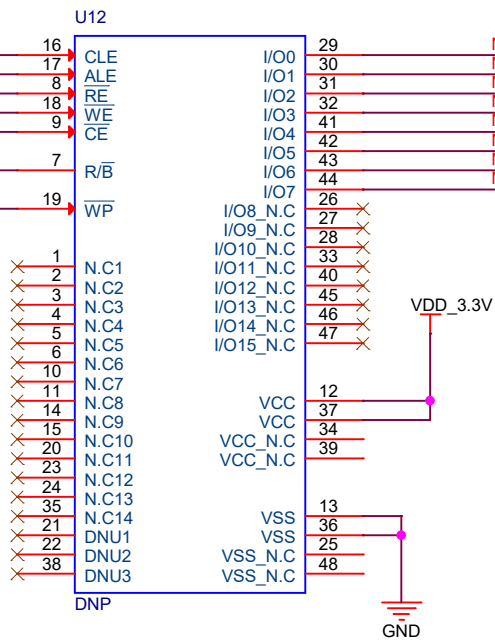
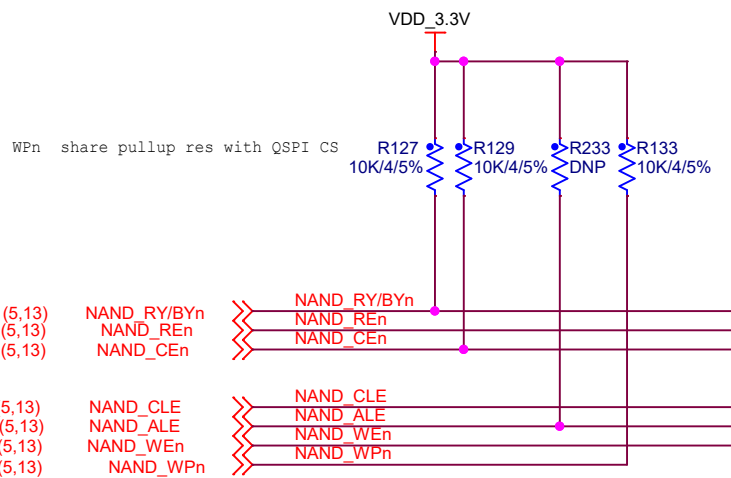
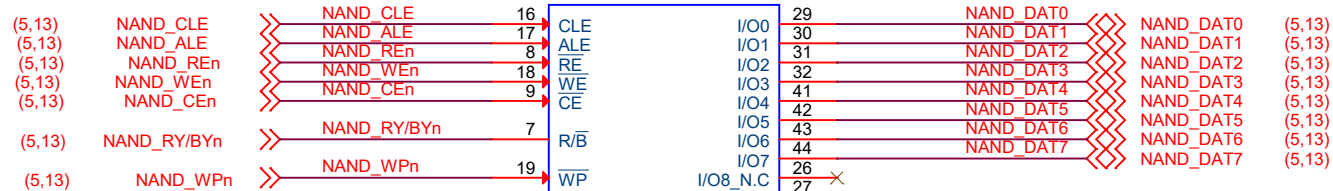
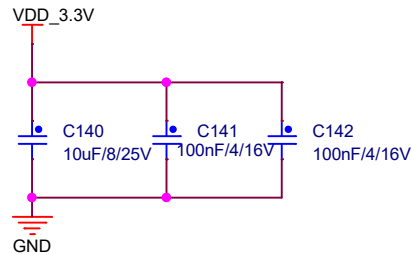


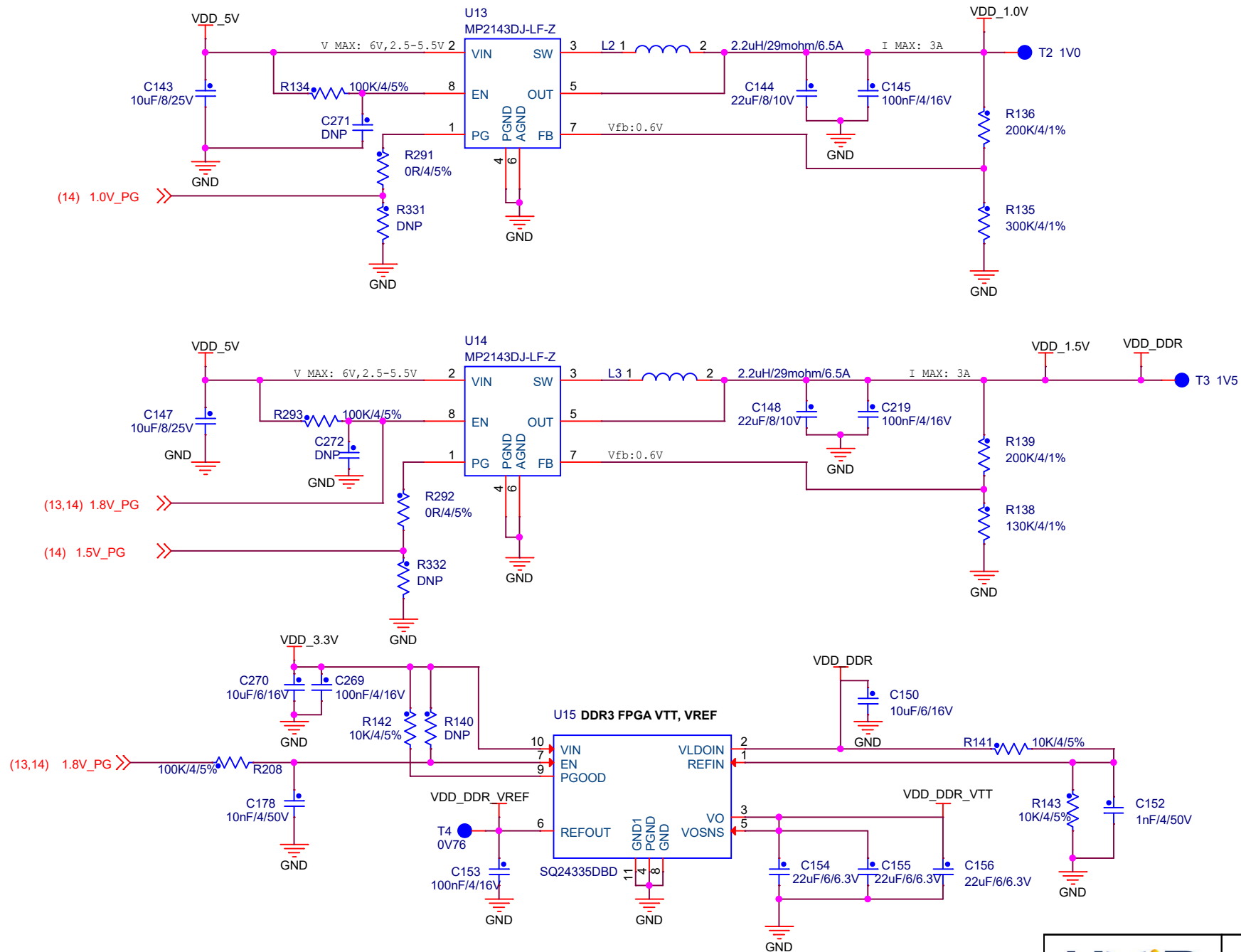
CFG_MODE0	CFG MODE[2:0]: Operation Mode Configuration. 3'b000: UTF <=> RGMII 3'b001: FIBER <=> RGMII 3'b010: UTF/FIBER <=> RGMII (Media Auto Detection)
CFG_MODE1	3'b011: UTF <=> SGMMI 3'b100: SGMMI (PHY side) <=> RGMII (MAC side), 3'b101: SGMMI (MAC side) <=> RGMII (PHY side), 3'b110: UTF <=> FIBER (Media Conversion auto mode)
CFG_MODE2	3'b111: UTF <=> FIBER (Media Conversion force mode)

CFG LDO[1:0]	Voltage selection for RGMII I/O
2'b00	3.3V
2'b01	2.5V
2'b10 or 2'b11	1.8V









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Title: MYS-7Z010-20-V2

Size:
A4

Document Number:
PWR:1.0V&1.5V,VTT

Rev:
V1.0

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Date: Thursday, November 10, 2022

Sheet: 13 of 17

