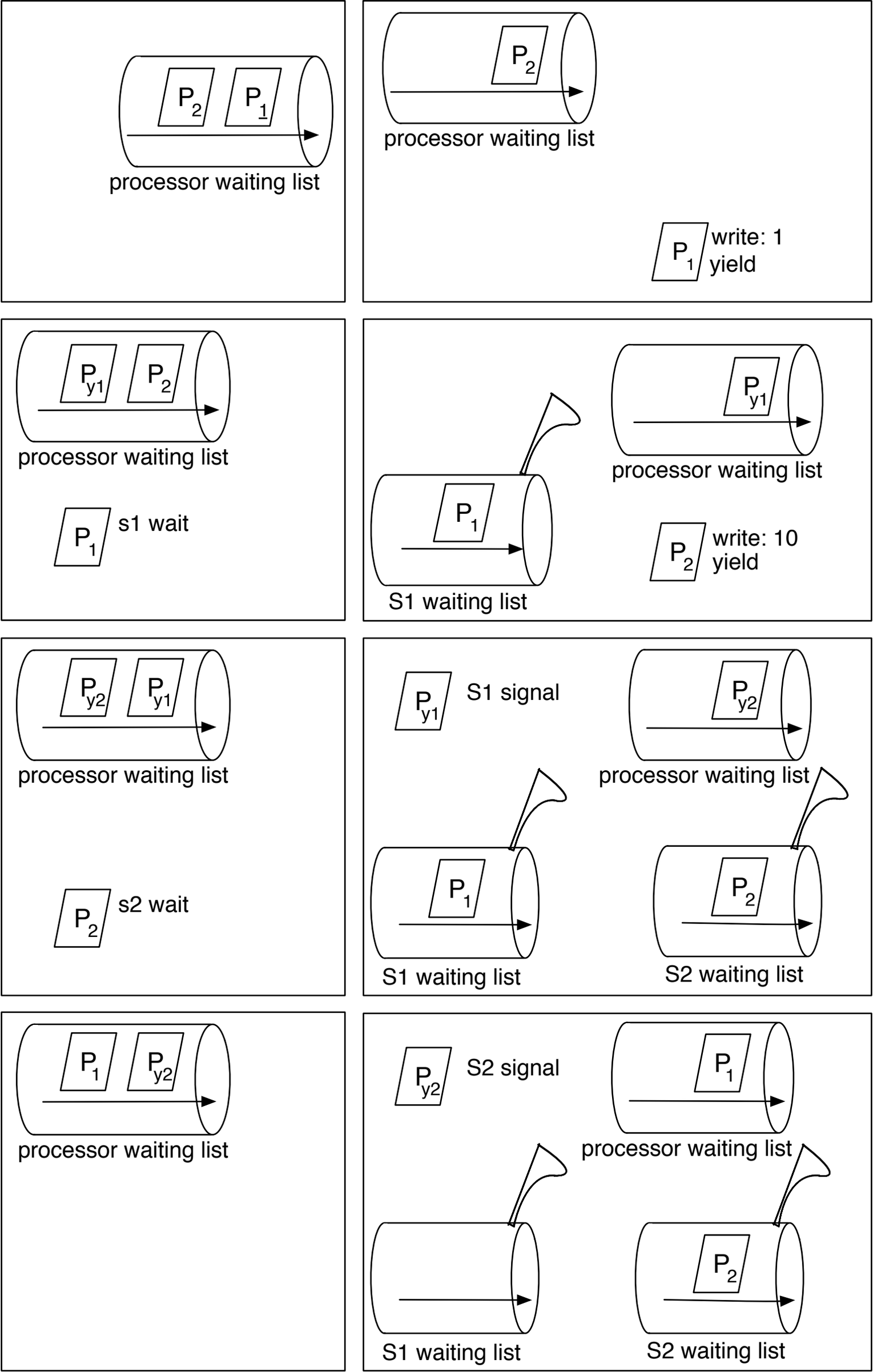
P2
P1
processor waiting list
P2
processor waiting list
P1
write: 1
yield
Py1
P2
processor waiting list
P1
s1 wait
P1
S1 waiting list
Py1
processor waiting list
P2
write: 10
yield
Py2
Py1
processor waiting list
P2
s2 wait
Py1
S1 signal
Py2
processor waiting list
P1
S1 waiting list
P2
S2 waiting list
P1
Py2
processor waiting list
Py2
S2 signal
P1
processor waiting list
S1 waiting list
P2
S2 waiting list