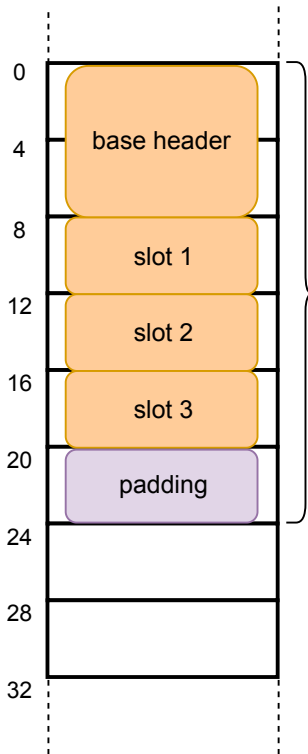


32-bits architecture



aligned
addresses
on 8 bytes

64-bits architecture

