SCL O SDA O VCC O GND O

TH-O 8 O SCL 7 O SDA 6 O TH-I 5 O RX 4 O TX 3 O VCC 2 O GND 1 O

7 Vcc

7 12V

7 Vcc CA3140 6 OUT Throttle

7 12V CA3140 6 OUT Throttle

2N4424

Throttle

Power Amp

Buffer Ou

vBat Buffe 12V THR OUT

 \bigcirc

1

2

To ARD

ттт

1 GND 2 VCC 3 TX 4 RX 5 TH-I 6 00000000

7

8

Pin 6 2

GND 4

Pin 6 2

 Θ

 Θ

 Θ GND

+IN 3 □

GND 4 □

12V Rail

THR OUT

+IN 3

DS3231 RTC

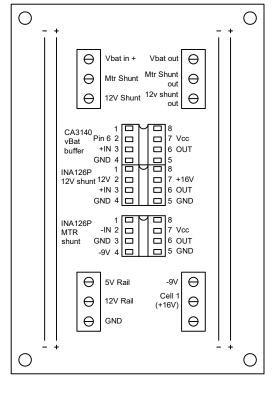
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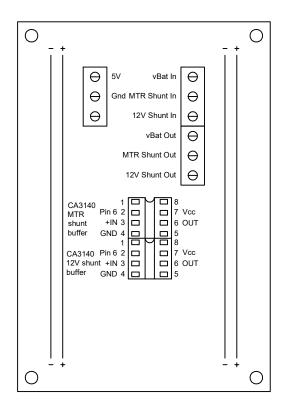
 ETH

To HMI

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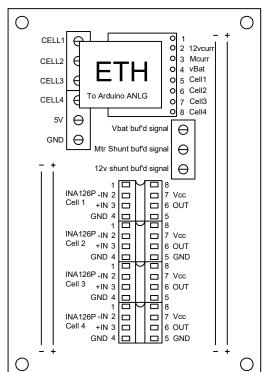
BOTTOM TO TOP

STACK ORDER,

4

3





FEB'20 FULL SCALE DATE

> <u>PRODUCED BY AN AUTODESK STUDENT VERSION</u> DESIGNED BOARDS STACK

001

CLASS LEGE

C.C.F

CLAIR COLI

S DRN B)

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