**PROJECT TITLE – UART COMMUNICATION TRANSMITTING AND RECEIVING ON BASYS-3 BOARD**

**GROUP MEMBERS:  PUJIITA SRIEYA BOLISETTY  - CB.EN.U4ECE21243**
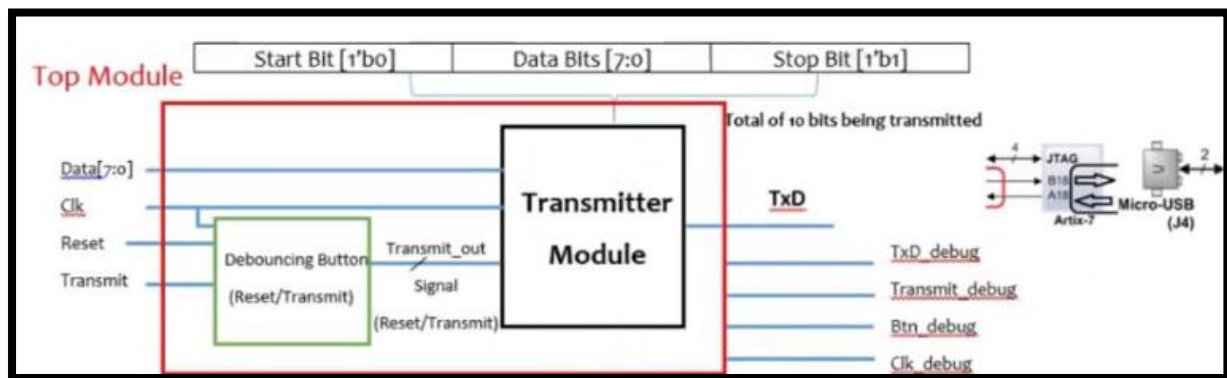
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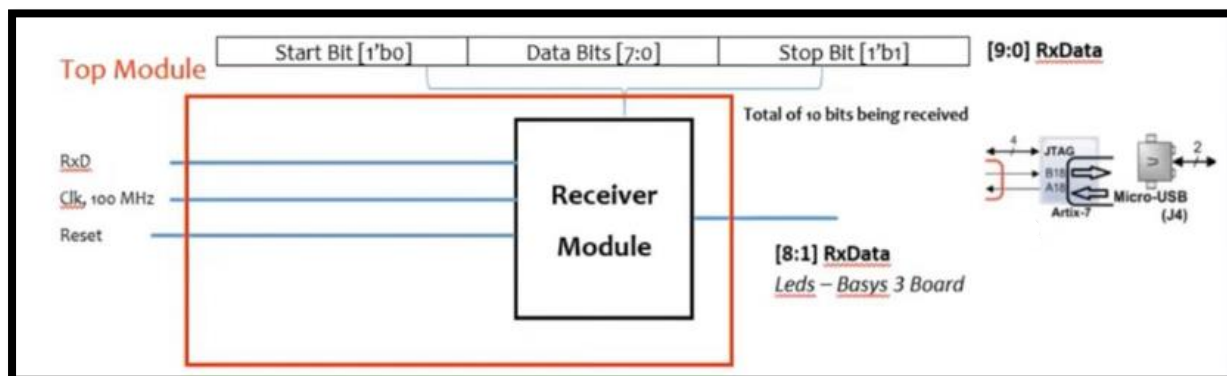**SVS KARTHIK                   - CB.EN.U4ECE21256**

**ABSTRACT**: To Implement UART (Universal Asynchronous Receiver/Transmitter) communication on the Basys-3 FPGA board, enabling bidirectional serial data transmission and reception for various digital system applications.

## BLOCK DIAGRAM :

## TRANSMITTER BLOCK:



## RECEIVER BLOCK:



## COMPONENTS REQUIRED:

1. Digilent Basys 3 Artix-7 Fpga Trainer Board