

1. For the output  $F$  to be 1 in the logic circuit shown, the input combination should be

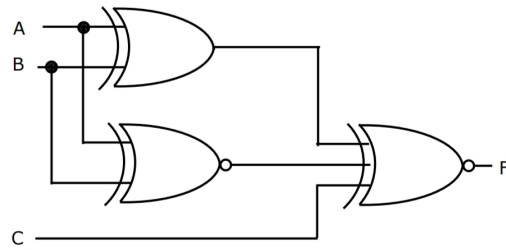


Figure 1:

- (a)  $A = 1, B = 1, C = 0$
- (b)  $A = 1, B = 0, C = 0$
- (c)  $A = 0, B = 1, C = 0$
- (d)  $A = 0, B = 0, C = 1$