

G. Sravani

CONTACT INFORMATION BARC Mumbai-400094

41 Satpura, Anushakthi Nagar

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CAREER **OBJECTIVE**

To excel in my field through hard work , research, skills and perseverance. To serve my parents and my country with best of my abilities

EDUCATION

Degree	College/School	University	Year of Passing	Percentage/Grade
B.E.(EXTC)	FCRIT, Vashi	Mumbai	2016	
Sem-5				9.22
Sem-4				9.75
Sem-3				9.13
Sem-2				9.33
Sem-1				8.04
Higher Secondary	AECS-4, BARC	CBSE Board	2012	89.8
Secondary	AECS-4, BARC	CBSE Board	2010	94.3

PROJECTS

- 1. Web based Home Automation using R-pi
- 2. Image Processing using FPGA
- 3. Light following robot using ATMEGA 16 Microcontroller

TRAINING AND INTERNSHIP

• Internship at DRHR, BARC based on PIC microcontrollers

RESEARCH **PUBLICATION**

Not yet published

TECHNICAL SKILLS

- Software: C, Assembly language, Python, HTML, Xilinx ISE, MATLAB, PSPICE,
- Hardware: Verilog HDL(Basic)

SOFT SKILLS

- 1. Good verbal and written communication skills.
- 2. Managed time efficiently to perform well in academics as well as extra curricular activities.
- 3. Tackled problems with a positive attitude.
- 4. Adapted well for different kinds of situations and have been patient throughout before jumping over to any conclusion.

- 5. Involved myself in organizing to learn more about teamwork and management skills.
- 6. Never hesitated to take my own decisions and always stood by them till the end.
- 7. Showcased leadership skills as a ladies representative in college.

EXTRA CURRICULAR ACTIVITIES

- Organizer at College fest
- Singing classical music
- Cooking and Baking
- Learning new languages

CO-CURRICULAR **ACTIVITIES**

- 1. Completed a course in R-pi programming using Python at Eduvance Academy
- 2. Completed a course in Front end VLSI Design course at Eduvance Academy
- 3. Participated in TI ADC 2105
- 4. Participated in activities organized by IETE council at college level

PERSONAL

Father's Name: G. V. K. Rao INFORMATION Mother's Name: S. Rajeswari

Sex: Female

Date of Birth: 3 January 1995

REFERENCE

Mr. Yogesh Chandurkar Assistant Professor

Department of Electronics and Telecommunication

FCRIT Vashi, Navi Mumbai