

The schematic diagram illustrates a multi-range digital voltmeter using four ICL7107 ICs. Each IC is configured with a 10k resistor network and a 100nF capacitor. The first stage is connected to a +5V supply and an ICL7107_AB4 input. The subsequent stages are connected to a common multi-range input (200V, 20V, 2V, 200mV). Each stage includes a 10k resistor network and a 100nF capacitor. The output of the fourth stage is connected to a common output terminal.

The diagram shows a 5V power supply circuit. It features a 5V voltage source connected to a network of capacitors (C1, C2, C3, C4, C5, C6, C7, C8) and diodes (D1, D2, D3, D4). The output of the circuit is labeled '5V' and is connected to a 'PWR_FLAG' signal. The circuit also includes a 'TP3 TestPoint' and a 'TP2 TestPoint'.

Reference capacitor

CREF+ C23 100n CREF-

48kHz Clock

OSC1 R13 100K OSC2 C26 100p OSC3

Auto-Zero Capacitor

A-Z R11 47K C24 470n BUFF C25 220n INT

Signal Input

IN_HI R12 1M AD737_OUTPUT C22 20p IN_LO COMA

Reference voltage

REF_HI REF_LO R14 24K AD737_COM COMA

100mV reference (100mV)









-5V

The diagram shows a circuit for a 1000V range selector. It includes a 9MM J1 VAC input, a 9MM J2 COMM input, and a GND connection. The circuit contains three 420V varistors (RV1, RV2, RV3), a 1200V capacitor (C9), and several resistors (R1, R2, R3, R4, R5, R6). A switch (SW1) is used for range selection, with positions 1, 3, 4, 5, 7, and 8. The output is labeled V1 and +5V. A threshold voltage is indicated.

[illegible]

ICL7107_E3	1
ICL7107_F3	2
ICL7107_B3	3
ICL7107_D3	4
ICL7107_E2	5
ICL7107_F2	6
ICL7107_A2	7
ICL7107_B2	8
ICL7107_C2	9
ICL7107_D2	10
ICL7107_E1	11
ICL7107_G1	12
ICL7107_F1	13
ICL7107_A1	14
ICL7107_B1	15
ICL7107_C1	16
ICL7107_D1	17
ICL7107_G2	18
ICL7107_C3	19
ICL7107_A3	20
ICL7107_G3	21
200V_RANGE	22
20V_RANGE	23
+5V	24

IC name	Consumption
AD737	210 μ A
LED display	720 mA
Power LED	15 mA
Total power consumption	765 mA

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