

Sreeja Pillarisetty

EDUCATION

Masters in Electrical and Microsystem Engineering	OTH Regensburg, Germany
<i>GPA 2.2</i>	<i>2023 – Present</i>
<ul style="list-style-type: none">Coursework in Surface Engineering with focus on etching, deposition and thin-film processesAdvanced Semiconductor Technology- Device fabrication steps, process integration, cleanroom protocolsAdvanced Optoelectronics- Semiconductor materials, optical properties of thin filmsExposure to cleanroom workflows and process demonstrations during lab visits	
Bachelors in Electronics and Communication Engineering	JNTUH University , India
<i>GPA 2.6</i>	<i>2018 – 2023</i>
<ul style="list-style-type: none">Digital and Analog Systems, VLSI Design, Microprocessors and Microcontrollers	

EXPERIENCE

Automotive Software Architect	2025
<i>Intern in Body control Module</i>	<i>Continental Automotive, Regensburg</i>
<ul style="list-style-type: none">Validating Configuration quality and Optimized task/resource mapping (analytic approach transferable to process optimization)	
Cyber Security Engineer	Apr. 2024 – Sep 2024
<i>Intern in Car Chassis</i>	<i>Continental Automotive, Regensburg</i>
<ul style="list-style-type: none">Designed Security task planning and database maintenance(structured problem solving and documentation)Maintaining and updating the Argus vulnerability databaseDesigning HSM Firmware update	
Embedded Software developer	Aug. 2022 – Feb 2023
<i>Internship</i>	<i>Shikara Embedded Solutions , India</i>
<ul style="list-style-type: none">Gained hands on experience in HIL Testing.worked on different microcontrollers ARM LPC2129,LPC2128, ARM-cortex	

SKILLS

Related Technical Skills
<ul style="list-style-type: none">Semiconductor Process fundamentals etching, thin-film deposition, lithography.Knowledge of process optimization(selectivity, uniformity, lateral etch control)Data analysis(Excel, Python, MATLAB) for process evaluation.familiar with cleanroom practices and Semiconductor workflow environmentMaterials characterization basics(surface roughness, thin-film integrity, SEM/TEM exposure from coursework
Additional Technical Skills
<ul style="list-style-type: none">Programming (C, Python, MATLAB)hardware (ARM Cortex, Arduino, FPGA, PCB Design)Tools (Vector, Git, Visual-studio, Excel automation)
Soft Skills
<ul style="list-style-type: none">Strong communication skillsGerman-completed level B1 studying B2-C1 course in Uni strong interest to improveEnglish- C2MS-Office

PROJECTS

Etching Simulation

2025

Independent Project

- Built a simple Python/Excel model to simulate nitride etching into oxide under different process parameters
- Analyzed effects of etch rate and time on process selectivity and uniformity
- Identified Optimal process window to minimize oxide damage, reflecting real semiconductor RIE challenges

Instrument Panel Cluster

2022 – 2023

- Design and tested a CAN based automotive instrument cluster on LPC2129. Gained experience in debugging , parameter testing and system integration.

Satellite Image Enhancement

2022

- Implemented SVD and DWT in MATLAB to improve image quality. Strengthened skills in signal/data processing and algorithm validation.

Flood Detecting System

2022

- Built a monitoring system using ARM micro-controller and GSM module highlighting skills in sensor integration and system reliability.

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