

TCAS INTERFACE CIRCUIT DIAGRAM

STATION : ADASROAD (ADD)



| DESIGN | VERIFIED BY DIVISION | | | | TCAS INTERFACE CIRCUIT | | SIGNAL & TELECOMMUNICATIONS ADASROAD (ADD) WESTERN RAILWAY DIVISION: VADODARA | |
|------------------|-----------------------|---------------------------|--|--|------------------------|---------------------------|--|--|
| | TITLE: COVER SHEET | | | | | | | |
| | Prepared By | SHREYANSH | Engineer | S.akhil Shreyansh (Aug 24, 2025 15:02:54 GMT+5:5) | REF:SIG PLAN NO. | | | |
| | Verified By | Y.AKHIL | Sr Engineer | Y.Akhil | प. रे आरेख. सं. | | | |
| | Approved By | MVH.PRASAD | Design Director | MVH Prasad (Aug 24, 2025 15:39:29 GMT+5:5) | IPU. 2059/4C | WR.DRG.NO.TCAS 2059/4C | | |
| | Version | V_ | 2.1.5 | SSE/Sig | DSTE | कूल शीट SHEETS | | |
| Proof Consultant | S.CHITRA | Associate General Manager | S.CHITRA S.Chitra (Aug 25, 2025 17:26:10 GMT+5:5) | DSTE | Sr.DSTE/N/BRC | 1 | 43 | |

| Sh. No. | SHEET CONTENT |
|---------|---|
| 1 | COVER SHEET |
| 2 | INDEX |
| 3 | INDEX |
| 4 | TERMINATION & FUSES |
| 5 | TERMINATION & FUSES |
| 6 | A41830 RECPR, A41830 HECP1R, A41830 DECP3R, A41830 HHECP1R, S5 RECPR, S5 HECP1R (REPEATER RELAYS) |
| 7 | S5 DECP1R, S5 HECR, C05 HECR, S5 UECPR, S5 A/AGECR, S12 RECPR (REPEATER RELAYS) |
| 8 | S12 HECP2R, S12 DECP1R, S12 HHECP1R, S12 A/AGECP1R, S37 RECPR, S37 HECP1R (REPEATER RELAYS) |
| 9 | S39 RECPR, S39 HECP1R, S39 DECP1R, S39 HECR, S39 AECR, S46 RECPR (REPEATER RELAYS) |
| 10 | S46 HECP1R, S46 DECP1R, S46 HHECP2R, C046 HECR, S46 UHECP1R, S46 A/AGECR (REPEATER RELAYS) |
| 11 | S14 RECPR, S14 HECP1R, S2/300 RECPR, S2/300 HECP1R, S2/300 DECP3R, S2/300 HHECP1R (REPEATER RELAYS) |
| 12 | S2/300 AECR, 101/102 NWKP4R, 101/102 RWKR, 103/104 NWKP1R, 103/104 RWKP1R, 105/106 NWKP1R (REPEATER RELAYS) |
| 13 | 105/106 RWKPR, 111/112 NWKR, 111/112 RWKR, 113/114 NWKP1R, 113/114 RWKR, 41830T AXTP2R (REPEATER RELAYS) |
| 14 | 41830AT AXTP2R, C05T TPR, 01T/AT TPR, 02T/AT TP2R, 03T/AT TP1R, 04T/AT TPR (REPEATER RELAYS) |
| 15 | 39T TPR, C46T TPR, 12T TPR, 2T AXTP2R, 2AT AXTP1R, LC250 XCKP5R (REPEATER RELAYS) |
| 16 | LC251 XCKP6R, 5T TP2R, 46T TP2R, A42218 RECPR, A42218 HECP1R, A42218 DECP1R (REPEATER RELAYS) |
| 17 | A42218 HHECP1R, S49 RECPR, S49 HECP1R, S49 DECP1R, S49 HHECP1R, S49 AECPR (REPEATER RELAYS) |
| 18 | 42218T AXTP1R, 42218AT AXTPR, 49T TPR, 49AT TPR, S41819 RECPR, S41819 HECP1R (REPEATER RELAYS) |
| 19 | S41819 DECP1R, S41819 HHECP1R, S41819 AECR, 41819T AXTPR, 41819AT AXTP1R, LC248 XCKP2R (REPEATER RELAYS) |
| 20 | A41830 RECPR, A41830 HECP2R, A41830 DECP4R, A41830 HHECP2R (INPUT RELAYS) |
| 21 | S5 RECPR, S5 HECP2R, S5 DECP2R, S5 HHECP1R (INPUT RELAYS) |
| 22 | C05 HECP1R, S5 UECPR, S5 A/AGECP1R, S12 RECPR (INPUT RELAYS) |
| 23 | S12 HECP3R, S12 DECP2R, S12 HHECP1R, S12 A/AGECP1R (INPUT RELAYS) |
| 24 | S37 RECPR, S37 HECP1R, S39 RECPR, S39 HECP2R (INPUT RELAYS) |
| 25 | S39 DECP2R, S39 HHECP1R, S39 AECPR, S46 RECPR (INPUT RELAYS) |

| HBL | DESIGN | | | VERIFIED BY DIVISION | | TCAS INTERFACE CIRCUIT | | SIGNAL & TELECOMMUNICATIONS |
|------------------|-------------|---------------------------|--|---|---------------|------------------------|-------------------|---------------------------------------|
| | Prepared By | SHREYANSH | Engineer | S.S. Shreyansh Shreyansh (Aug 24, 2025 15:02:54 GMT+5:5) | SSE/Sig | ADSTE | TITLE: INDEX | |
| Verified By | Y.AKHIL | Sr Engineer | Y.Akhil | | | | REF: SIG PLAN NO. | प. रे आरेख सं. |
| Approved By | MVH.PRASAD | Design Director | MVH Prasad MVH Prasad (Aug 24, 2025 15:39:29 GMT+5:5) | | | | IPU. 2059/4C | WR.DRG.NO.TCAS 2059/4C |
| Version | V_ | 2.1.5 | | | | | शीट SHEET | कॉल शीट SHEETS |
| Proof Consultant | S.CHITRA | Associate General Manager | S.Chitra S.CHITRA (Aug 25, 2025 17:26:10 GMT+5:5) | DSTE | Sr.DSTE/N/BRC | 2 | 43 | WESTERN RAILWAY DIVISION: VADODARA |

| Sh. No. | SHEET CONTENT |
|---------|--|
| 26 | S46 HECP2R, S46 DECP2R, S46 HHECP3R, C046 HECP1R (INPUT RELAYS) |
| 27 | S46 UHECP2R, S46 A/AGECPR, S14 RECP1R, S14 HECP1R (INPUT RELAYS) |
| 28 | S2/300 RECP2R, S2/300 HECP2R, S2/300 DECP4R, S2/300 HHECP2R (INPUT RELAYS) |
| 29 | S2/300 AECPR, 101/102 NWKP5R, 101/102 RWKPR, 103/104 NWKP2R (INPUT RELAYS) |
| 30 | 103/104 RWKP2R, 105/106 NWKP2R, 105/106 RWKP1R, 111/112 NWKPR (INPUT RELAYS) |
| 31 | 111/112 RWKPR, 113/114 NWKP2R, 113/114 RWKPR, 41830T AXTP3R (INPUT RELAYS) |
| 32 | 41830AT AXTP3R, C05T TP1R, 01T/AT TP1R, 02T/AT TP3R (INPUT RELAYS) |
| 33 | 03T/AT TP2R, 04T/AT TP1R, 39T TP1R, C46T TP1R (INPUT RELAYS) |
| 34 | 12T TP1R, 2T AXTP3R, 2AT AXTP2R, LC250 XCKP6R (INPUT RELAYS) |
| 35 | LC251 XCKP7R, 5T TP3R, 46T TP3R, A42218 RECP1R (INPUT RELAYS) |
| 36 | A42218 HECP2R, A42218 DECP2R, A42218 HHECP1R, S49 RECP2R (INPUT RELAYS) |
| 37 | S49 HECP2R, S49 DECP2R, S49 HHECP2R, S49 AECP1R (INPUT RELAYS) |
| 38 | 42218T AXTP2R, 42218AT AXTP1R, 49T TP1R, 49AT TP1R (INPUT RELAYS) |
| 39 | S41819 RECP2R, S41819 HECP2R, S41819 DECP2R, S41819 HHECP2R (INPUT RELAYS) |
| 40 | S41819 AECPR, 41819T AXTP1R, 41819AT AXTP2R, LC248 XCKP3R (INPUT RELAYS) |
| 41 | TCAS RELAY RACK-1 LAYOUT |
| 42 | TCAS RELAY RACK-2 LAYOUT |
| 43 | REVISION HISTORY |

| HBL | DESIGN | | | VERIFIED BY DIVISION | | TCAS INTERFACE CIRCUIT | | SIGNAL & TELECOMMUNICATIONS ADASROAD (ADD) WESTERN RAILWAY DIVISION: VADODARA | |
|------------------|-------------|---------------------------|-----------------|----------------------|------|------------------------|------------------------|--|--|
| | | | | | | TITLE: INDEX | | | |
| | Prepared By | SHREYANSH | Engineer | | | REF: SIG PLAN NO. | प. रे आरेख सं. | | |
| | Verified By | Y.AKHIL | Sr Engineer | | | IPU_2059/4C | WR.DRG.NO.TCAS 2059/4C | | |
| | Approved By | MVH.PRASAD | Design Director | | | शीट SHEET | कॉल शीट SHEETS | | |
| | Version | V_2.1.5 | | | | 3 | 43 | | |
| Proof Consultant | S.CHITRA | Associate General Manager | | | DSTE | Sr.DSTE/N/BRC | | | |

TCAS RACK-1

| | | | | | | | | |
|--------------------------|--------------------------|-------------------------|--------------------------|--------------------------|--------------------------|--------------------------|--------------------------|--------------------------|
| A41830 RECP1R B1 | A41830 HECP2R B3 | A41830 DECP4R B5 | A41830 HHECP2R B7 | S5 RECP2R B9 | S5 HECP2R B11 | S5 DECP2R B13 | S5 HHECP2R B15 | C05 HECP2R B17 |
| S5 UECP1R B19 | S5 A/AGECPR B21 | S12 RECP2R B23 | S12 HECP3R B25 | S12 DECP2R B27 | S12 HHECP1R B29 | S12 A/AGECP1R B31 | S37 RECP1R B33 | S37 HECP1R B35 |
| S39 RECP2R B37 | S39 HECP2R B39 | S39 DECP2R B41 | S39 HHECP2R B43 | S39 AECPR B45 | S46 RECP1R B47 | S46 HECP2R B49 | S46 DECP2R B51 | S46 HHECP3R B53 |
| C046 HECP1R B55 | S46 UHECP2R B57 | S46 A/AGECPR B59 | S14 RECP1R B61 | S14 HECP1R B63 | S2/300 RECP2R B65 | S2/300 HECP2R B67 | S2/300 DECP4R B69 | S2/300 HHECP2R B71 |
| S2/300 AECPR B73 | 101/102 NWKP5R B75 | 101/102 RWKPR B77 | 103/104 NWKP2R B79 | 103/104 RWKP2R B81 | 105/106 NWKP2R B83 | 105/106 RWKP1R B85 | 111/112 NWKP2R B87 | 111/112 RWKPR B89 |
| 113/114 NWKP2R B91 | 113/114 RWKPR B93 | 41830T AXTP3R B95 | | | | | | |

| | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
|---|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| A | F1 | F2 | F3 | F4 | F5 | F6 | F7 | F8 | F9 | F10 | F11 | F12 | F13 | F14 | F15 | F16 | F17 | F18 | F19 | F20 | F21 | F22 | F23 | F24 | F25 | F26 | F27 | F28 | F29 | F30 | F31 | F32 |
| B | N1 | N2 | N3 | N4 | N5 | N6 | N7 | N8 | N9 | N10 | N11 | N12 | N13 | N14 | N15 | N16 | N17 | N18 | N19 | N20 | N21 | N22 | N23 | N24 | N25 | N26 | N27 | N28 | N29 | N30 | N31 | N32 |
| A | F33 | F34 | F35 | F36 | F37 | F38 | F39 | F40 | F41 | F42 | F43 | F44 | F45 | F46 | F47 | F48 | F49 | F50 | F51 | F52 | F53 | F54 | F55 | F56 | F57 | F58 | F59 | F60 | F61 | F62 | F63 | F64 |
| B | N33 | N34 | N35 | N36 | N37 | N38 | N39 | N40 | N41 | N42 | N43 | N44 | N45 | N46 | N47 | N48 | N49 | N50 | N51 | N52 | N53 | N54 | N55 | N56 | N57 | N58 | N59 | N60 | N61 | N62 | N63 | N64 |
| A | F65 | F66 | F67 | F68 | F69 | F70 | F71 | F72 | F73 | F74 | F75 | F76 | F77 | F78 | F79 | F80 | F81 | F82 | F83 | F84 | F85 | F86 | F87 | F88 | F89 | F90 | F91 | F92 | F93 | F94 | F95 | F96 |
| B | N65 | N66 | N67 | N68 | N69 | N70 | N71 | N72 | N73 | N74 | N75 | N76 | N77 | N78 | N79 | N80 | N81 | N82 | N83 | N84 | N85 | N86 | N87 | N88 | N89 | N90 | N91 | N92 | N93 | N94 | N95 | N96 |

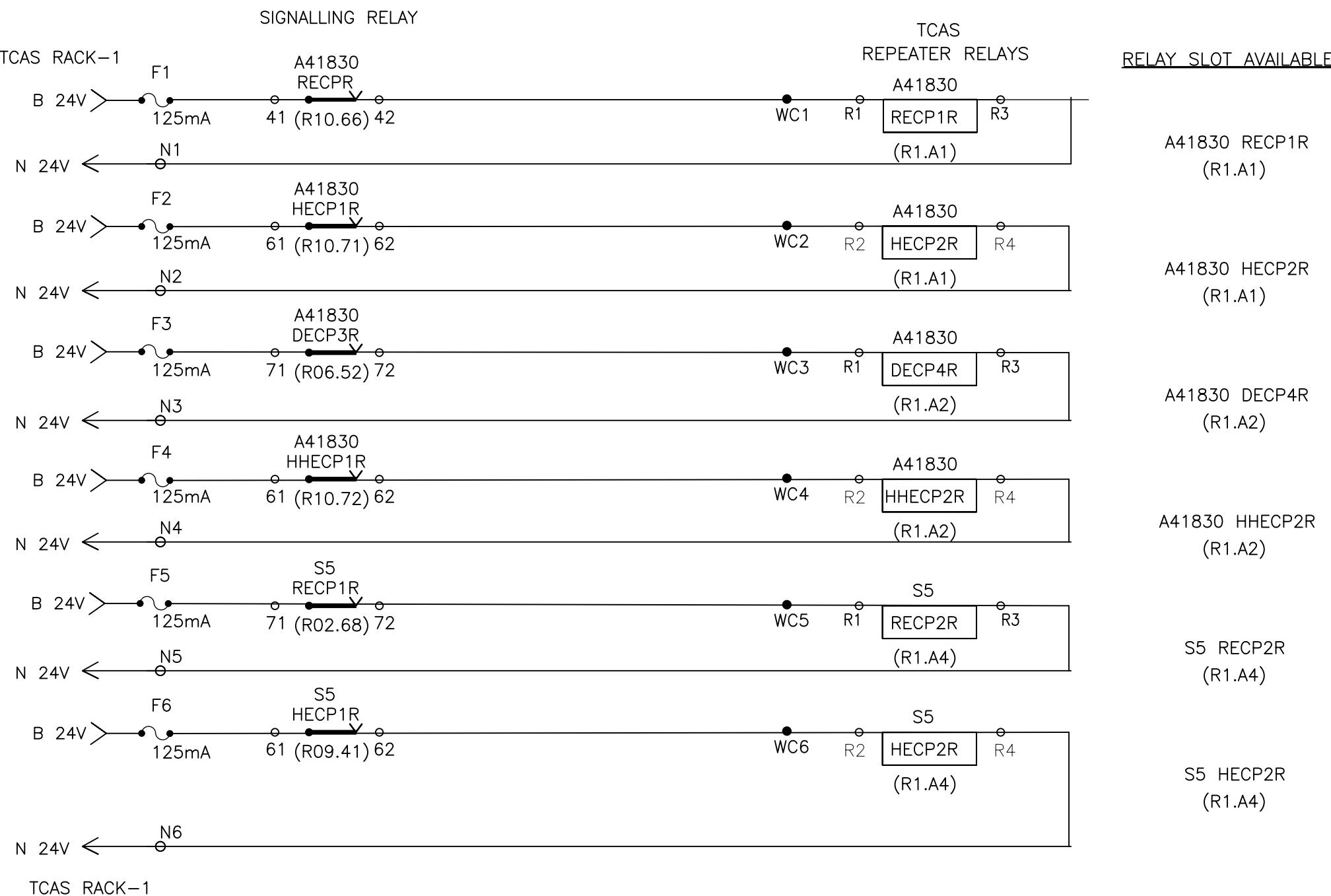
| HBL | DESIGN | | | | | VERIFIED BY DIVISION | | | | | TCAS INTERFACE CIRCUIT | | | | SIGNAL & TELECOMMUNICATIONS | | | | |
|-----|------------------|------------|---------------------------|------------|--|----------------------|--|--|--|--|------------------------|------------------|--|------------------------|-----------------------------|--|--|--|--|
| | Prepared By | | SHREYANSH | Engineer | | | | | | | | TITLE: | | TERMINATION & FUSES | | | | | |
| | Verified By | Y.AKHIL | Sr Engineer | Y.Akhil | | | | | | | | REF:SIG PLAN NO. | | प. रे आरेख. सं. | | | | | |
| | Approved By | MVH.PRASAD | Design Director | MVH Prasad | | | | | | | | IPU_2059/4C | | WR.DRG.NO.TCAS 2059/4C | | | | | |
| | Version | V | 2.1.5 | | | | | | | | | शीट SHEET | | कॉल शीट SHEETS | | | | | |
| | Proof Consultant | S.CHITRA | Associate General Manager | S.Chitra | | | | | | | | 4 | | 43 | | | | | |
| | | | | | | | | | | | | DSTE | | Sr.DSTE/N/BRC | | | | | |

TCAS RACK-2

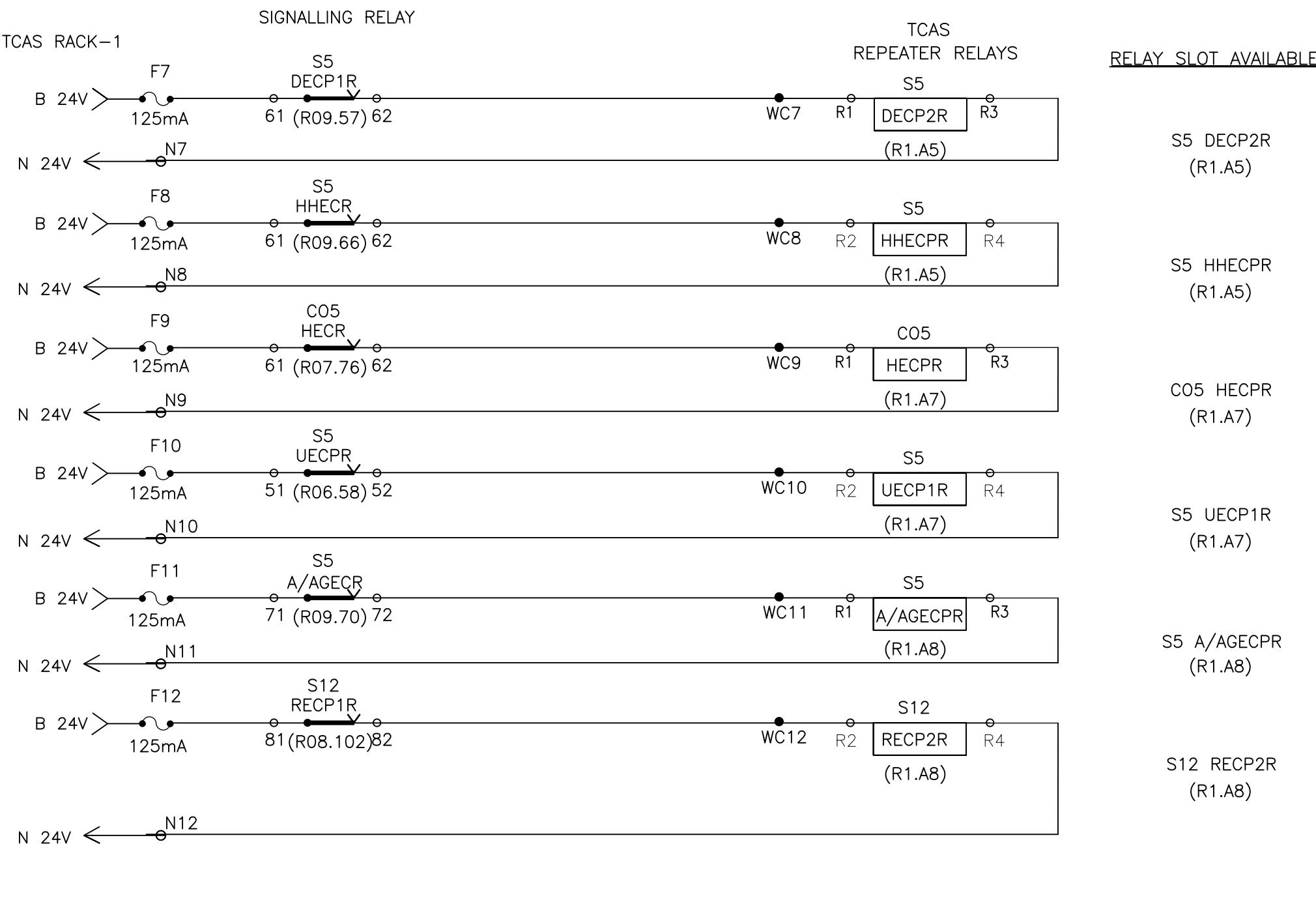
| | | | | | | | | |
|-------------------|------------------|------------------|------------------|-------------------|------------------|------------------|-------------------|------------------|
| 41830AT AXTP3R | C05T TP1R | 01T/AT TP1R | 02T/AT TP3R | 03T/AT TP2R | 04T/AT TP1R | 39T TP1R | C46T TP1R | 12T TP1R |
| [B97] [N98] | [B99] [N100] | [B101] [N102] | [B103] [N104] | [B105] [N106] | [B107] [N108] | [B109] [N110] | [B111] [N112] | [B113] [N114] |
| 2T AXTP3R | 2AT AXTP2R | LC250 XCKP6R | LC251 XCKP7R | 5T TP3R | 46T TP3R | A42218 RECP1R | A42218 HECP2R | A42218 DECP2R |
| [B115] [N116] | [B117] [N118] | [B119] [N120] | [B121] [N122] | [B123] [N124] | [B125] [N126] | [B127] [N128] | [B129] [N130] | [B131] [N132] |
| A42218 HHECP1R | S49 RECP2R | S49 HECP2R | S49 DECP2R | S49 HHECP2R | S49 AECP1R | 42218T AXTP2R | 42218AT AXTP1R | 49T TP1R |
| [B133] [N134] | [B135] [N136] | [B137] [N138] | [B139] [N140] | [B141] [N142] | [B143] [N144] | [B145] [N146] | [B147] [N148] | [B149] [N150] |
| 49AT TP1R | S41819 RECP2R | S41819 HECP2R | S41819 DECP2R | S41819 HHECP2R | S41819 AECP1R | 41819T AXTP1R | 41819AT AXTP2R | LC248 XCKP3R |
| [B151] [N152] | [B153] [N154] | [B155] [N156] | [B157] [N158] | [B159] [N160] | [B161] [N162] | [B163] [N164] | [B165] [N166] | [B167] [N168] |

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|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| [A] | F1 | F2 | F3 | F4 | F5 | F6 | F7 | F8 | F9 | F10 | F11 | F12 | F13 | F14 | F15 | F16 | F17 | F18 | F19 | F20 | F21 | F22 | F23 | F24 | F25 | F26 | F27 | F28 | F29 | F30 | F31 | F32 |
| [B] | N1 | N2 | N3 | N4 | N5 | N6 | N7 | N8 | N9 | N10 | N11 | N12 | N13 | N14 | N15 | N16 | N17 | N18 | N19 | N20 | N21 | N22 | N23 | N24 | N25 | N26 | N27 | N28 | N29 | N30 | N31 | N32 |
| [A] | F33 | F34 | F35 | F36 | F37 | F38 | F39 | F40 | F41 | F42 | F43 | F44 | F45 | F46 | F47 | F48 | F49 | F50 | F51 | F52 | F53 | F54 | F55 | F56 | F57 | F58 | F59 | F60 | F61 | F62 | F63 | F64 |
| [B] | N33 | N34 | N35 | N36 | N37 | N38 | N39 | N40 | N41 | N42 | N43 | N44 | N45 | N46 | N47 | N48 | N49 | N50 | N51 | N52 | N53 | N54 | N55 | N56 | N57 | N58 | N59 | N60 | N61 | N62 | N63 | N64 |
| [A] | F65 | F66 | F67 | F68 | F69 | F70 | F71 | F72 | F73 | F74 | F75 | F76 | F77 | F78 | F79 | F80 | F81 | F82 | F83 | F84 | F85 | F86 | F87 | F88 | F89 | F90 | F91 | F92 | F93 | F94 | F95 | F96 |
| [B] | N65 | N66 | N67 | N68 | N69 | N70 | N71 | N72 | N73 | N74 | N75 | N76 | N77 | N78 | N79 | N80 | N81 | N82 | N83 | N84 | N85 | N86 | N87 | N88 | N89 | N90 | N91 | N92 | N93 | N94 | N95 | N96 |

| HBL | DESIGN | | | | VERIFIED BY DIVISION | | | | TCAS INTERFACE CIRCUIT | | SIGNAL & TELECOMMUNICATIONS ADASROAD (ADD) WESTERN RAILWAY DIVISION: VADODARA | | | |
|--|------------------|--|------------|---------------------------|----------------------|--|---------|--|-------------------------------|--|--|---------------------------|--|--|
| | Prepared By | | SHREYANSH | Engineer | | | | | TITLE: TERMINATION & FUSES | | | | | |
| | Verified By | | Y.AKHIL | Sr Engineer | | | | | REF: SIG PLAN NO. | | | | | |
| | Approved By | | MVH.PRASAD | Design Director | | | SSE/Sig | | ADSTE | | IPU. 2059/4C | WR.DRG.NO.TCAS 2059/4C | | |
| | Version | | V_2.1.5 | | | | | | DSTE | | शीट SHEET | कॉल शीट SHEETS | | |
| | Proof Consultant | | S.CHITRA | Associate General Manager | | | | | Sr.DSTE/N/BRC | | 5 | 43 | | |
| <i>Shreyansh Shreyansh (Aug 24, 2025 15:02:54 GMT+5.5)</i> | | | | | | | | | | | | | | |
| <i>Y.Akhil Y.Akhil (Aug 24, 2025 15:39:29 GMT+5.5)</i> | | | | | | | | | | | | | | |
| <i>MVH.Prasad MVH Prasad (Aug 24, 2025 15:39:29 GMT+5.5)</i> | | | | | | | | | | | | | | |
| <i>S.Chitra S.CHITRA (Aug 25, 2025 17:26:10 GMT+5.5)</i> | | | | | | | | | | | | | | |



| HBL | DESIGN | | | VERIFIED BY DIVISION | | TCAS INTERFACE CIRCUIT | | Signal & Drv Sanchay SIGNAL & TELECOMMUNICATIONS | |
|------------------|-------------|---------------------------|----------|--|---------|------------------------|-------------------|---|---------------------------------------|
| | Prepared By | SHREYANSH | Engineer | S.S. Shreyansh Shreyansh [Aug 24, 2025 15:02:54 GMT+5.5] | SSE/Sig | ADSTE | REF: SIG PLAN NO. | P. रे आरेख सं. WR.DRG.NO.TCAS 2059/4C | |
| Verified By | Y.AKHIL | Sr Engineer | Y.Akhil | M.V.H. Prasad M.V.H. Prasad [Aug 24, 2025 15:39:29 GMT+5.5] | DSTE | | IPU. 2059/4C | कृत शीट कृल कृत SHEETS | ADASROAD (ADD) |
| Approved By | MVH.PRASAD | Design Director | | S.CHIKRA S.CHIKRA [Aug 25, 2025 17:26:10 GMT+5.5] | DSTE | Sr.DSTE/N/BRC | 6 | 43 | |
| Version | V | 2.1.5 | | | | | | | WESTERN RAILWAY DIVISION: VADODARA |
| Proof Consultant | S.CHIKRA | Associate General Manager | | | | | | | |



TCAS RACK-1

HBL

DESIGN

| | | | |
|------------------|------------|---------------------------|--|
| Prepared By | SHREYANSH | Engineer | <small>Shreyansh Shreyansh (Aug 24, 2025 15:02:54 GMT+5:5)</small> |
| Verified By | Y.AKHIL | Sr Engineer | <small>Y.Akhil Y.Akhil</small> |
| Approved By | MVH.PRASAD | Design Director | <small>MVH Prasad MVH Prasad (Aug 24, 2025 15:39:29 GMT+5:5)</small> |
| Version | V_2.1.5 | | |
| Proof Consultant | S.CHITRA | Associate General Manager | <small>S.Chitra S.Chitra (Aug 25, 2025 17:26:10 GMT+5:5)</small> |

VERIFIED BY DIVISION

| | |
|---------|---------------|
| SSE/Sig | ADSTE |
| DSTE | Sr.DSTE/N/BRC |

TCAS INTERFACE CIRCUIT

सिग्नल व द्रव संचार

SIGNAL & TELECOMMUNICATIONS

TITLE:

REPEATER RELAY WIRING DIAGRAM

ADASROAD (ADD)

REF: SIG PLAN NO.

प. रे आरेख. सं.

IPU_2059/4C

WR.DRG.NO.TCAS 2059/4C

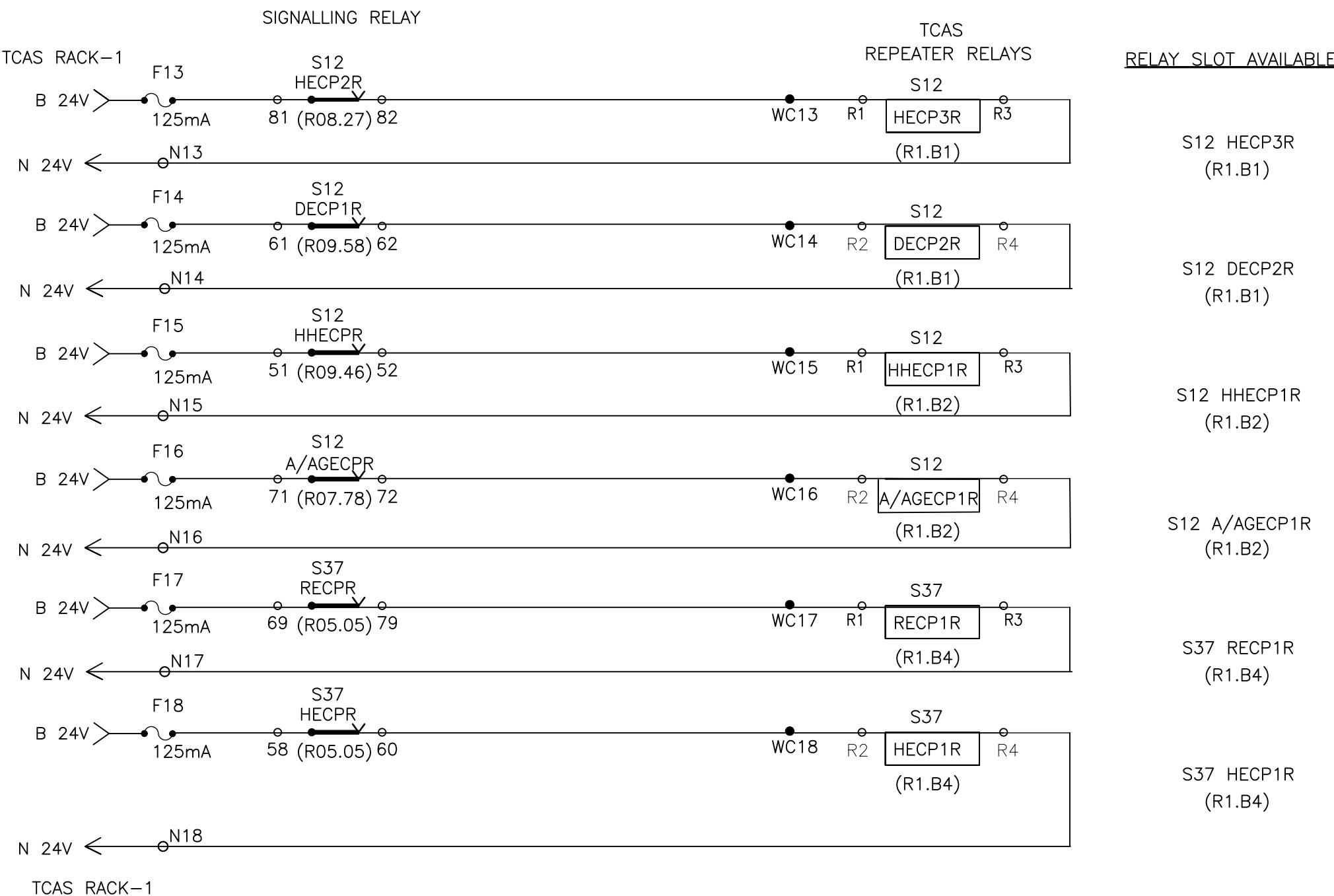
शीट SHEET

कॉल शीट SHEETS

WESTERN RAILWAY

DIVISION: VADODARA

7 43



HBL

DESIGN

| | | | |
|------------------|------------|---------------------------|--|
| Prepared By | SHREYANSH | Engineer | <small>Shreyansh Shreyansh [Aug 24, 2025 15:02:54 GMT+5:5]</small> |
| Verified By | Y.AKHIL | Sr Engineer | <small>Y.Akhil Y.Akhil [Aug 24, 2025 15:39:29 GMT+5:5]</small> |
| Approved By | MVH.PRASAD | Design Director | <small>MVH Prasad MVH Prasad [Aug 24, 2025 15:39:29 GMT+5:5]</small> |
| Version | V_2.1.5 | | |
| Proof Consultant | S.CHITRA | Associate General Manager | <small>S.Chitra S.Chitra [Aug 25, 2025 17:26:10 GMT+5:5]</small> |

VERIFIED BY DIVISION

DSTE

Sr.DSTE/N/BRC

TCAS INTERFACE CIRCUIT

सिग्नल व द्रव संचार

SIGNAL & TELECOMMUNICATIONS

TITLE:

REPEATER RELAY WIRING DIAGRAM

ADASROAD (ADD)

REF: SIG PLAN NO.

प. रे आरेख. सं.

IPU_2059/4C

WR.DRG.NO.TCAS 2059/4C

शीट SHEET

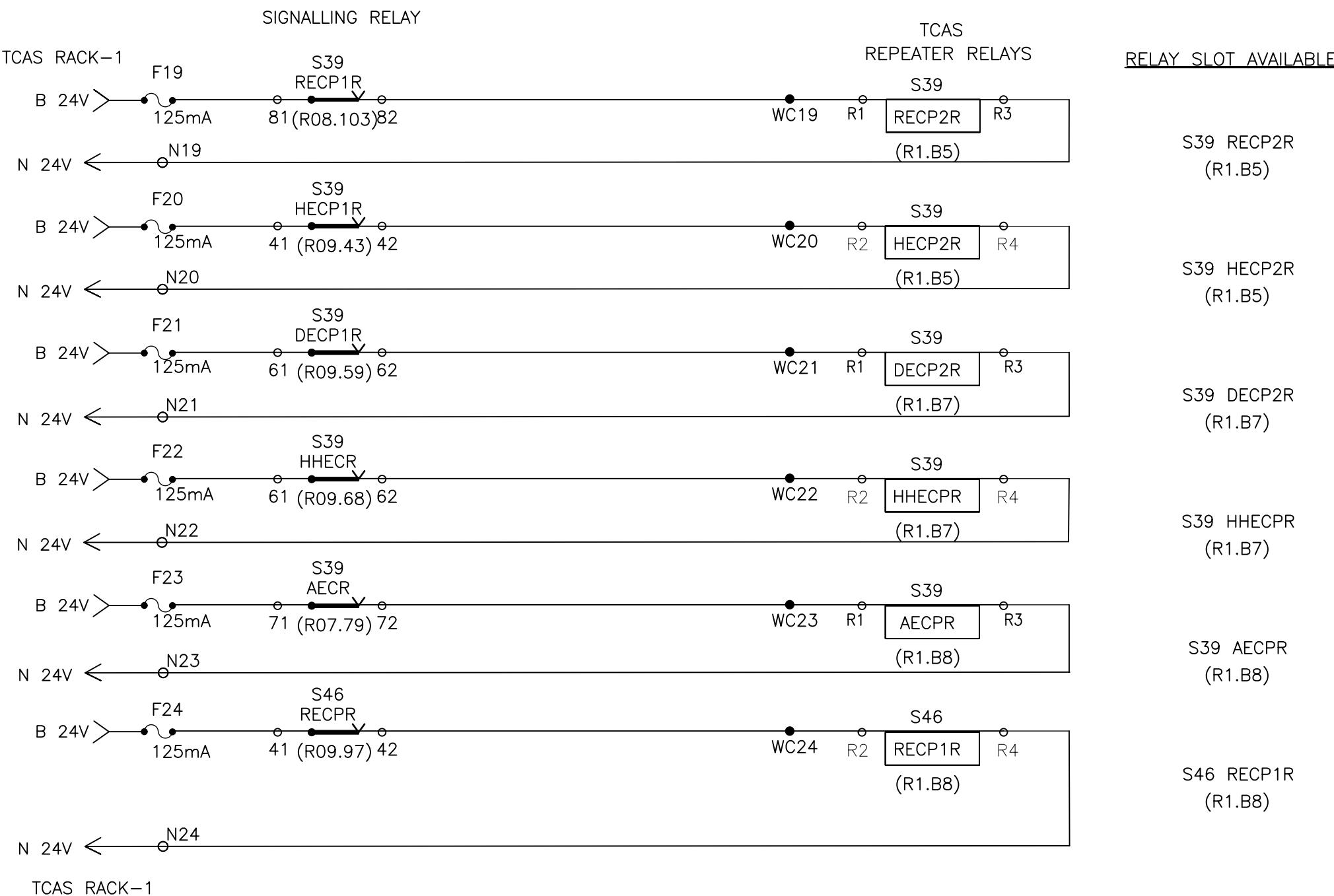
कॉल शीट SHEETS

WESTERN RAILWAY

DIVISION: VADODARA

8

43


HBL
DESIGN

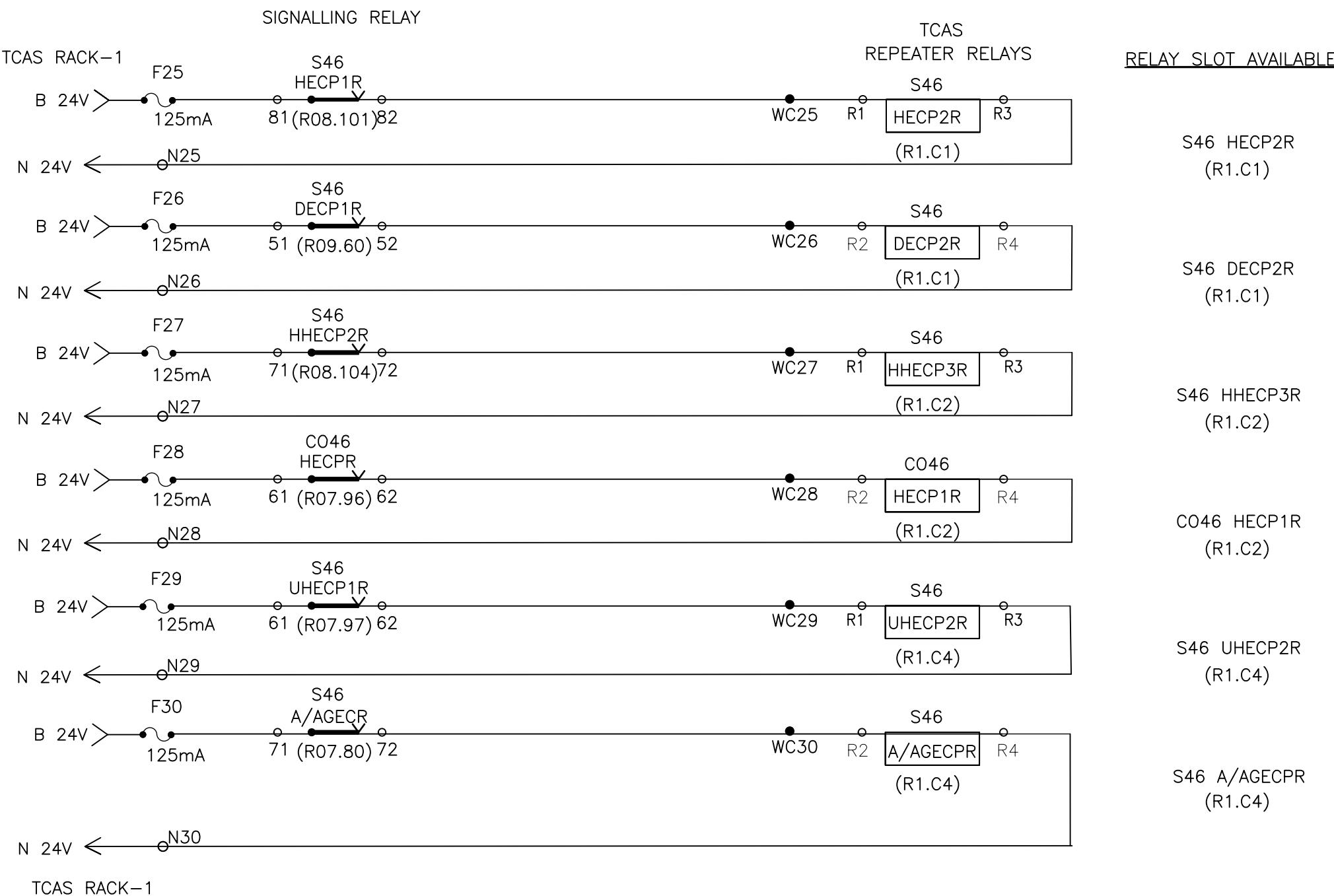
| | | | |
|------------------|------------|---------------------------|--|
| Prepared By | SHREYANSH | Engineer | <small>Shreyansh Shreyansh (Aug 24, 2025 15:02:54 GMT+5:5)</small> |
| Verified By | Y.AKHIL | Sr Engineer | <small>Y.Akhil Y.Akhil</small> |
| Approved By | MVH.PRASAD | Design Director | <small>MVH Prasad MVH Prasad (Aug 24, 2025 15:39:29 GMT+5:5)</small> |
| Version | V_2.1.5 | | |
| Proof Consultant | S.CHITRA | Associate General Manager | <small>S.Chitra S.Chitra (Aug 25, 2025 17:26:10 GMT+5:5)</small> |

VERIFIED BY DIVISION

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| SSE/Sig | ADSTE |
| | DSTE |
| | Sr.DSTE/N/BRC |

TCAS INTERFACE CIRCUIT

| | | |
|---|--|---------------------------------------|
| TITLE: REPEATER RELAY WIRING DIAGRAM | | सिग्नल व द्रव संचार |
| REF: SIG PLAN NO. | | SIGNAL & TELECOMMUNICATIONS |
| IPU_2059/4C | प. रे आरेख. सं. WR.DRG.NO.TCAS 2059/4C | ADASROAD (ADD) |
| शीट SHEET | कॉल शीट SHEETS | WESTERN RAILWAY DIVISION: VADODARA |
| 9 | 43 | |



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DESIGN

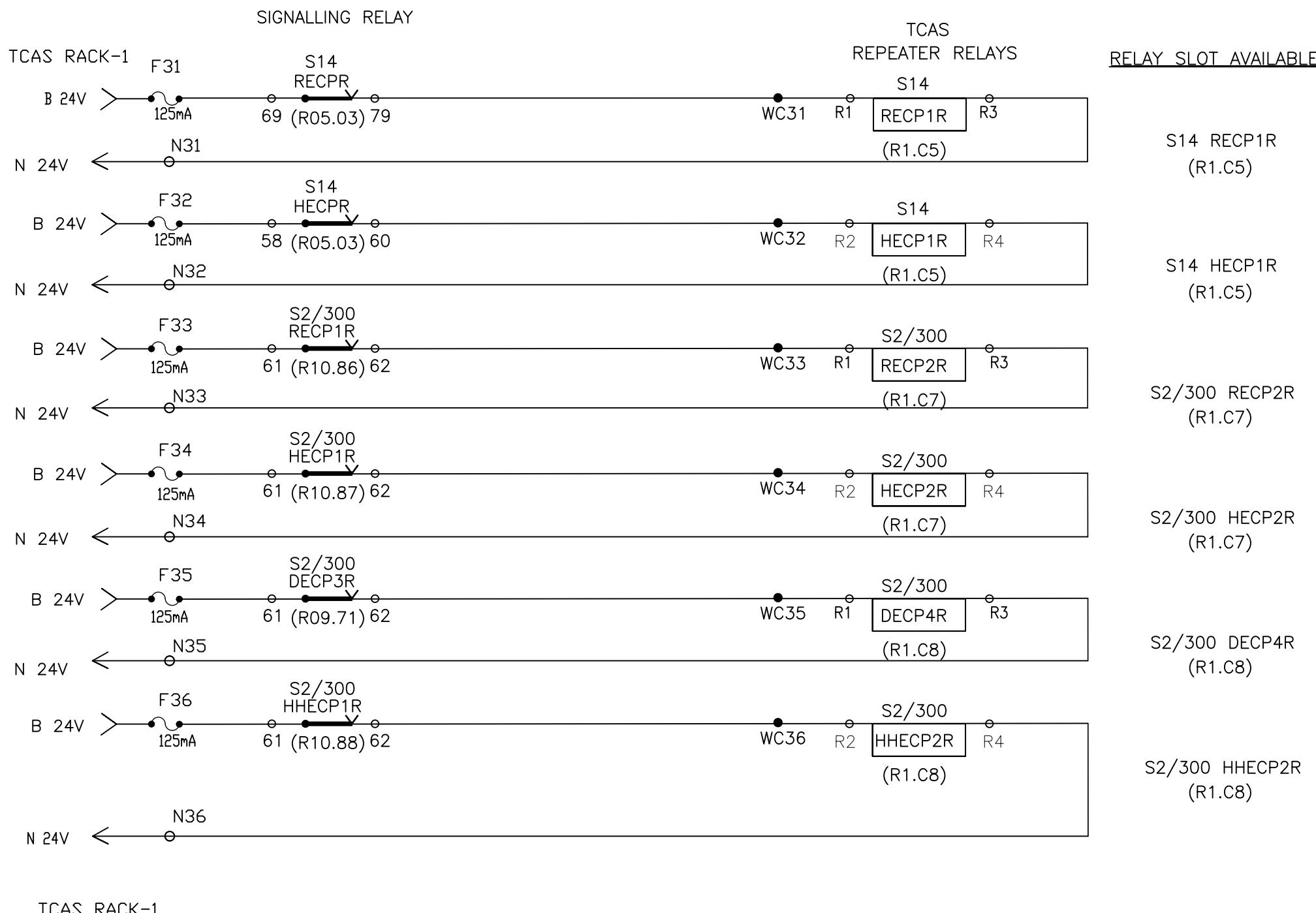
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| Prepared By | SHREYANSH | Engineer | <small>Shreyansh Shreyansh (Aug 24, 2025 15:02:54 GMT+5:5)</small> |
| Verified By | Y.AKHIL | Sr Engineer | <small>Y.Akhil Y.Akhil</small> |
| Approved By | MVH.PRASAD | Design Director | <small>MVH Prasad MVH Prasad (Aug 24, 2025 15:39:29 GMT+5:5)</small> |
| Version | V_2.1.5 | | |
| Proof Consultant | S.CHITRA | Associate General Manager | <small>S.Chitra S.Chitra (Aug 25, 2025 17:26:10 GMT+5:5)</small> |

VERIFIED BY DIVISION

DSTE

| TCAS INTERFACE CIRCUIT | |
|---|------------------------|
| TITLE: REPEATER RELAY WIRING DIAGRAM | |
| REF: SIG PLAN NO. | प. रे आरेख. सं. |
| IPU_2059/4C | WR.DRG.NO.TCAS 2059/4C |
| शीट SHEET | कुल शीट SHEETS |
| 10 | 43 |

सिग्नल व द्रव संचार
SIGNAL & TELECOMMUNICATIONS
ADASROAD (ADD)
WESTERN RAILWAY
DIVISION: VADODARA



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DESIGN

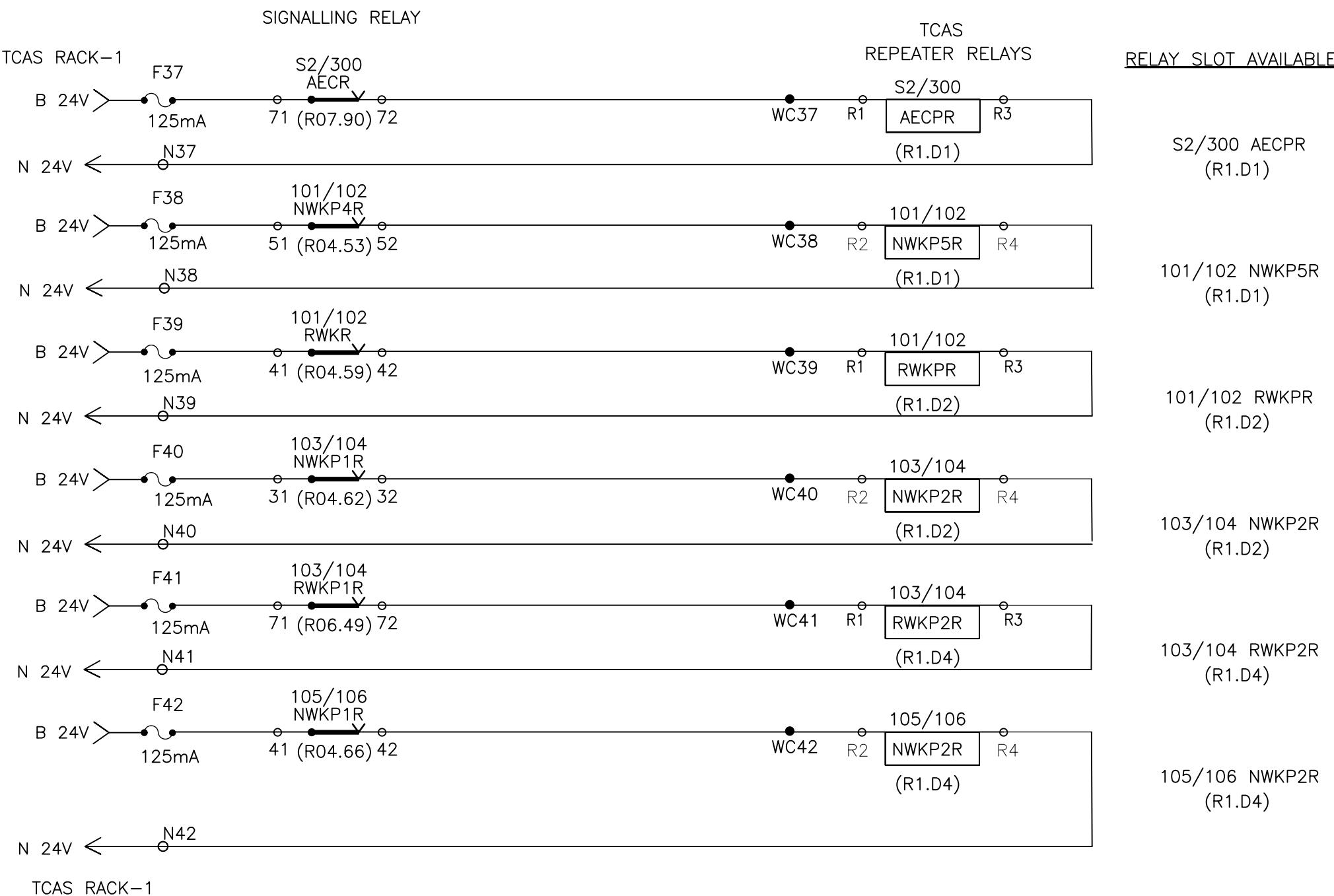
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| Prepared By | SHREYANSH | Engineer | <small>Shreyansh Shreyansh (Aug 24, 2025 15:02:54 GMT+5.5)</small> |
| Verified By | Y.AKHIL | Sr Engineer | <small>Y.Akhil Y.Akhil</small> |
| Approved By | MVH.PRASAD | Design Director | <small>MVH Prasad MVH Prasad (Aug 24, 2025 15:39:29 GMT+5.5)</small> |
| Version | V_2.1.5 | | |
| Proof Consultant | S.CHITRA | Associate General Manager | <small>S.Chitra S.Chitra (Aug 25, 2025 17:26:10 GMT+5.5)</small> |

VERIFIED BY DIVISION

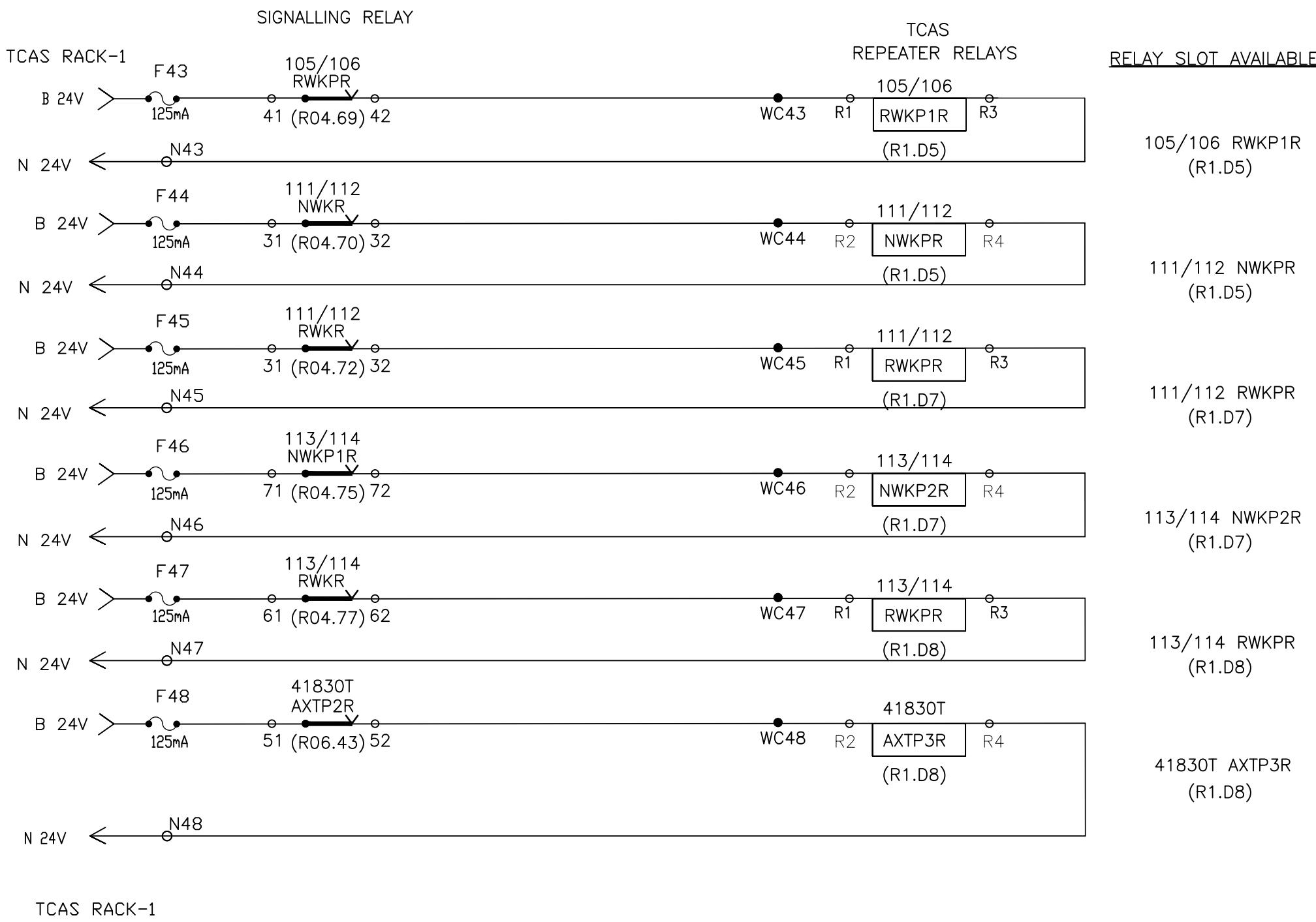
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| SSE/Sig | ADSTE |
| DSTE | Sr.DSTE/N/BRC |

TCAS INTERFACE CIRCUIT

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|---|--|-----------------------------|
| TITLE: REPEATER RELAY WIRING DIAGRAM | | सिग्नल व द्रव संचार |
| REF: SIG PLAN NO. | | SIGNAL & TELECOMMUNICATIONS |
| IPU_2059/4C | प. रे आरेख. सं. WR.DRG.NO.TCAS 2059/4C | ADASROAD (ADD) |
| शीट SHEET | कॉल शीट SHEETS | WESTERN RAILWAY |
| 11 | 43 | DIVISION: VADODARA |



| HBL | DESIGN | | | VERIFIED BY DIVISION | | TCAS INTERFACE CIRCUIT | | Signal & Telecommunications ADASROAD (ADD) |
|------------------|-------------|---------------------------|--|---|---------|------------------------|-------------------|---|
| | Prepared By | SHREYANSH | Engineer | S.SHEYANSH Shreyansh (Aug 24, 2025 15:02:54 GMT+5.5) | SSE/Sig | ADSTE | REF: SIG PLAN NO. | P. रे आरेख सं. WR.DRG.NO.TCAS 2059/4C |
| Verified By | Y.AKHIL | Sr Engineer | Y.Akhil | | | | | |
| Approved By | MVH.PRASAD | Design Director | MVH.Prasad (Aug 24, 2025 15:39:29 GMT+5.5) | | | | | |
| Version | V_2.1.5 | | | | | | | |
| Proof Consultant | S.CHITRA | Associate General Manager | S.CHITRA (Aug 25, 2025 17:26:10 GMT+5.5) | DSTE | | Sr.DSTE/N/BRC | 12 | 43 |



TCAS RACK-1

HBL

DESIGN

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|------------------|------------|---------------------------|---|
| Prepared By | SHREYANSH | Engineer | <i>Shreyansh</i> Shreyansh (Aug 24, 2025 15:02:54 GMT+5:5) |
| Verified By | Y.AKHIL | Sr Engineer | <i>Y.Akhil</i> |
| Approved By | MVH.PRASAD | Design Director | <i>MVH Prasad</i> MVH Prasad (Aug 24, 2025 15:39:29 GMT+5:5) |
| Version | V | 2.1.5 | |
| Proof Consultant | S.CHITRA | Associate General Manager | <i>S.Chitra</i> S.CHITRA (Aug 25, 2025 17:26:10 GMT+5:5) |

VERIFIED BY DIVISION

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|---------|---------------|
| SSE/Sig | ADSTE |
| DSTE | Sr.DSTE/N/BRC |
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TCAS INTERFACE CIRCUIT

सिग्नल व द्रव संचार

SIGNAL & TELECOMMUNICATIONS

TITLE:

REPEATER RELAY WIRING DIAGRAM

ADASROAD (ADD)

REF: SIG PLAN NO.

प. रे आरेख. सं.

WESTERN RAILWAY

IPU_2059/4C

WR.DRG.NO.TCAS 2059/4C

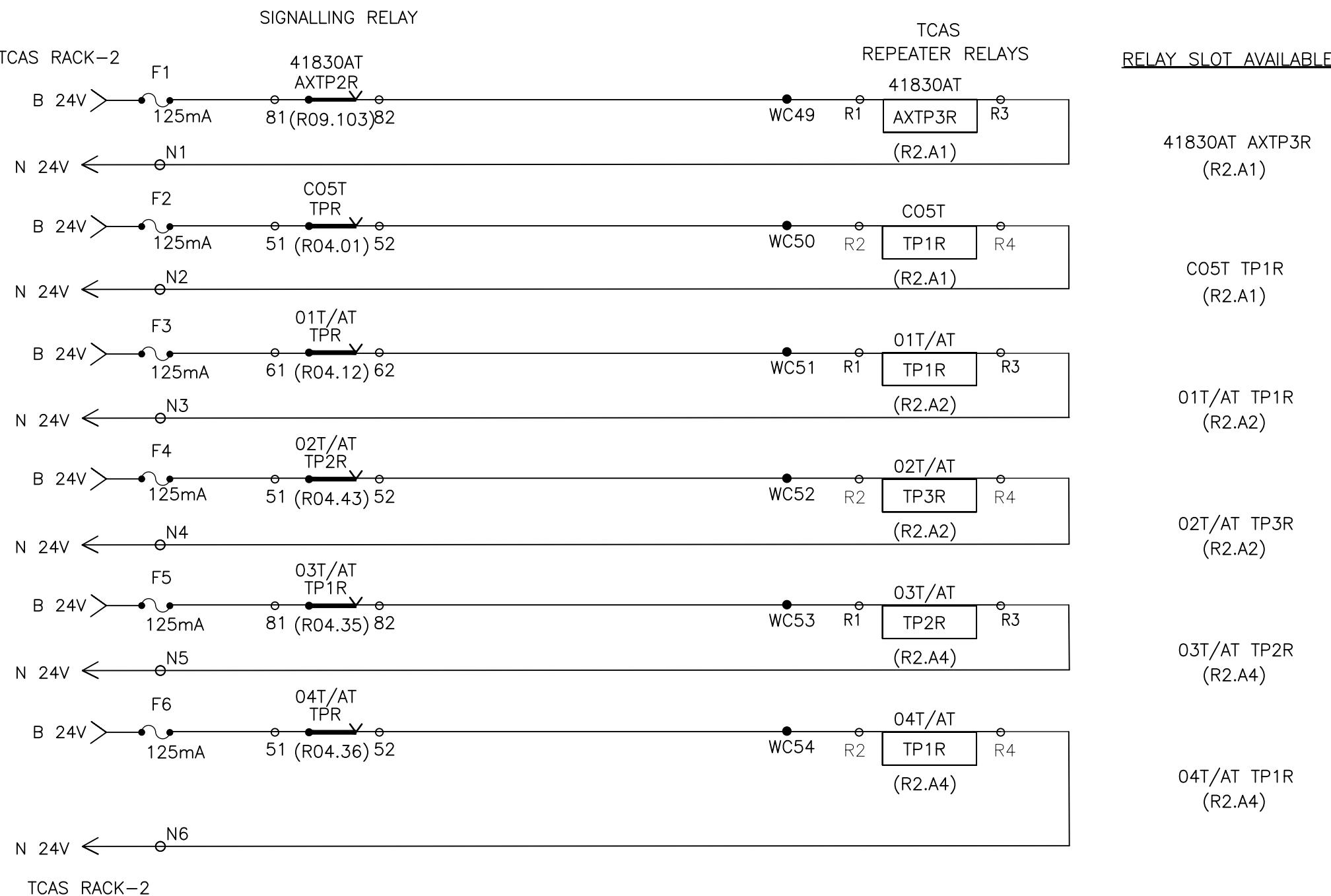
DIVISION: VADODARA

शीट SHEET

कॉल शीट SHEETS

13

43



HBL

DESIGN

| | | | |
|------------------|------------|---------------------------|--|
| Prepared By | SHREYANSH | Engineer | <small>Shreyansh Shreyansh [Aug 24, 2025 15:02:54 GMT+5:5]</small> |
| Verified By | Y.AKHIL | Sr Engineer | <small>Y.Akhil Y.Akhil [Aug 24, 2025 15:39:29 GMT+5:5]</small> |
| Approved By | MVH.PRASAD | Design Director | <small>MVH Prasad MVH Prasad [Aug 24, 2025 15:39:29 GMT+5:5]</small> |
| Version | V_2.1.5 | | |
| Proof Consultant | S.CHITRA | Associate General Manager | <small>S.Chitra S.Chitra [Aug 25, 2025 17:26:10 GMT+5:5]</small> |

VERIFIED BY DIVISION

DSTE

Sr.DSTE/N/BRC

TCAS INTERFACE CIRCUIT

सिग्नल व द्रव संचार

SIGNAL & TELECOMMUNICATIONS

TITLE:

REPEATER RELAY WIRING DIAGRAM

ADASROAD (ADD)

REF: SIG PLAN NO.

प. रे आरेख. सं.

IPU_2059/4C

WR.DRG.NO.TCAS 2059/4C

शीट SHEET

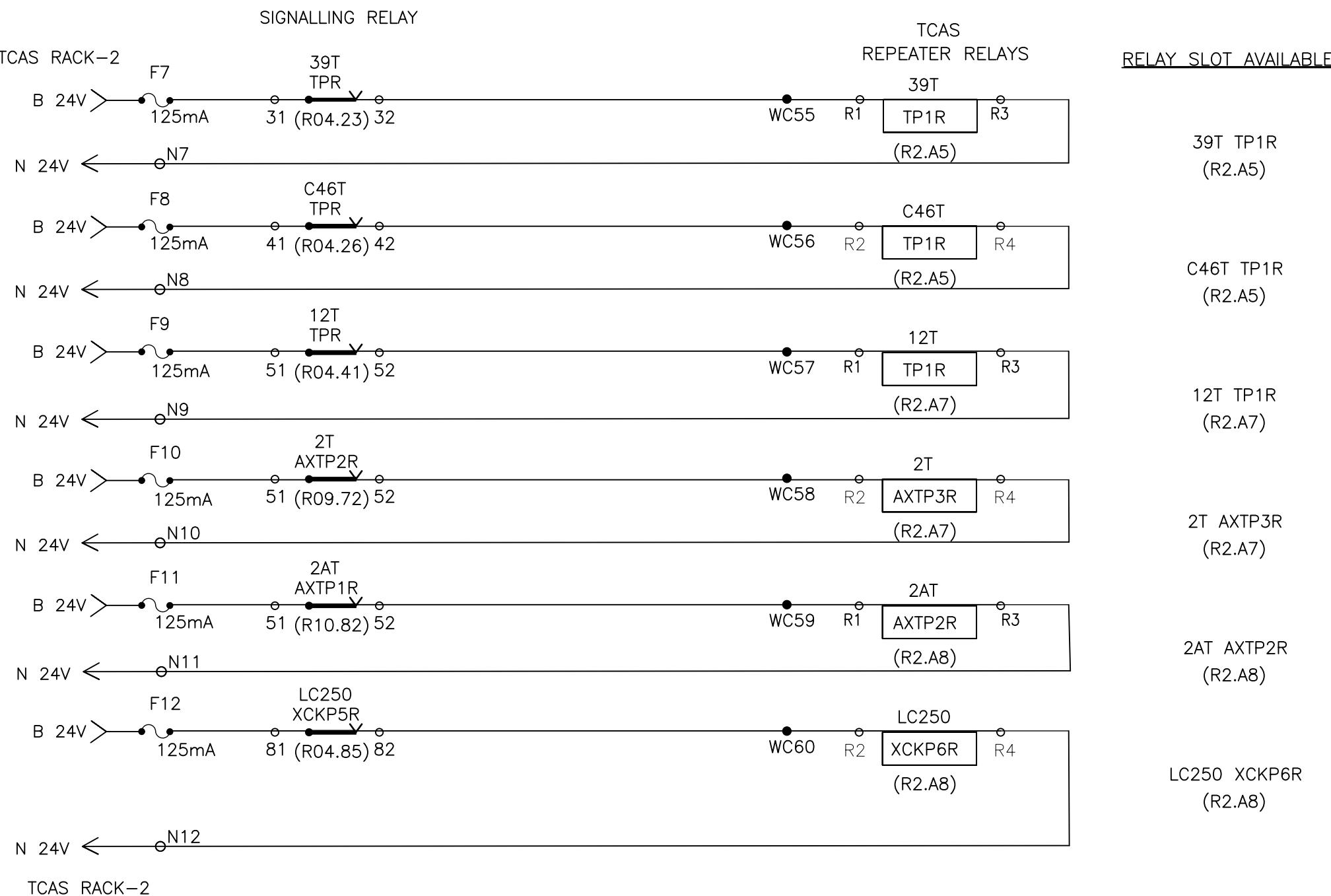
कॉल शीट SHEETS

14

43

WESTERN RAILWAY

DIVISION: VADODARA



HBL

DESIGN

| | | | |
|------------------|------------|---------------------------|--|
| Prepared By | SHREYANSH | Engineer | <small>Shreyansh Shreyansh (Aug 24, 2025 15:02:54 GMT+5.5)</small> |
| Verified By | Y.AKHIL | Sr Engineer | <small>Y.Akhil Y.Akhil</small> |
| Approved By | MVH.PRASAD | Design Director | <small>MVH Prasad MVH Prasad (Aug 24, 2025 15:39:29 GMT+5.5)</small> |
| Version | V_2.1.5 | | |
| Proof Consultant | S.CHITRA | Associate General Manager | <small>S.Chitra S.Chitra (Aug 25, 2025 17:26:10 GMT+5.5)</small> |

VERIFIED BY DIVISION

DSTE

Sr.DSTE/N/BRC

TCAS INTERFACE CIRCUIT

सिग्नल व द्रव संचार

SIGNAL & TELECOMMUNICATIONS

TITLE:

REPEATER RELAY WIRING DIAGRAM

ADASROAD (ADD)

REF: SIG PLAN NO.

प. रे आरेख. सं.

IPU_2059/4C

WR.DRG.NO.TCAS 2059/4C

शीट SHEET

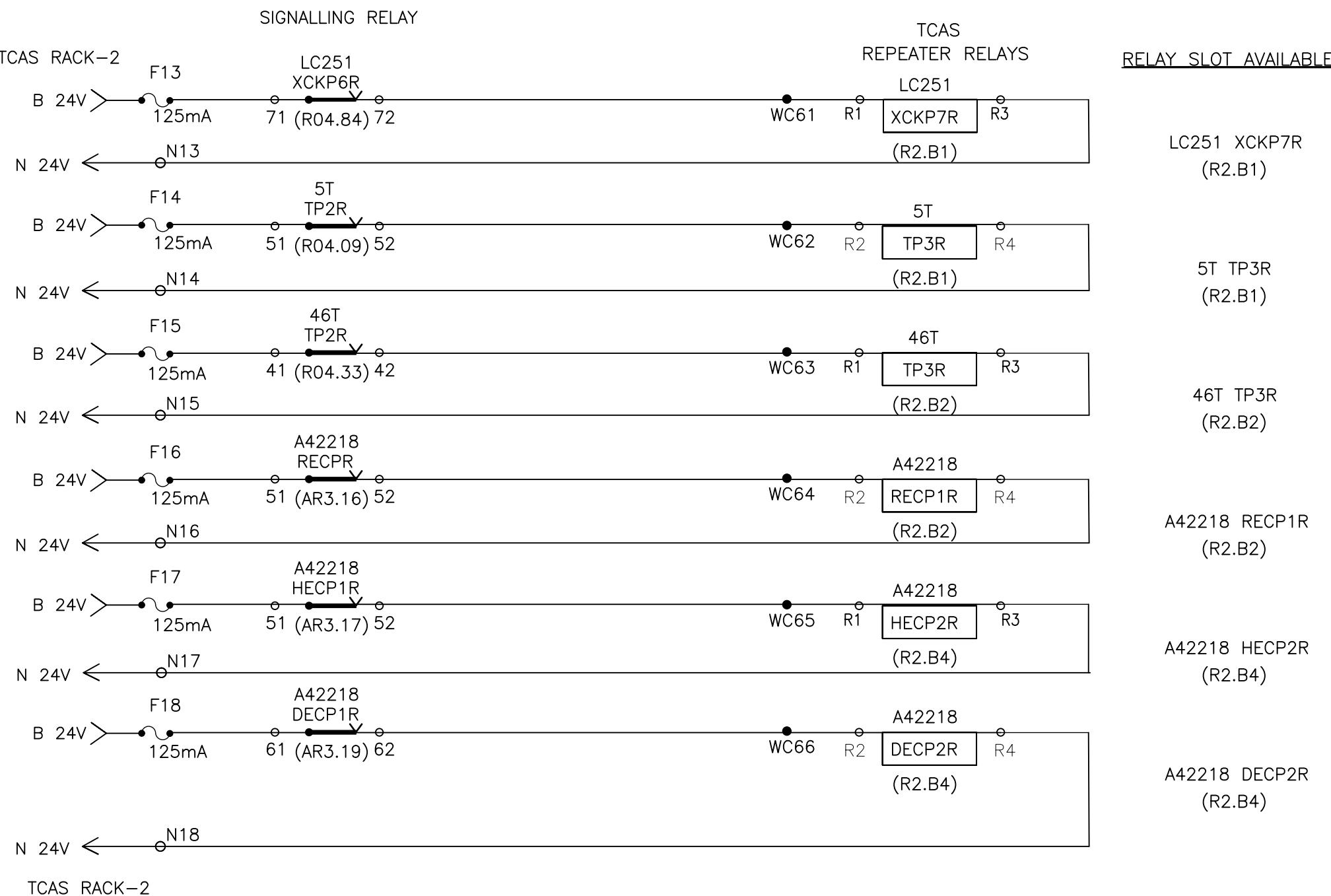
कॉल शीट SHEETS

WESTERN RAILWAY

DIVISION: VADODARA

15

43



HBL

DESIGN

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|------------------|------------|---------------------------|--|
| Prepared By | SHREYANS | Engineer | <small>Shreyansh Shreyansh [Aug 24, 2025 15:02:54 GMT+5:5]</small> |
| Verified By | Y.AKHIL | Sr Engineer | <small>Y.Akhil Y.Akhil [Aug 24, 2025 15:39:29 GMT+5:5]</small> |
| Approved By | MVH.PRASAD | Design Director | <small>MVH Prasad MVH Prasad [Aug 24, 2025 15:39:29 GMT+5:5]</small> |
| Version | V_2.1.5 | | |
| Proof Consultant | S.CHITRA | Associate General Manager | <small>S.Chitra S.Chitra [Aug 25, 2025 17:26:10 GMT+5:5]</small> |

VERIFIED BY DIVISION

| | |
|---------|---------------|
| SSE/Sig | ADSTE |
| DSTE | Sr.DSTE/N/BRC |
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TCAS INTERFACE CIRCUIT

TITLE:
REPEATER RELAY WIRING DIAGRAM

सिग्नल व द्रव संचार

SIGNAL & TELECOMMUNICATIONS

ADASROAD (ADD)

REF: SIG PLAN NO. प. रे आरेख. सं.

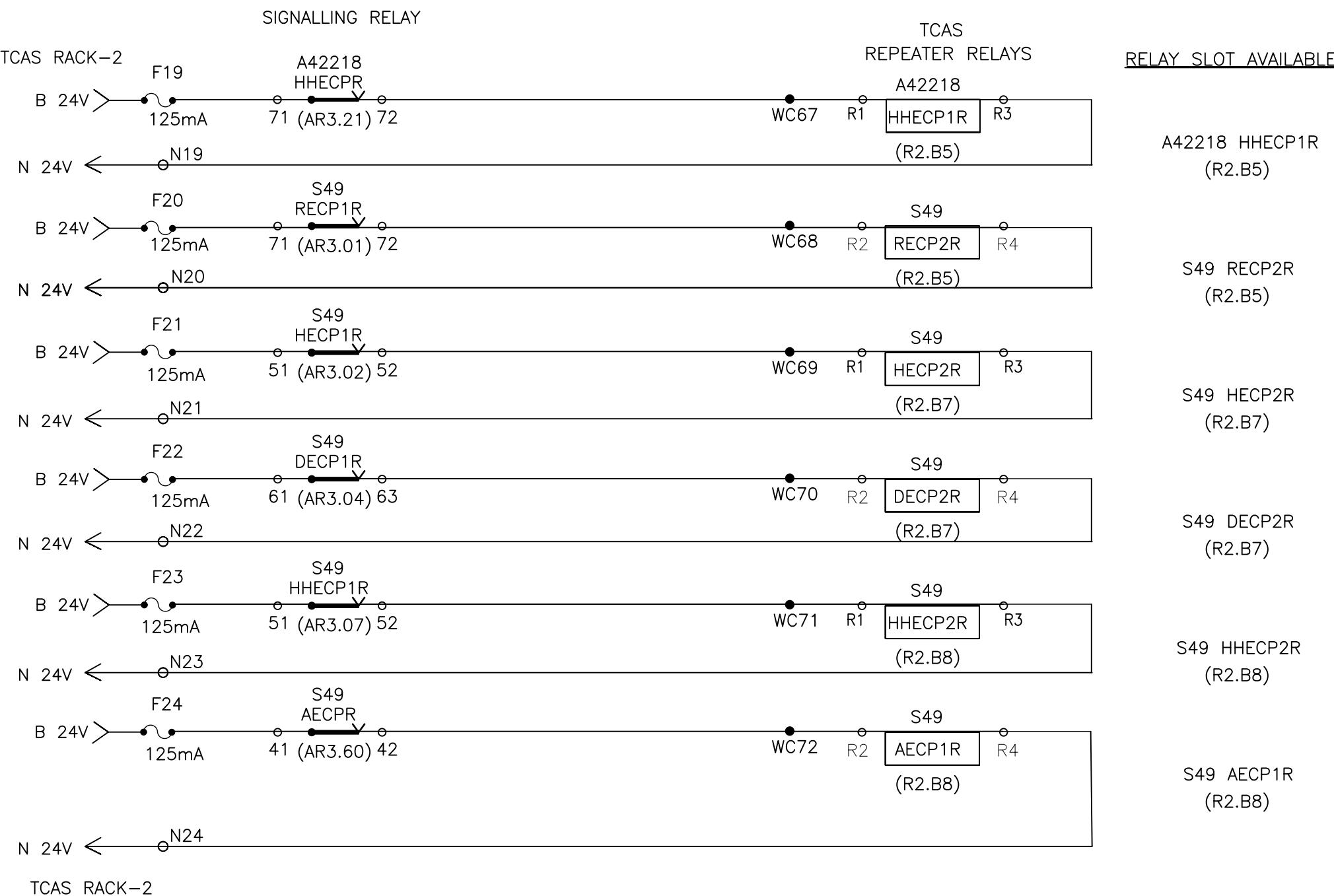
IPU_2059/4C WR.DRG.NO.TCAS 2059/4C

शीट SHEET कूल शीट SHEETS

WESTERN RAILWAY

DIVISION: VADODARA

16 43



HBL

DESIGN

| | | | |
|------------------|------------|---------------------------|--|
| Prepared By | SHREYANSH | Engineer | <small>Shreyansh Shreyansh (Aug 24, 2025 15:02:54 GMT+5.5)</small> |
| Verified By | Y.AKHIL | Sr Engineer | <small>Y.Akhil Y.Akhil</small> |
| Approved By | MVH.PRASAD | Design Director | <small>MVH Prasad MVH Prasad (Aug 24, 2025 15:39:29 GMT+5.5)</small> |
| Version | V_2.1.5 | | |
| Proof Consultant | S.CHITRA | Associate General Manager | <small>S.Chitra S.Chitra (Aug 25, 2025 17:26:10 GMT+5.5)</small> |

VERIFIED BY DIVISION

DSTE

Sr.DSTE/N/BRC

TCAS INTERFACE CIRCUIT

सिग्नल व द्रव संचार

SIGNAL & TELECOMMUNICATIONS

TITLE:

REPEATER RELAY WIRING DIAGRAM

ADASROAD (ADD)

REF: SIG PLAN NO.

प. रे आरेख. सं.

IPU_2059/4C

WR.DRG.NO.TCAS 2059/4C

शीट SHEET

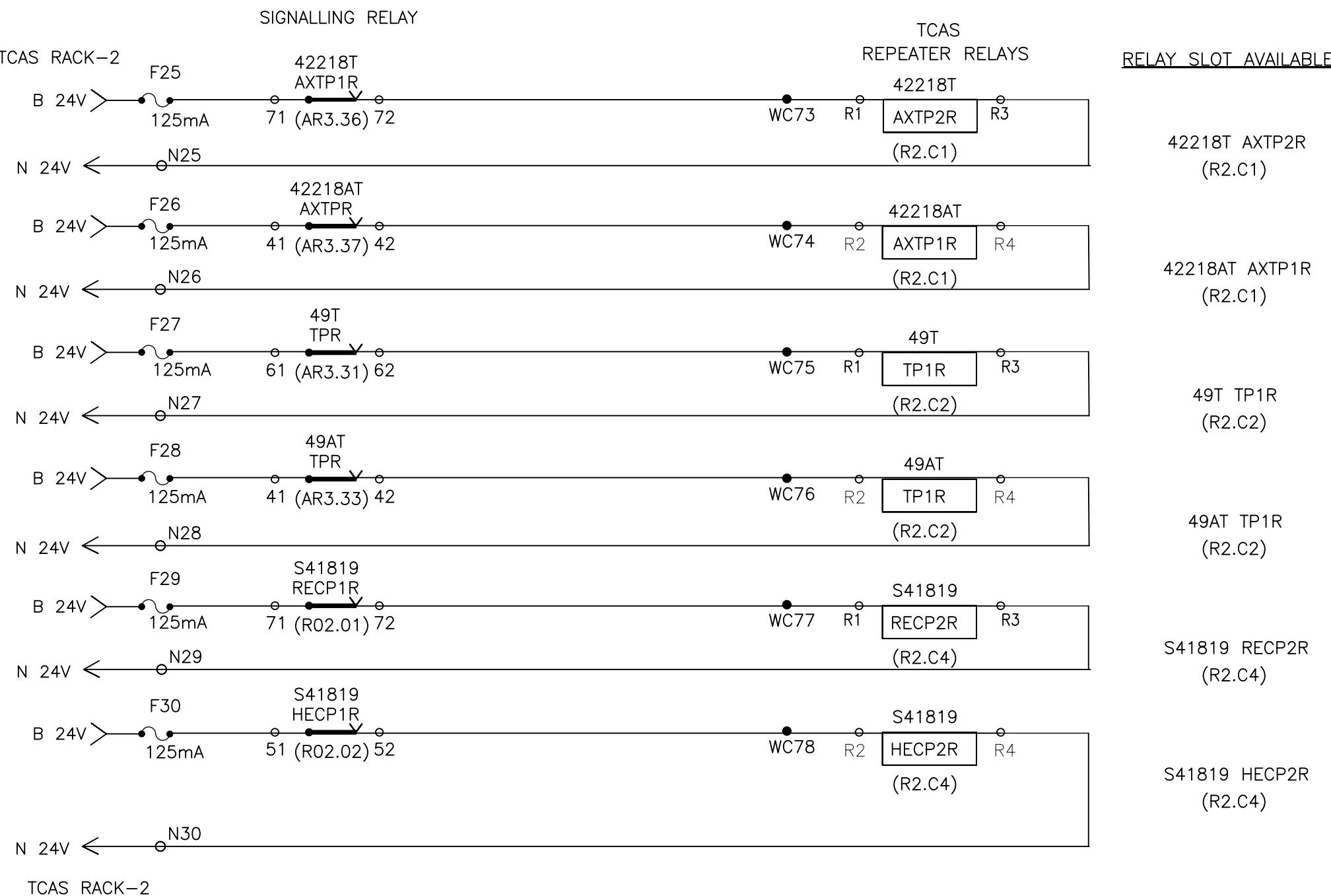
कॉल शीट SHEETS

WESTERN RAILWAY

DIVISION: VADODARA

17

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HBL

DESIGN

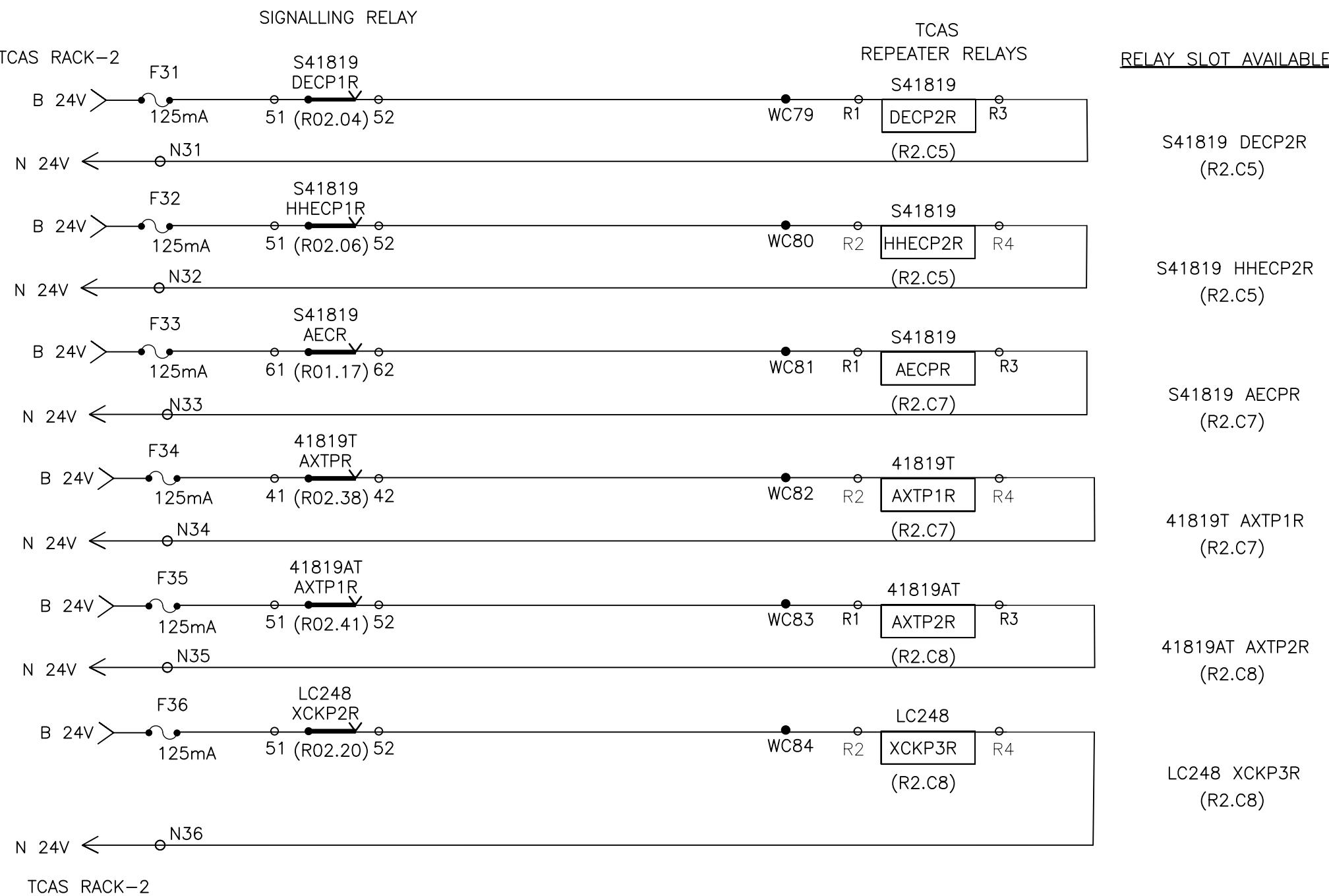
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| Prepared By | SHREYANSH | Engineer | <small>Shreyansh Shreyansh (Aug 24, 2025 15:02:54 GMT+5.5)</small> |
| Verified By | Y.AKHIL | Sr Engineer | <small>Y.Akhil Y.Akhil</small> |
| Approved By | MVH.PRASAD | Design Director | <small>MVH Prasad MVH Prasad (Aug 24, 2025 15:39:29 GMT+5.5)</small> |
| Version | V_2.1.5 | | |
| Proof Consultant | S.CHITRA | Associate General Manager | <small>S.Chitra S.Chitra (Aug 25, 2025 17:26:10 GMT+5.5)</small> |

VERIFIED BY DIVISION

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| SSE/Sig | ADSTE |
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| | Sr.DSTE/N/BRC |

TCAS INTERFACE CIRCUIT

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| TITLE: | SIGNAL & TELECOMMUNICATIONS | |
| REPEATER RELAY WIRING DIAGRAM | | |
| REF: SIG PLAN NO. | प. रे आरेख. सं. | |
| IPU_2059/4C | WR.DRG.NO.TCAS 2059/4C | |
| शीट SHEET | कॉल शीट SHEETS | |
| 18 | 43 | WESTERN RAILWAY DIVISION: VADODARA |



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DESIGN

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|------------------|------------|---------------------------|--|
| Prepared By | SHREYANSH | Engineer | <small>Shreyansh Shreyansh [Aug 24, 2025 15:02:54 GMT+5.5]</small> |
| Verified By | Y.AKHIL | Sr Engineer | <small>Y.Akhil Y.Akhil [Aug 24, 2025 15:39:29 GMT+5.5]</small> |
| Approved By | MVH.PRASAD | Design Director | <small>MVH Prasad MVH Prasad [Aug 24, 2025 15:39:29 GMT+5.5]</small> |
| Version | V_2.1.5 | | |
| Proof Consultant | S.CHITRA | Associate General Manager | <small>S.Chitra S.Chitra [Aug 25, 2025 17:26:10 GMT+5.5]</small> |

VERIFIED BY DIVISION

DSTE

Sr.DSTE/N/BRC

TCAS INTERFACE CIRCUIT

सिग्नल व द्रव संचार

SIGNAL & TELECOMMUNICATIONS

TITLE:

REPEATER RELAY WIRING DIAGRAM

ADASROAD (ADD)

REF: SIG PLAN NO.

प. रे आरेख सं.

IPU_2059/4C

WR.DRG.NO.TCAS 2059/4C

शीट SHEET

कॉल शीट SHEETS

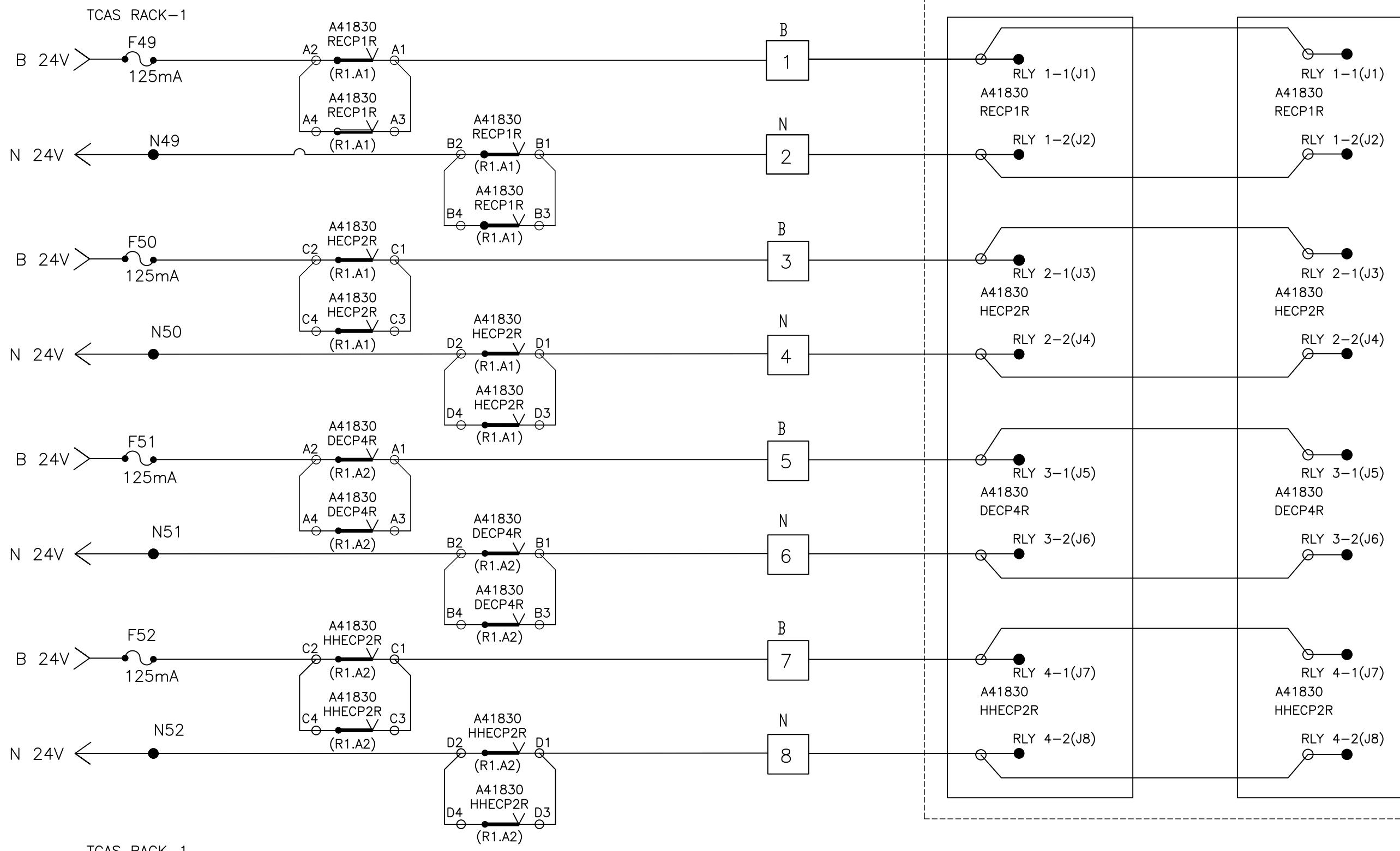
WESTERN RAILWAY

DIVISION: VADODARA

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STATION TCAS



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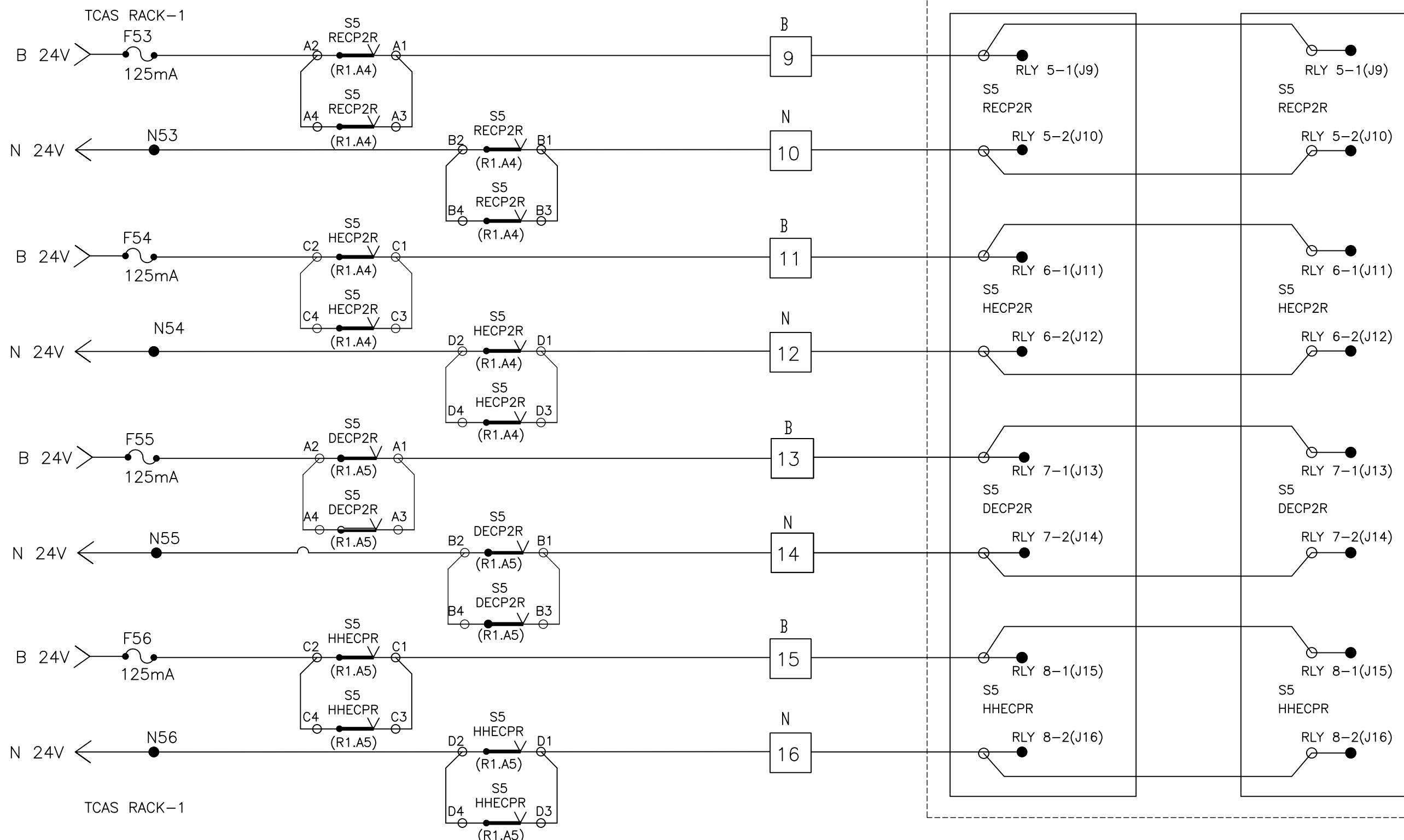
DESIGN

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| Prepared By | SHREYANSH | Engineer | <small>Shreyansh Shreyansh [Aug 24, 2025 15:02:54 GMT+5:5]</small> |
| Verified By | Y.AKHIL | Sr Engineer | <small>Y.Akhil Y.Akhil [Aug 24, 2025 15:02:54 GMT+5:5]</small> |
| Approved By | MVH.PRASAD | Design Director | <small>MVH.Prasad MVH Prasad [Aug 24, 2025 15:39:29 GMT+5:5]</small> |
| Version | V_2.1.5 | | |
| Proof Consultant | S.CHITRA | Associate General Manager | <small>S.Chitra S.Chitra [Aug 25, 2025 17:26:10 GMT+5:5]</small> |

VERIFIED BY DIVISION

| TCAS INTERFACE CIRCUIT | | सिग्नल व द्रव संचार |
|------------------------|------------------------|---------------------------------------|
| TITLE: | | SIGNAL & TELECOMMUNICATIONS |
| RELAY WIRING DIAGRAM | | |
| REF: SIG PLAN NO. | प. रे आरेख. सं. | |
| IPU_2059/4C | WR.DRG.NO.TCAS 2059/4C | ADASROAD (ADD) |
| शीट SHEET | कॉल शीट SHEETS | |
| 20 | 43 | WESTERN RAILWAY DIVISION: VADODARA |

STATION TCAS



HBL

DESIGN

| | | | |
|------------------|------------|---------------------------|--|
| Prepared By | SHREYANSH | Engineer | <small>Shreyansh Shreyansh [Aug 24, 2025 15:02:54 GMT+5:5]</small> |
| Verified By | Y.AKHIL | Sr Engineer | <small>Y.Akhil Y.Akhil [Aug 24, 2025 15:02:54 GMT+5:5]</small> |
| Approved By | MVH.PRASAD | Design Director | <small>MVH.Prasad MVH Prasad [Aug 24, 2025 15:39:29 GMT+5:5]</small> |
| Version | V_2.1.5 | | |
| Proof Consultant | S.CHITRA | Associate General Manager | <small>S.Chitra S.Chitra [Aug 25, 2025 17:26:10 GMT+5:5]</small> |

VERIFIED BY DIVISION

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| SSE/Sig | ADSTE |
| DSTE | Sr.DSTE/N/BRC |

TCAS INTERFACE CIRCUIT

TITLE:
RELAY WIRING DIAGRAM

REF:SIG PLAN NO. प. रे आरेख सं.
IPU_2059/4C WR.DRG.NO.TCAS 2059/4C

शीट SHEET कॉल शीट SHEETS

21 **43**

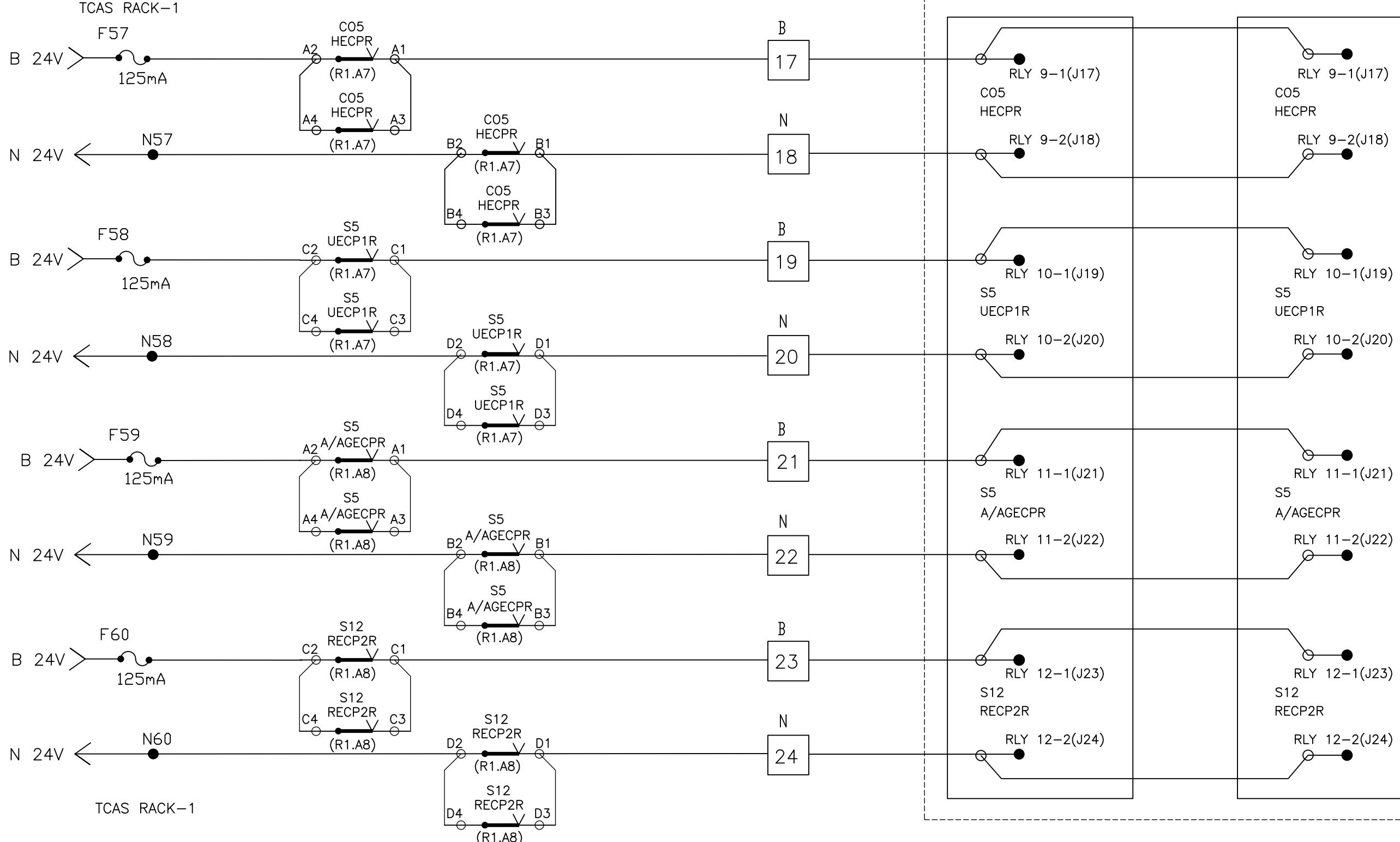
सिग्नल व द्रव संचार

SIGNAL & TELECOMMUNICATIONS

ADASROAD (ADD)

WESTERN RAILWAY
DIVISION: VADODARA

STATION TCAS



HBL

DESIGN

| | | | |
|------------------|------------|---------------------------|--|
| Prepared By | SHREYANSH | Engineer | <small>Shreyansh Shreyansh [Aug 24, 2025 15:02:54 GMT+5:5]</small> |
| Verified By | Y.AKHIL | Sr Engineer | <small>Y.Akhil Y.Akhil [Aug 24, 2025 15:02:54 GMT+5:5]</small> |
| Approved By | MVH.PRASAD | Design Director | <small>MVH.Prasad MVH.Prasad [Aug 24, 2025 15:39:29 GMT+5:5]</small> |
| Version | V_2.1.5 | | |
| Proof Consultant | S.CHITRA | Associate General Manager | <small>S.Chitra S.Chitra [Aug 25, 2025 17:26:10 GMT+5:5]</small> |

VERIFIED BY DIVISION

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|---------|-------|
| SSE/Sig | ADSTE |
| | DSTE |

TCAS INTERFACE CIRCUIT

TITLE:
RELAY WIRING DIAGRAM

REF: SIG PLAN NO. प. रे आरेख सं.
IPU_2059/4C WR.DRG.NO.TCAS 1059/4C

शीट SHEET कुल शीट SHEETS

22 43

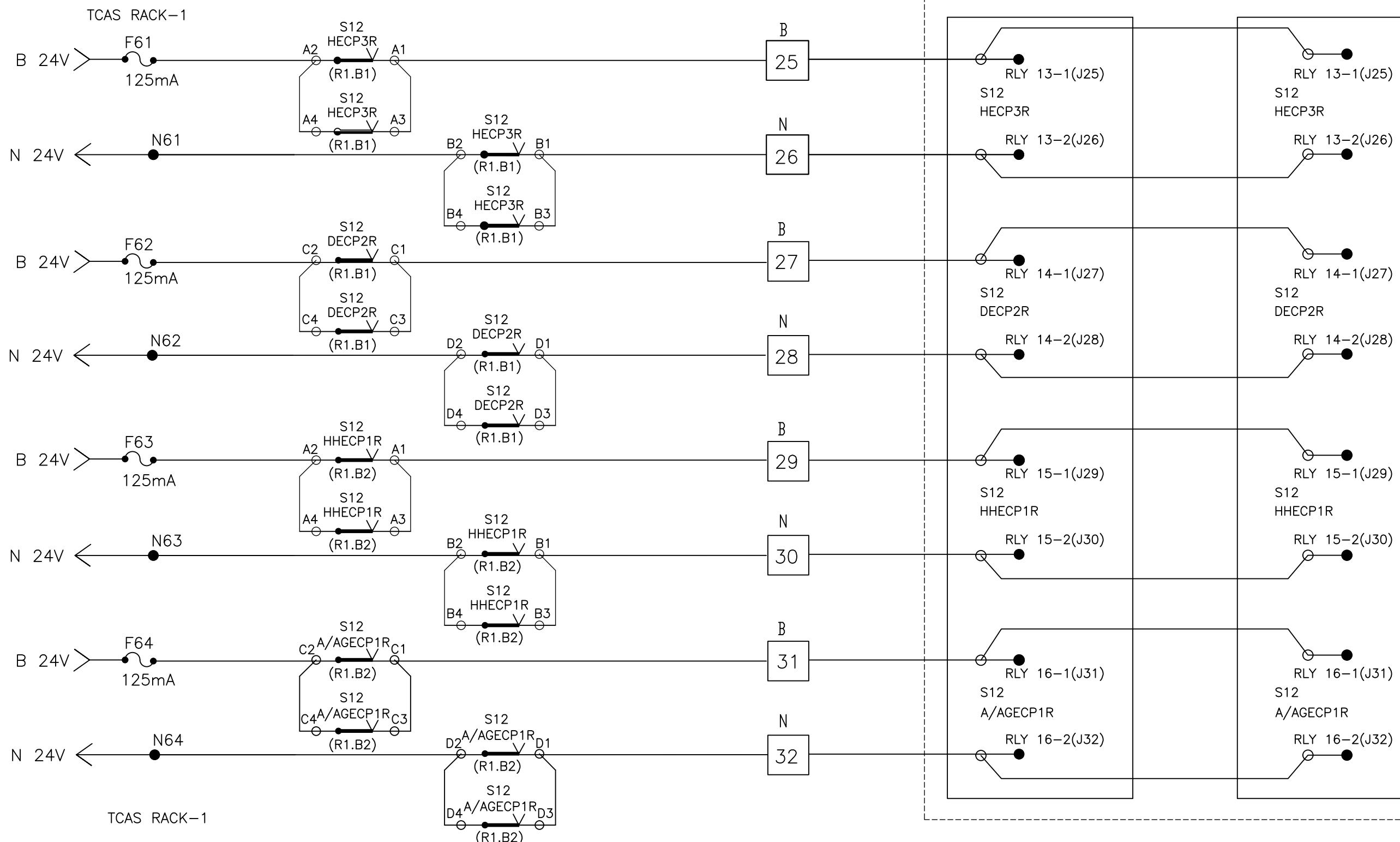
सिग्नल व द्रव संचार

SIGNAL & TELECOMMUNICATIONS

ADASROAD (ADD)

WESTERN RAILWAY
DIVISION: VADODARA

STATION TCAS



HBL

DESIGN

Prepared By SHREYANSH Engineer

Verified By Y.AKHIL Sr Engineer

Approved By MVH.PRASAD Design Director

Version V 2.1.5

Proof Consultant S.CHITRA Associate General Manager

VERIFIED BY DIVISION

SSE/Sig

ADSTE

DSTE

Sr.DSTE/N/BRC

TCAS INTERFACE CIRCUIT

TITLE: RELAY WIRING DIAGRAM

REF: SIG PLAN NO.

P. रे आरेख. सं.

IPU_2059/4C

WR.DRG.NO.TCAS 2059/4C

शीट SHEET

कॉल शीट SHEETS

सिग्नल व द्रव संचार

SIGNAL & TELECOMMUNICATIONS

ADASROAD (ADD)

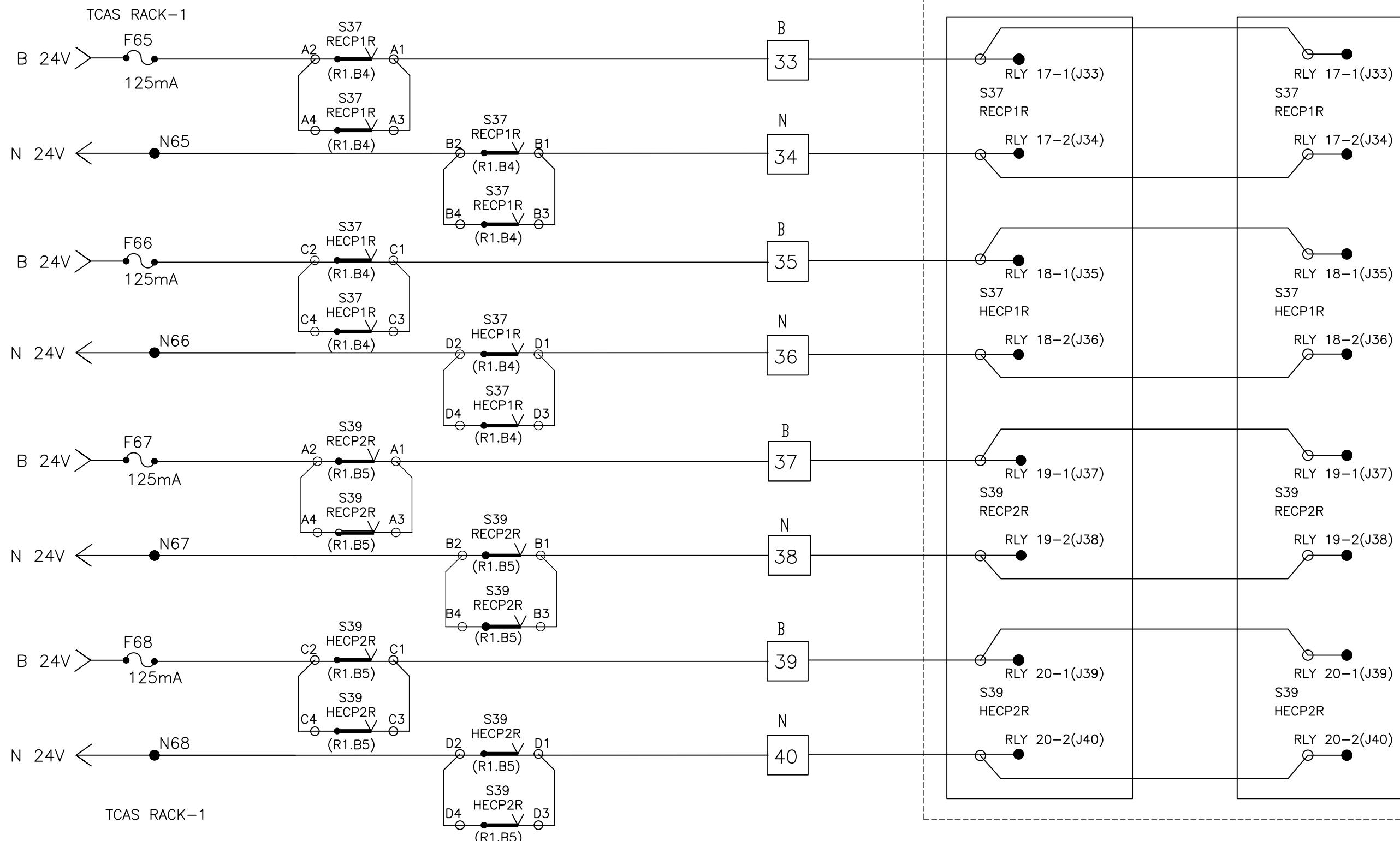
WESTERN RAILWAY

DIVISION: VADODARA

23

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STATION TCAS



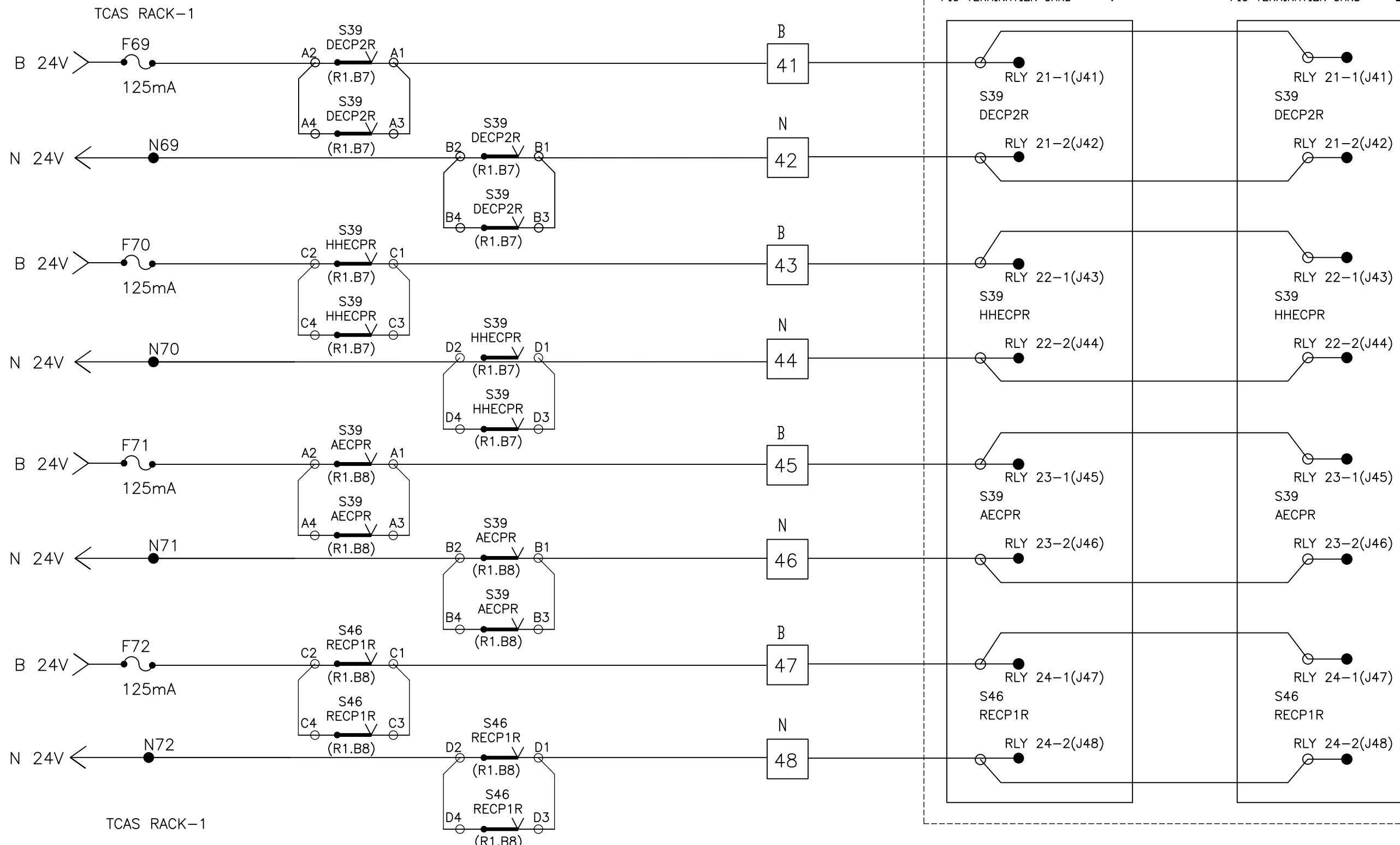
DESIGN

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|------------------|------------|---------------------------|--|
| Prepared By | SHREYANSH | Engineer | <small>Shreyansh Shreyansh [Aug 24, 2025 15:02:54 GMT+5.5]</small> |
| Verified By | Y.AKHIL | Sr Engineer | <small>Y.Akhil Y.Akhil [Aug 24, 2025 15:02:54 GMT+5.5]</small> |
| Approved By | MVH.PRASAD | Design Director | <small>MVH.Prasad MVH Prasad [Aug 24, 2025 15:39:29 GMT+5.5]</small> |
| Version | V_2.1.5 | | |
| Proof Consultant | S.CHITRA | Associate General Manager | <small>S.Chitra S.Chitra [Aug 25, 2025 17:26:10 GMT+5.5]</small> |

VERIFIED BY DIVISION

| TCAS INTERFACE CIRCUIT | | Signal & Telecommunications | |
|---------------------------------------|---------------------------|---------------------------------------|--|
| TITLE: RELAY WIRING DIAGRAM | | SIGNAL & TELECOMMUNICATIONS | |
| REF: SIG PLAN NO. | प. रे आरेख. सं. | | |
| IPU_2059/4C | WR.DRG.NO.TCAS 2059/4C | ADASROAD (ADD) | |
| शीट SHEET | कॉल शीट SHEETS | | |
| 24 | 43 | WESTERN RAILWAY DIVISION: VADODARA | |

STATION TCAS



HBL

DESIGN

Prepared By SHREYANSH Engineer

Shreyansh
Shreyansh [Aug 24, 2025 15:02:54 GMT+5:5]

Verified By Y.AKHIL Sr Engineer

Y.Akhil
Y.Akhil [Aug 24, 2025 15:39:29 GMT+5:5]

Approved By MVH.PRASAD Design Director

MVH.Prasad
MVH Prasad [Aug 24, 2025 15:39:29 GMT+5:5]

Version V 2.1.5

S.CHI

Proof Consultant S.CHI

S.CHI
S.CHI [Aug 25, 2025 17:26:10 GMT+5:5]

VERIFIED BY DIVISION

SSE/Sig

ADSTE

DSTE

Sr.DSTE/N/BRC

TCAS INTERFACE CIRCUIT

TITLE: RELAY WIRING DIAGRAM

REF: SIG PLAN NO. प. रे आरेख. सं.

IPU_2059/4C WR.DRG.NO.TCAS 2059/4C

शीट SHEET कुल शीट SHEETS

25 43

सिग्नल व दूर संचार

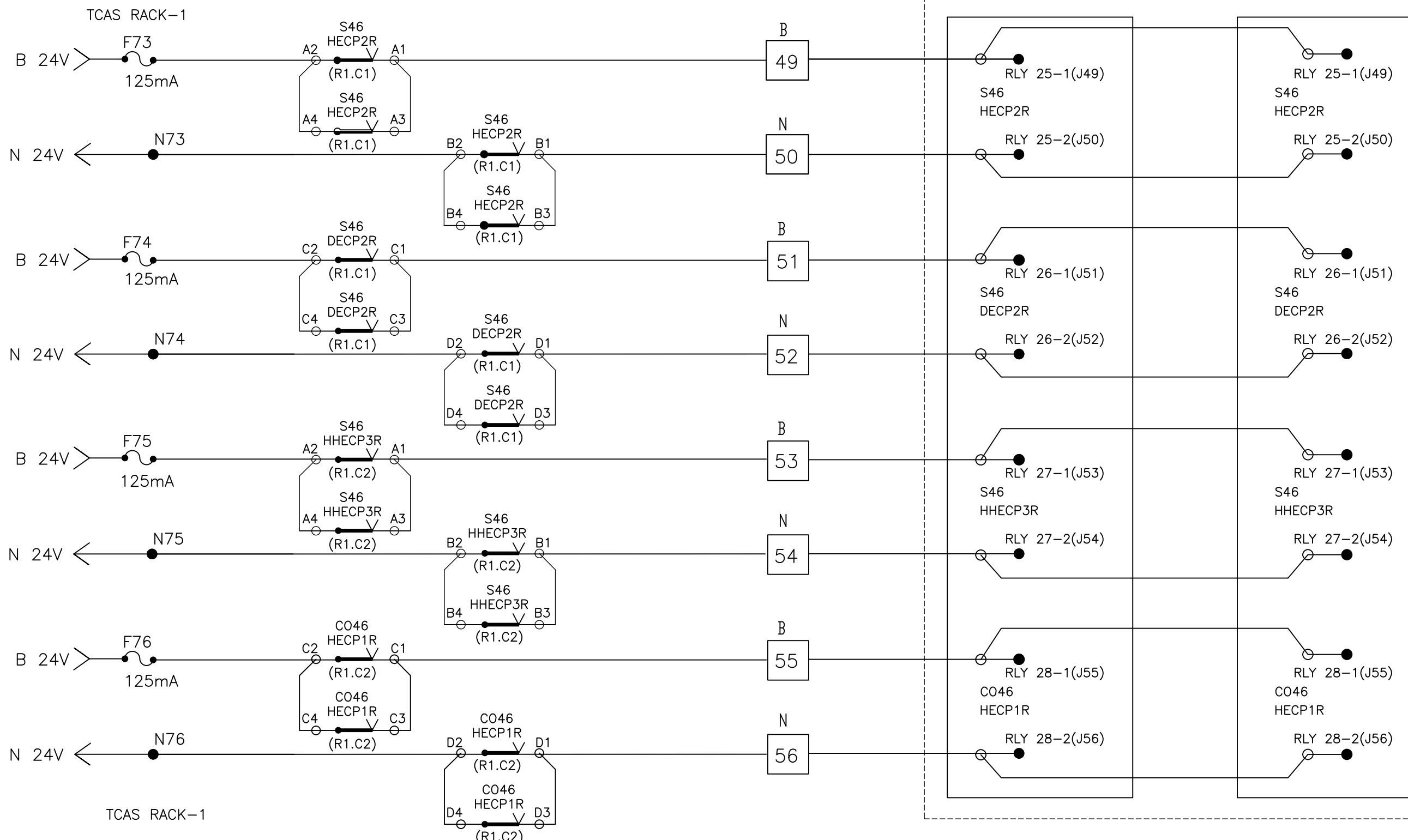
SIGNAL & TELECOMMUNICATIONS

ADASROAD (ADD)

WESTERN RAILWAY

DIVISION: VADODARA

STATION TCAS



HBL

DESIGN

Prepared By SHREYANSH Engineer

Shreyansh
Shreyansh (Aug 24, 2025 15:02:54 GMT+5:5)

Verified By Y.AKHIL Sr Engineer

Y.Akhil

Approved By MVH.PRASAD Design Director

MVH Prasad
MVH Prasad (Aug 24, 2025 15:39:29 GMT+5:5)

Version V 2.1.5

S.Chitra

Proof Consultant S.CHITRA Associate General Manager

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VERIFIED BY DIVISION

SSE/Sig

ADSTE

DSTE

Sr.DSTE/N/BRC

TCAS INTERFACE CIRCUIT

TITLE: RELAY WIRING DIAGRAM

REF: SIG PLAN NO.

प. रे आरेख. सं.

IPU_2059/4C

WR.DRG.NO.TCAS 1059/4C

शीट SHEET

कुल शीट SHEETS

26

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सिग्नल व द्रव संचार

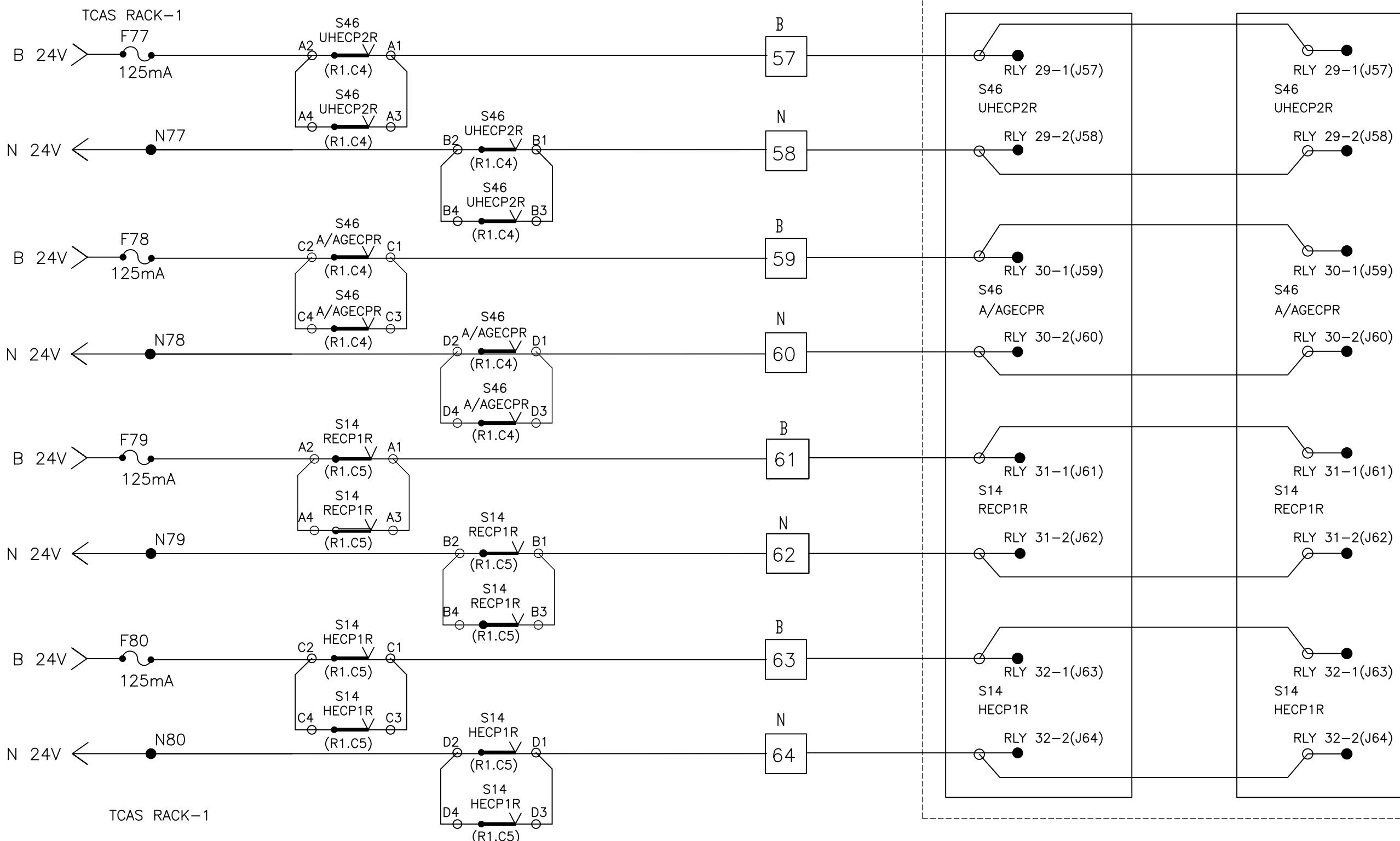
SIGNAL & TELECOMMUNICATIONS

ADASROAD (ADD)

WESTERN RAILWAY

DIVISION: VADODARA

STATION TCAS



HBL

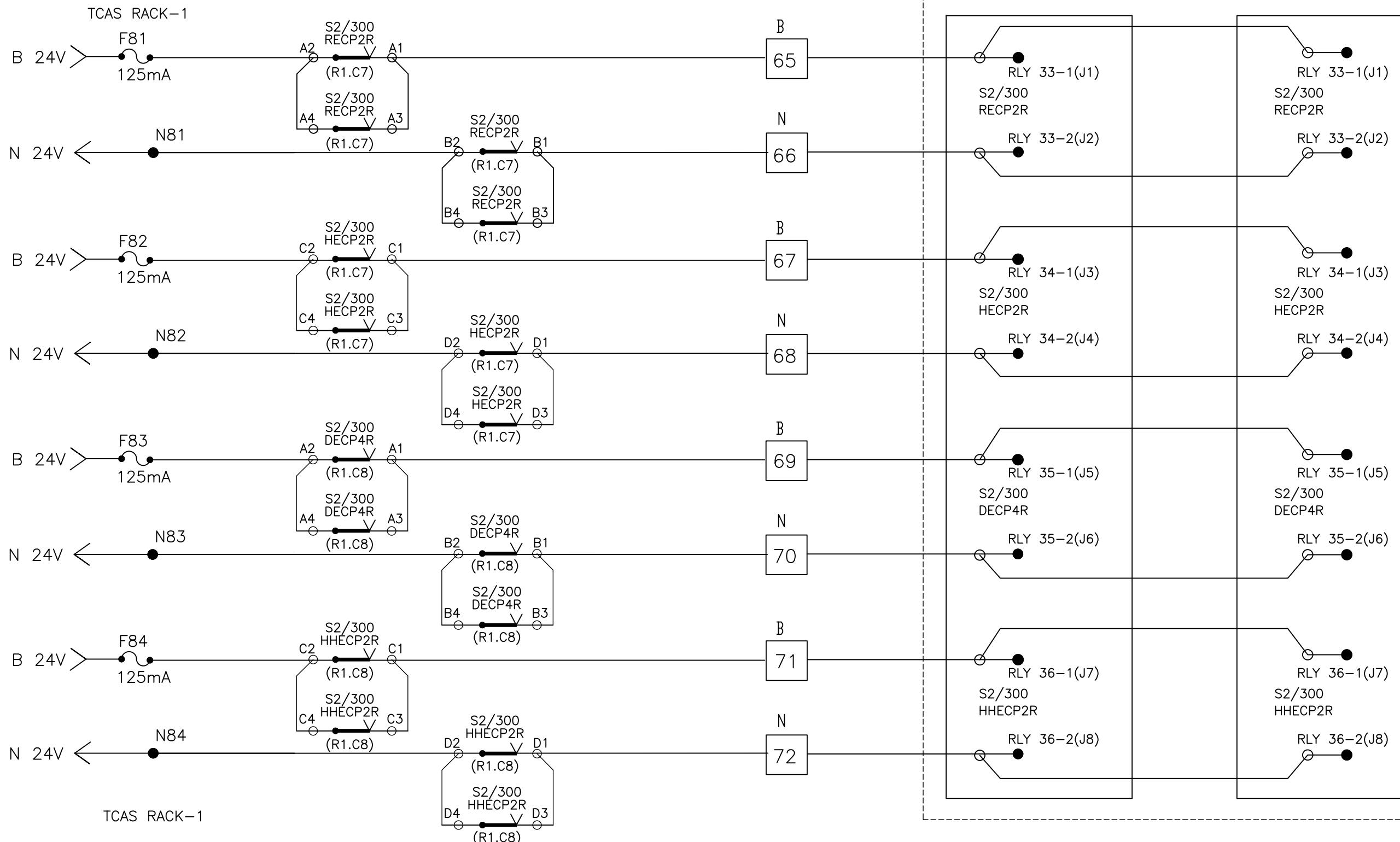
DESIGN

| | | | |
|------------------|------------|---------------------------|--|
| Prepared By | SHREYANSH | Engineer | <small>Shreyansh Shreyansh [Aug 24, 2025 15:02:54 GMT+5:5]</small> |
| Verified By | Y.AKHIL | Sr Engineer | <small>Y.Akhil Y.Akhil [Aug 24, 2025 15:39:29 GMT+5:5]</small> |
| Approved By | MVH.PRASAD | Design Director | <small>MVH.Prasad MVH.Prasad [Aug 24, 2025 15:39:29 GMT+5:5]</small> |
| Version | V_2.1.5 | | |
| Proof Consultant | S.CHITRA | Associate General Manager | <small>S.Chitra S.Chitra [Aug 25, 2025 17:26:10 GMT+5:5]</small> |

VERIFIED BY DIVISION

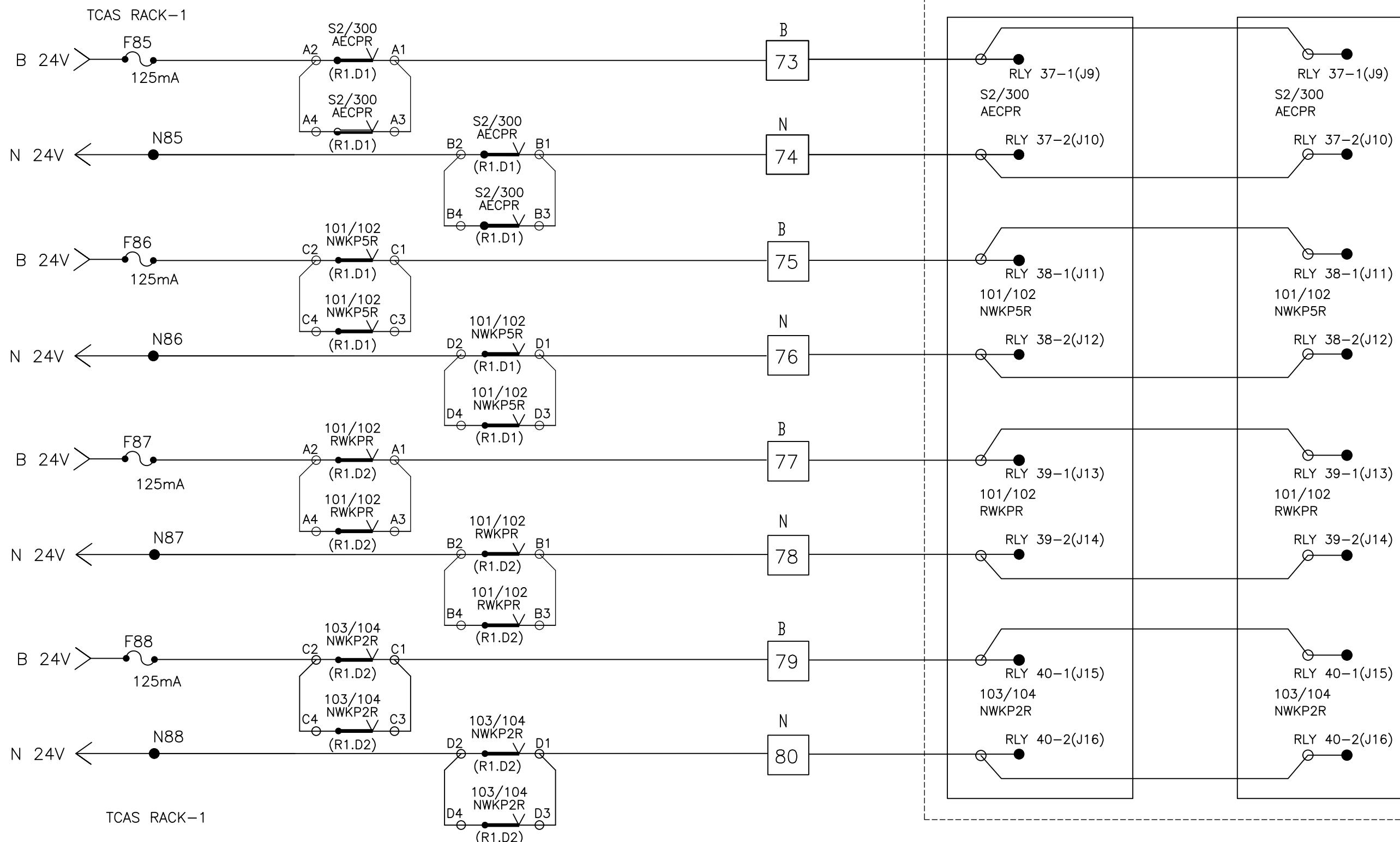
| TCAS INTERFACE CIRCUIT | | Signal & Telecommunications | |
|---------------------------------------|---------------------------|-----------------------------|--|
| TITLE: RELAY WIRING DIAGRAM | | SIGNAL & TELECOMMUNICATIONS | |
| REF: SIG PLAN NO. | P. रे आरेख. सं. | | |
| IPU_2059/4C | WR.DRG.NO.TCAS 1059/4C | | |
| शीट SHEET | कॉल शीट SHEETS | | |
| 27 | 43 | | |
| WESTERN RAILWAY | | DIVISION: VADODARA | |

STATION TCAS



| HBL | DESIGN | | | VERIFIED BY DIVISION | | TCAS INTERFACE CIRCUIT | | Signal & Telecommunications ADASROAD (ADD) |
|------------------|-------------|---------------------------|------------|----------------------|---------------|------------------------|------------------------|---|
| | Prepared By | SHREYANSH | Engineer | SSE/Sig | ADSTE | REF: SIG PLAN NO. | प. रे आरेख. सं. | |
| Verified By | Y.AKHIL | Sr Engineer | Y.Akhil | | | IPU_2059/4C | WR.DRG.NO.TCAS 2059/4C | |
| Approved By | MVH.PRASAD | Design Director | MVH Prasad | | | शीट SHEET | कॉल शीट SHEETS | |
| Version | V_2.1.5 | | | DSTE | | 28 | 43 | WESTERN RAILWAY DIVISION: VADODARA |
| Proof Consultant | S.CHITRA | Associate General Manager | S.Chitra | | Sr.DSTE/N/BRC | | | |

STATION TCAS



HBL

DESIGN

| | | | |
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| Prepared By | SHREYANSH | Engineer | <small>Shreyansh Shreyansh [Aug 24, 2025 15:02:54 GMT+5.5]</small> |
| Verified By | Y.AKHIL | Sr Engineer | <small>Y.Akhil Y.Akhil [Aug 24, 2025 15:39:29 GMT+5.5]</small> |
| Approved By | MVH.PRASAD | Design Director | <small>MVH.Prasad MVH Prasad [Aug 24, 2025 15:39:29 GMT+5.5]</small> |
| Version | V_2.1.5 | | |
| Proof Consultant | S.CHITRA | Associate General Manager | <small>S.Chitra S.Chitra [Aug 25, 2025 17:26:10 GMT+5.5]</small> |

VERIFIED BY DIVISION

| | |
|---------|---------------|
| SSE/Sig | ADSTE |
| DSTE | Sr.DSTE/N/BRC |

TCAS INTERFACE CIRCUIT

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|--------|----------------------|
| TITLE: | RELAY WIRING DIAGRAM |
|--------|----------------------|

| | |
|------------------|------------------------|
| REF:SIG PLAN NO. | प. रे आरेख. सं. |
| IPU_2059/4C | WR.DRG.NO.TCAS 2059/4C |

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| 29 | 43 |

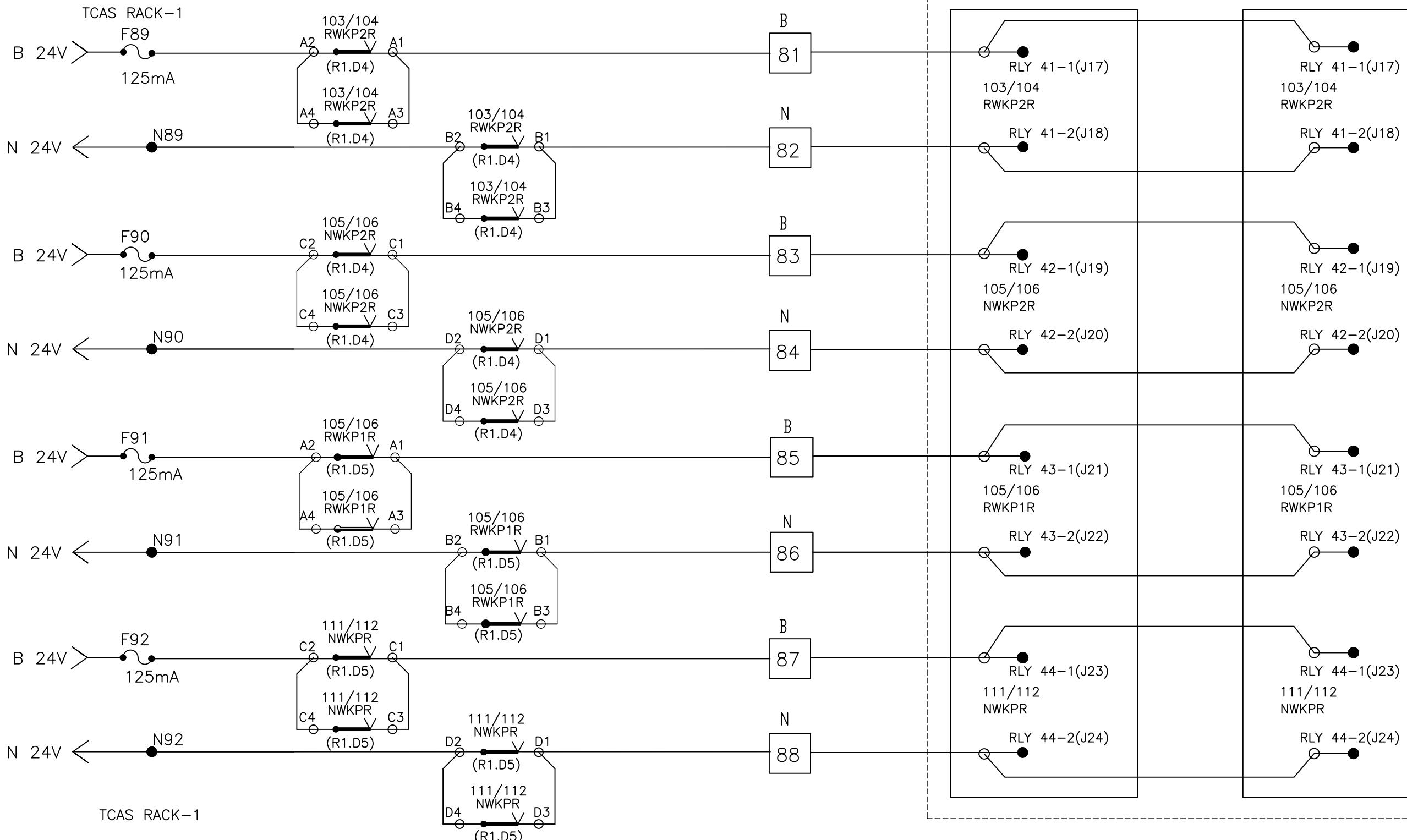
सिग्नल व द्रव संचार

SIGNAL & TELECOMMUNICATIONS

ADASROAD (ADD)

WESTERN RAILWAY
DIVISION: VADODARA

STATION TCAS



HBL

DESIGN

Prepared By SHREYANSH Engineer

Shreyansh
Shreyansh [Aug 24, 2025 15:02:54 GMT+5:5]

Verified By Y.AKHIL Sr Engineer

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Approved By MVH.PRASAD Design Director

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Proof Consultant S.CHI TRA Associate General Manager

S.CHI TRA
S.CHI TRA [Aug 25, 2025 17:26:10 GMT+5:5]

VERIFIED BY DIVISION

SSE/Sig

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Sr.DSTE/N/BRC

TCAS INTERFACE CIRCUIT

TITLE: RELAY WIRING DIAGRAM

REF: SIG PLAN NO. प. रे आरेख. सं.

IPU_2059/4C WR.DRG.NO.TCAS 2059/4C

शीट SHEET कुल शीट SHEETS

30 43

सिग्नल व द्रव संचार

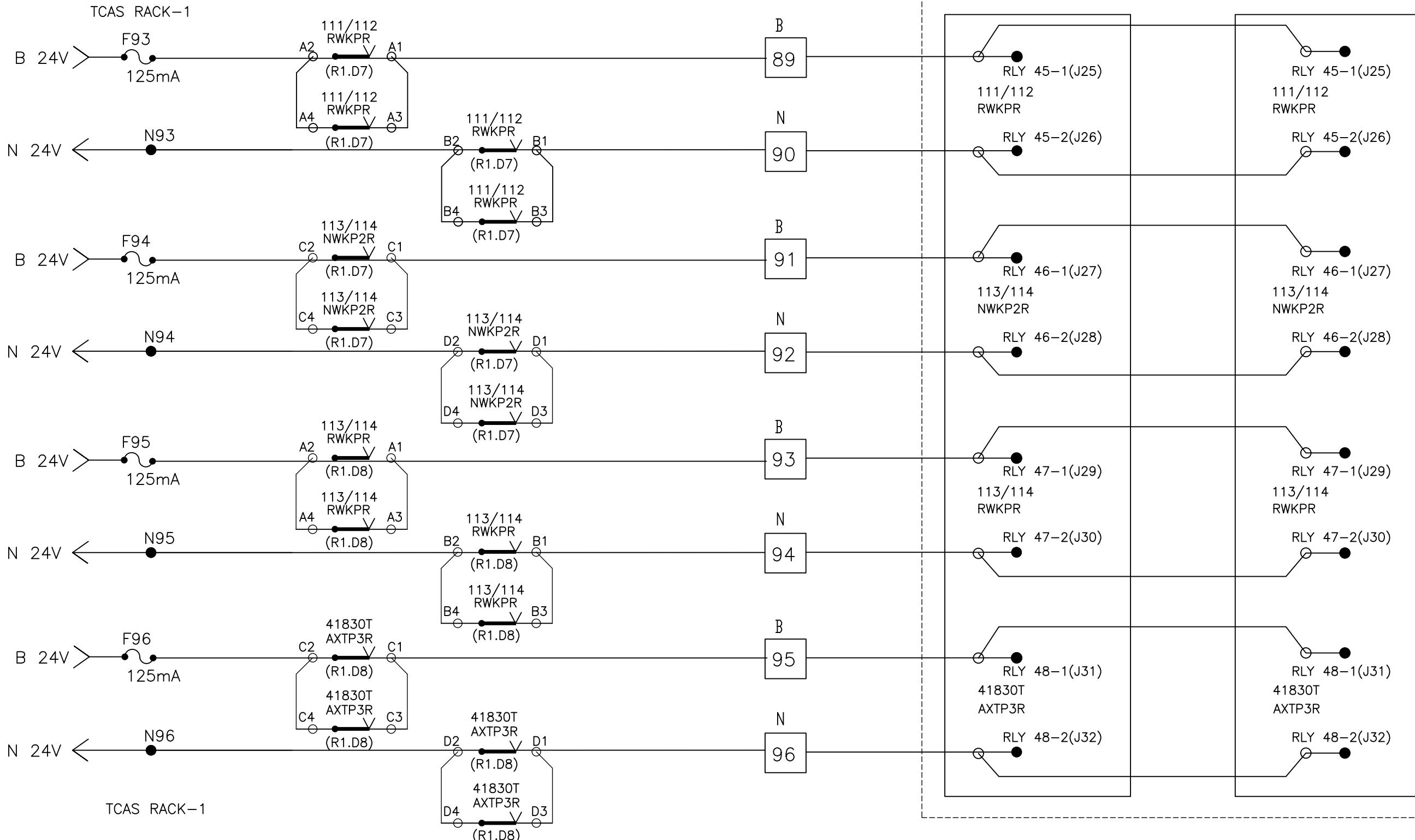
SIGNAL & TELECOMMUNICATIONS

ADASROAD (ADD)

WESTERN RAILWAY

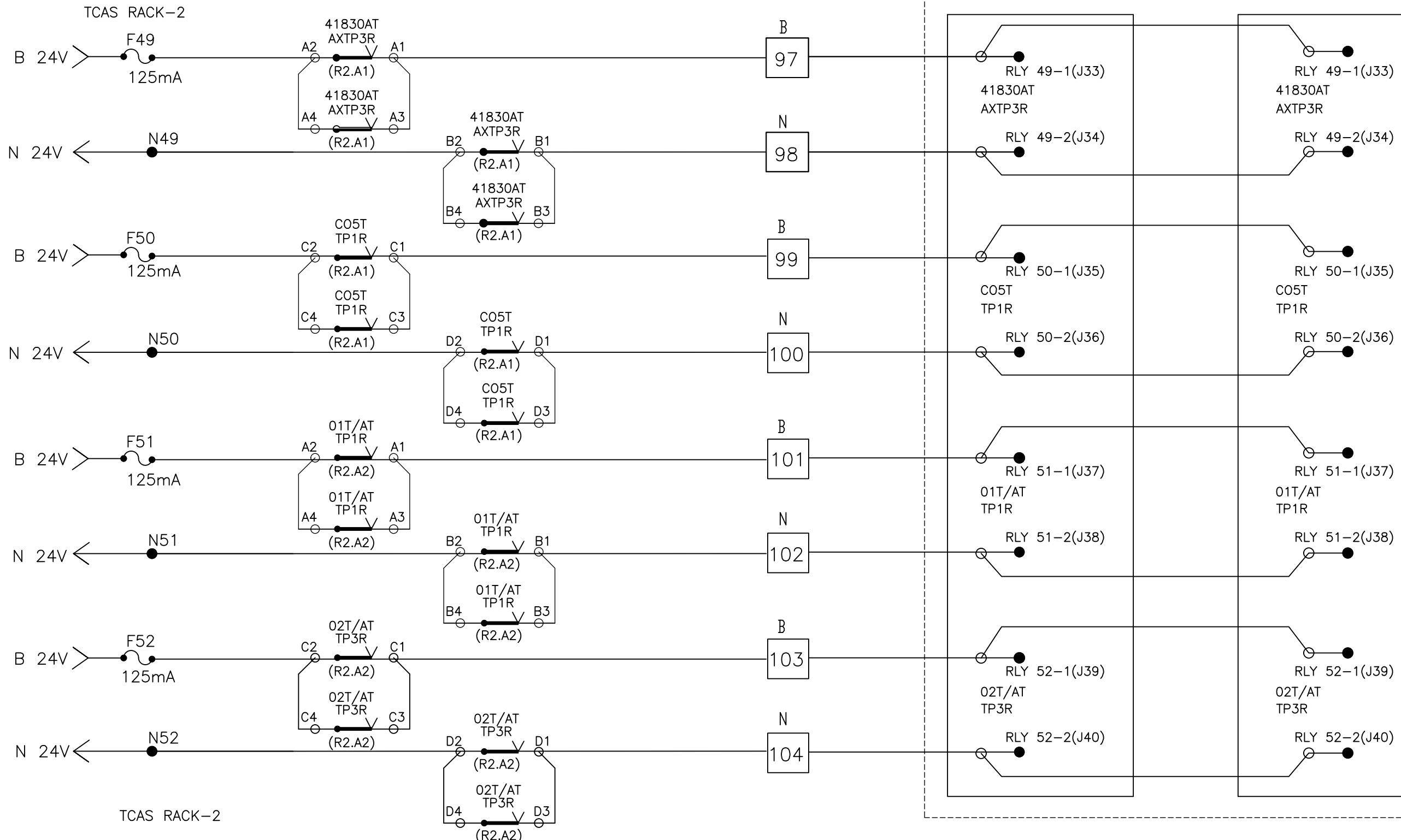
DIVISION: VADODARA

STATION TCAS



| HBL | DESIGN | | | | VERIFIED BY DIVISION | | TCAS INTERFACE CIRCUIT | | SIGNAL & TELECOMMUNICATIONS ADASROAD (ADD) WESTERN RAILWAY DIVISION: VADODARA | |
|-----|------------------|------------|---------------------------|---|----------------------|--|--------------------------------|--|--|--|
| | Prepared By | SHREYANSH | Engineer | <small>Shreyansh (Aug 24, 2025 15:02:54 GMT+5:5)</small> | | | TITLE: RELAY WIRING DIAGRAM | | | |
| | Verified By | Y.AKHIL | Sr Engineer | <small>Y.Akhil</small> | SSE/Sig | | ADSTE | | | |
| | Approved By | MVH.PRASAD | DesignDirector | <small>MVH Prasad (Aug 24, 2025 15:39:29 GMT+5:5)</small> | | | | | | |
| | Version | V | 2.1.5 | | | | | | | |
| | Proof Consultant | S.CHITRA | Associate General Manager | <small>S.CHITRA (Aug 25, 2025 17:26:10 GMT+5:5)</small> | DSTE | | Sr.DSTE/N/BRC | | | |

STATION TCAS



HBL

DESIGN

Prepared By SHREYANSH Engineer

Shreyansh
Shreyansh [Aug 24, 2025 15:02:54 GMT+5.5]

Verified By Y.AKHIL Sr Engineer

Y.Akhil

Approved By MVH.PRASAD Design Director

MVH Prasad
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Version V 2.1.5

S.CHI

Proof Consultant S.CHI

Associate General Manager

S.Chi
S.CHI [Aug 25, 2025 17:26:10 GMT+5.5]

VERIFIED BY DIVISION

SSE/Sig

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DSTE

Sr.DSTE/N/BRC

TCAS INTERFACE CIRCUIT

TITLE:
RELAY WIRING DIAGRAM

REF:SIG PLAN NO. प.रे आरेख सं.
IPU_2059/4C WR.DRG.NO.TCAS
2059/4C

शीट SHEET कुल शीट SHEETS
32 43

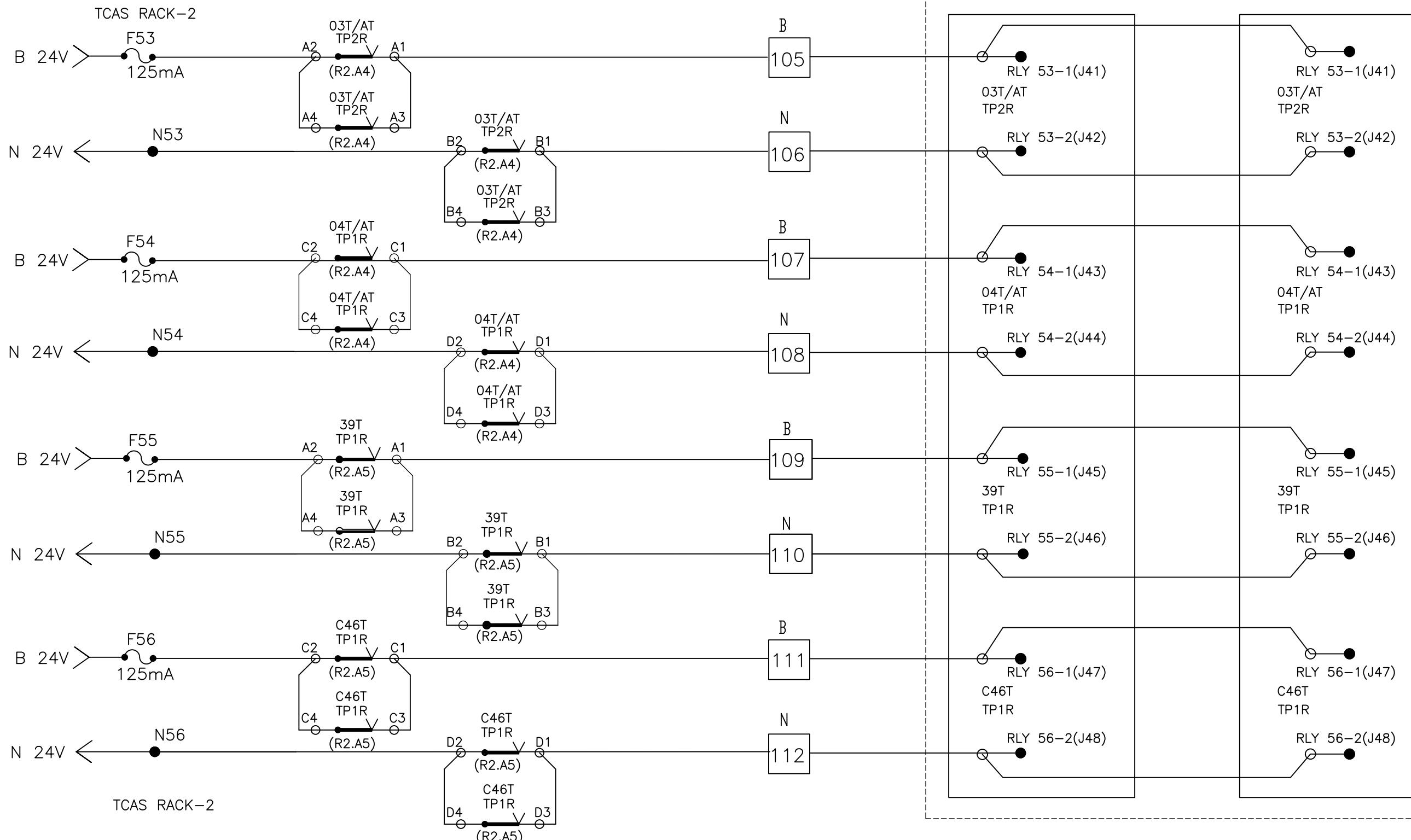
सिग्नल व दूर संचार

SIGNAL & TELECOMMUNICATIONS

ADASROAD (ADD)

WESTERN RAILWAY
DIVISION: VADODARA

STATION TCAS



HBL

DESIGN

Prepared By SHREYANSH Engineer

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Shreyansh [Aug 24, 2025 15:02:54 GMT+5:5]

Verified By Y.AKHIL Sr Engineer

Y.Akhil

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Version V 2.1.5

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TCAS INTERFACE CIRCUIT

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IPU_2059/4C WR.DRG.NO.TCAS 2059/4C

शीट SHEET कॉल शीट SHEETS

33 43

सिग्नल व द्रव संचार

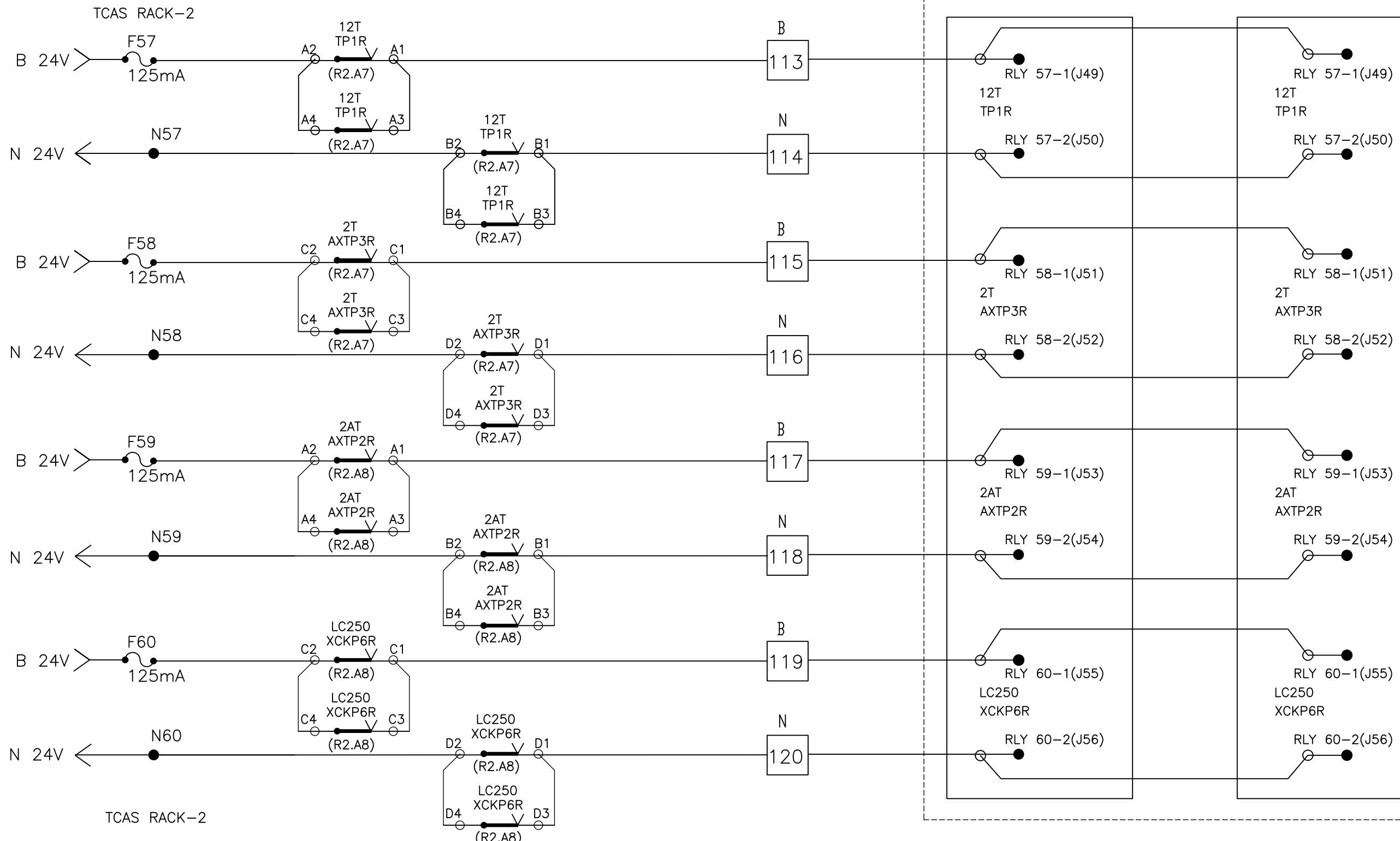
SIGNAL & TELECOMMUNICATIONS

ADASROAD (ADD)

WESTERN RAILWAY

DIVISION: VADODARA

STATION TCAS



HBL

DESIGN

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| Prepared By | SHREYANSH | Engineer | <small>Shreyansh Shreyansh [Aug 24, 2025 15:02:54 GMT+5:5]</small> |
| Verified By | Y.AKHIL | Sr Engineer | <small>Y.Akhil Y.Akhil [Aug 24, 2025 15:39:29 GMT+5:5]</small> |
| Approved By | MVH.PRASAD | Design Director | <small>MVH.Prasad MVH Prasad [Aug 24, 2025 15:39:29 GMT+5:5]</small> |
| Version | V_2.1.5 | | |
| Proof Consultant | S.CHITRA | Associate General Manager | <small>S.Chitra S.Chitra [Aug 25, 2025 17:26:10 GMT+5:5]</small> |

VERIFIED BY DIVISION

| | |
|---------|---------------|
| SSE/Sig | ADSTE |
| DSTE | Sr.DSTE/N/BRC |

TCAS INTERFACE CIRCUIT

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RELAY WIRING DIAGRAM

REF: SIG PLAN NO. प. रे आरेख. सं.
IPU_2059/4C WR.DRG.NO.TCAS 2059/4C

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34 43

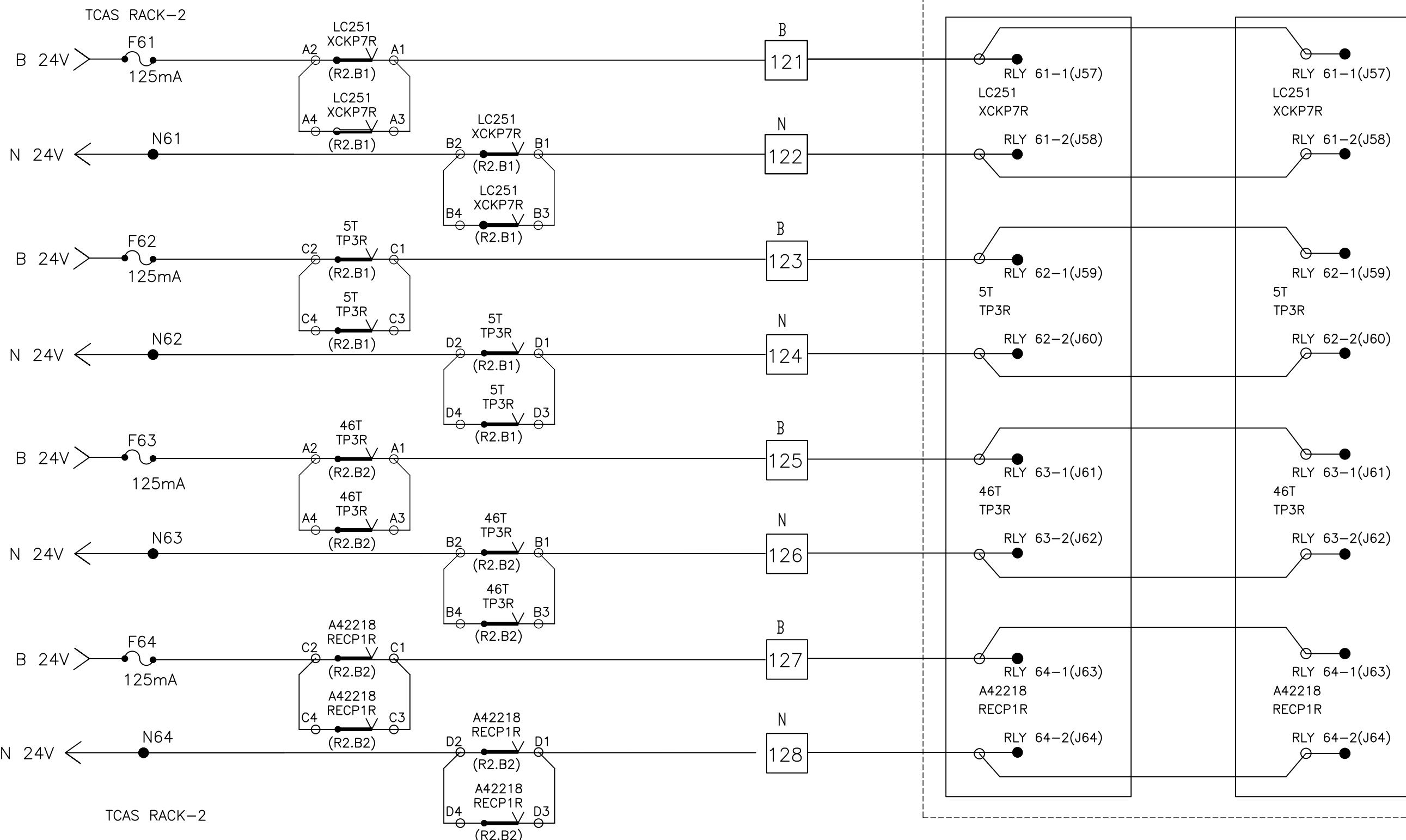
सिग्नल व द्रव संचार

SIGNAL & TELECOMMUNICATIONS

ADASROAD (ADD)

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WR.DRG.NO.TCAS

2059/4C 2059/4C

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35 43

सिग्नल व द्रव संचार

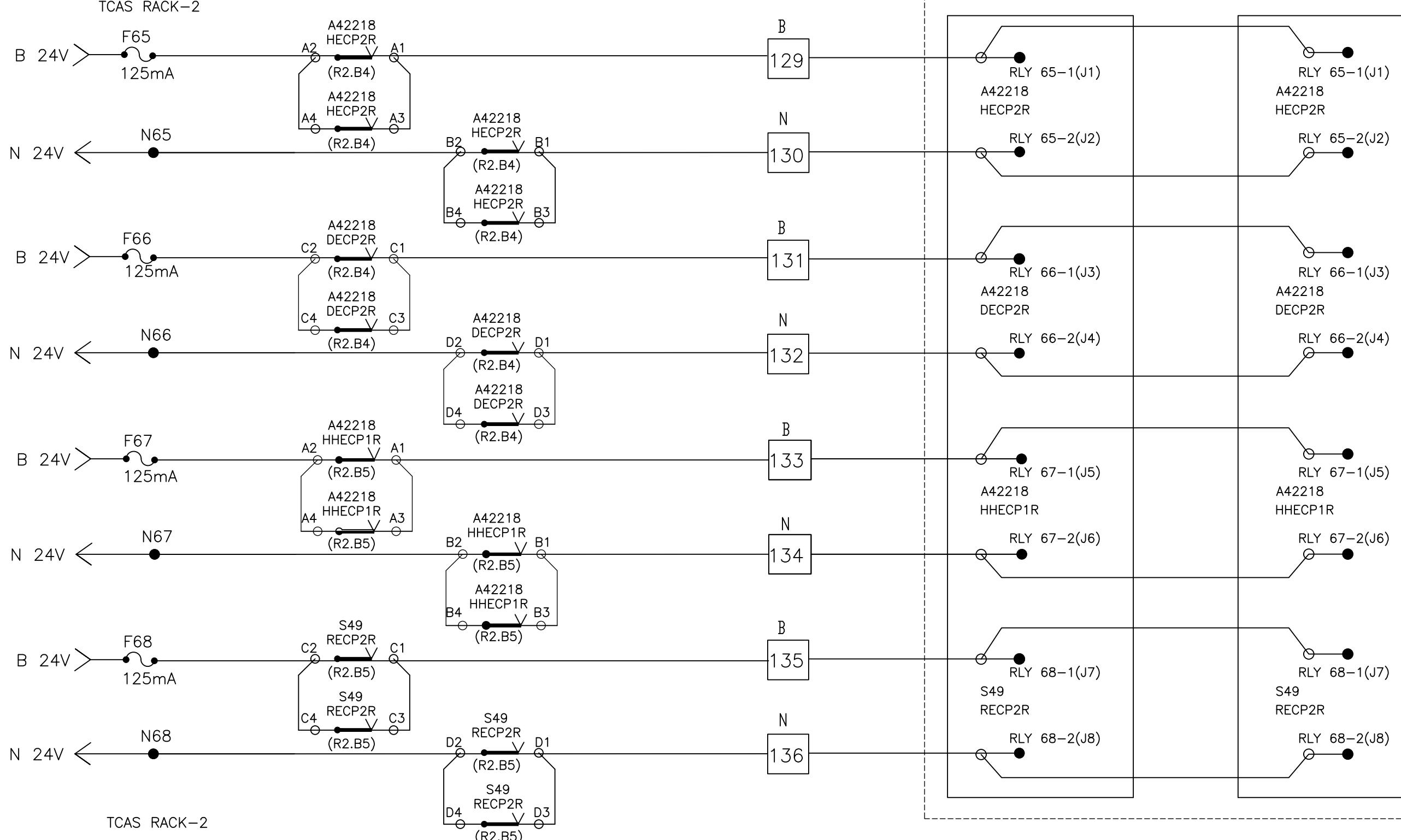
SIGNAL & TELECOMMUNICATIONS

ADASROAD (ADD)

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MVH Prasad
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Proof Consultant S.CHITRA Associate General Manager

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TCAS INTERFACE CIRCUIT

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RELAY WIRING DIAGRAM

REF:SIG PLAN NO.

प. रे आरेख. सं.

IPU_2059/4C

WR.DRG.NO.TCAS 2059/4C

शीट SHEET

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सिग्नल व द्रव संचार

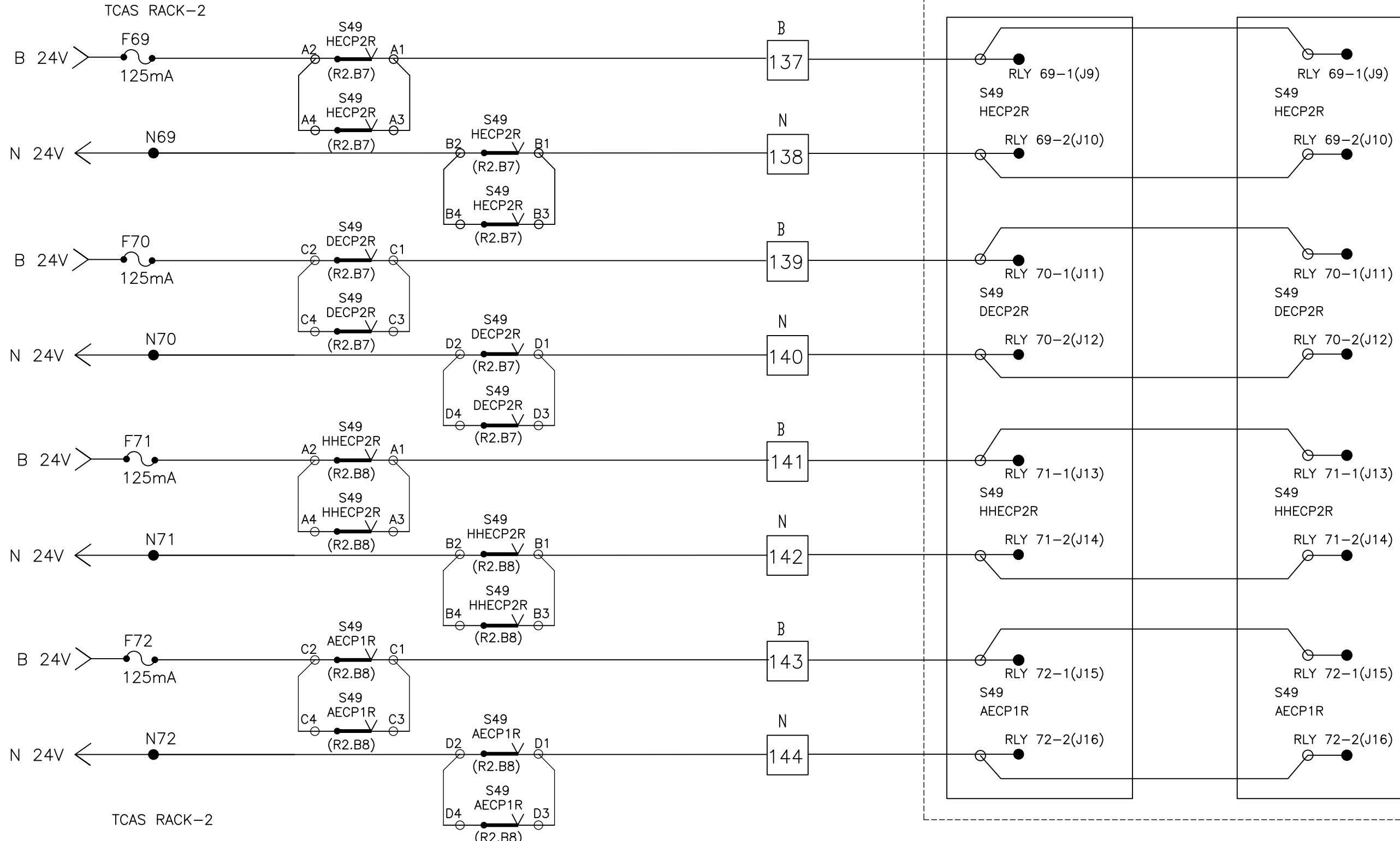
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WESTERN RAILWAY

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STATION TCAS



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IPU_2059/4C WR.DRG.NO.TCAS 2059/4C

शीट SHEET कुल शीट SHEETS

37 43

सिगनल व दूर संचार

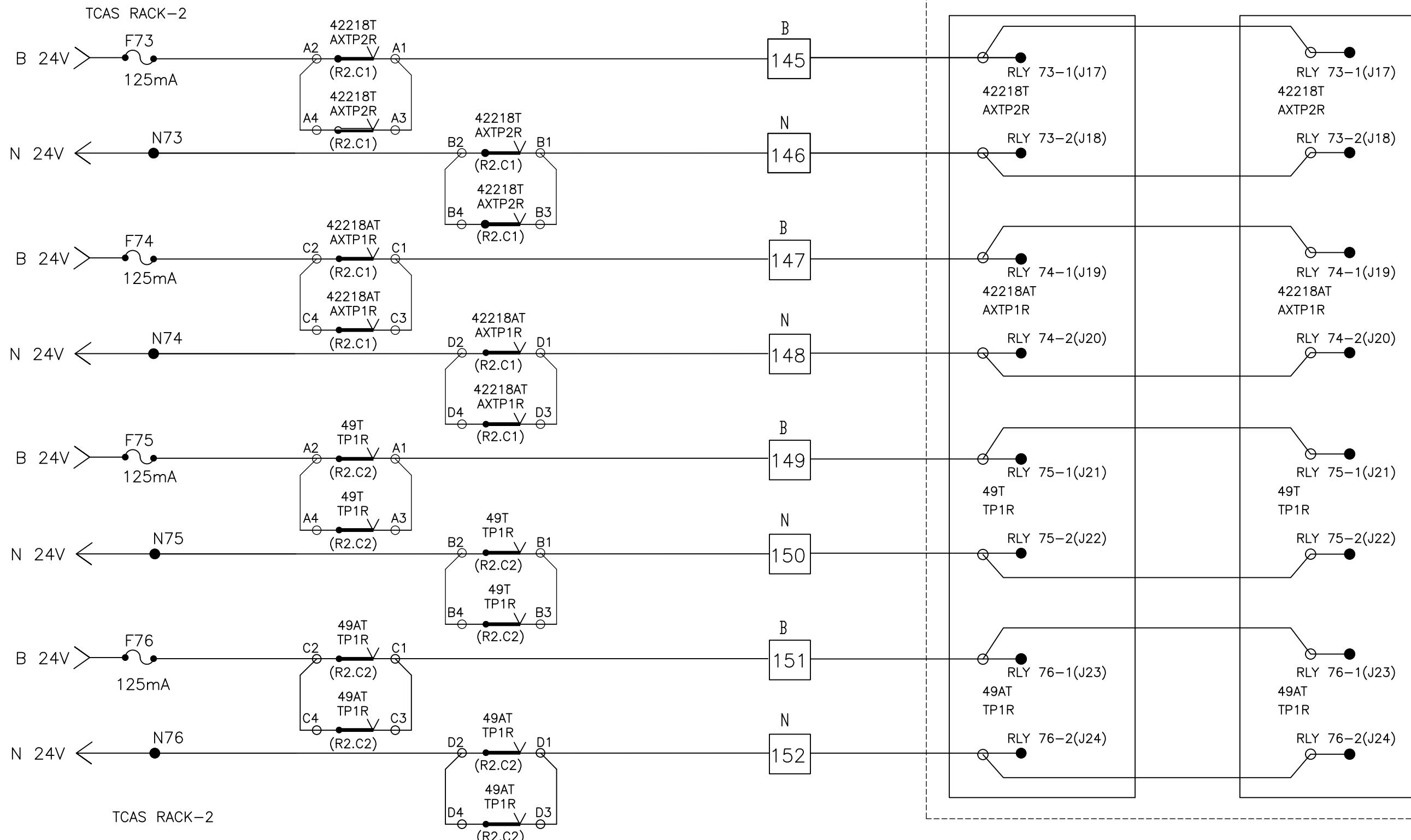
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DIVISION: VADODARA

STATION TCAS



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Version V 2.1.5

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IPU_2059/4C WR.DRG.NO.TCAS 2059/4C

शीट SHEET कुल शीट SHEETS

38 43

सिग्नल व द्रव संचार

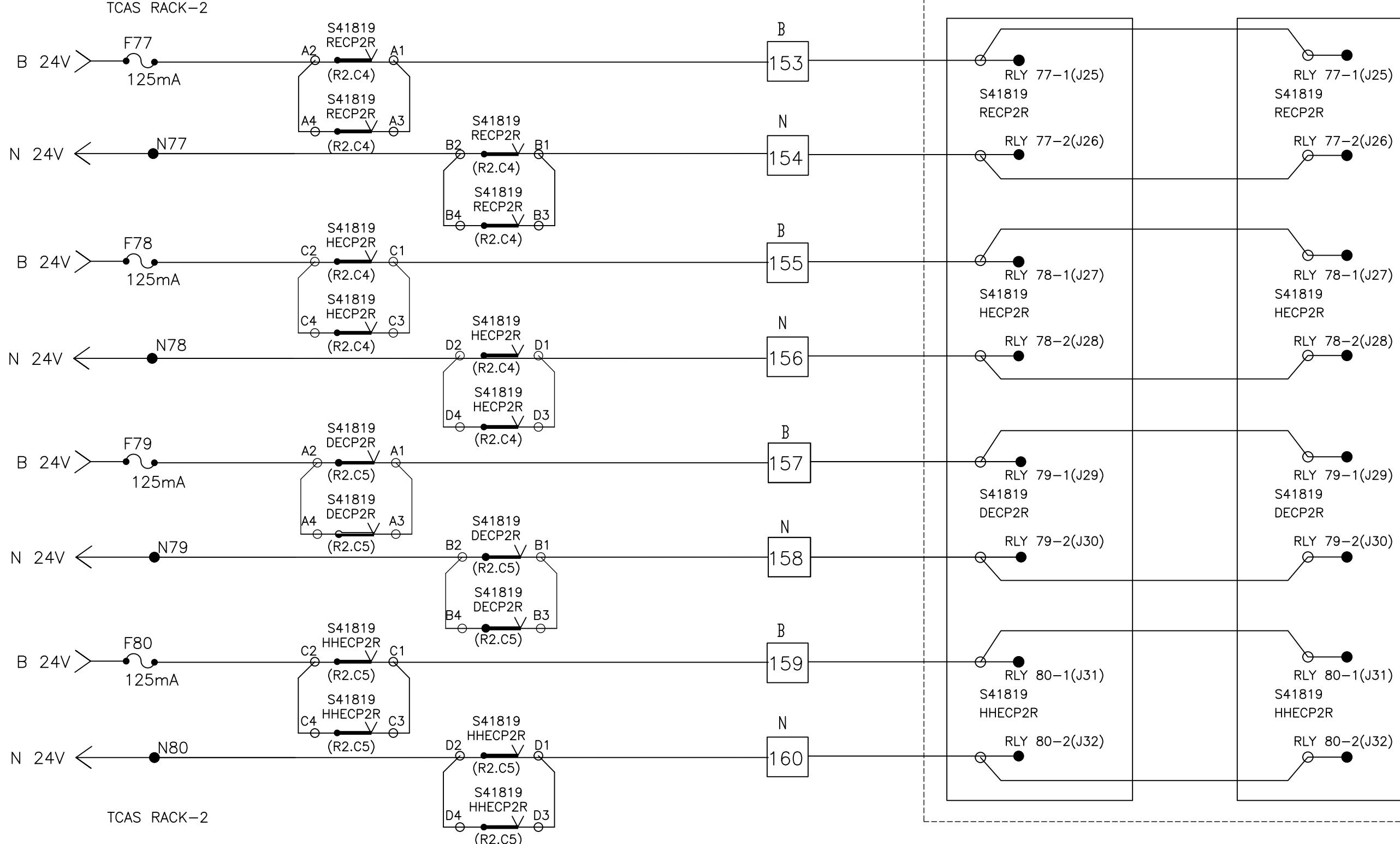
SIGNAL & TELECOMMUNICATIONS

ADASROAD (ADD)

WESTERN RAILWAY

DIVISION: VADODARA

STATION TCAS



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MVH.Prasad
MVH Prasad [Aug 24, 2025 15:39:29 GMT+5:5]

Version V 2.1.5

S.CHITRA

Proof Consultant S.CHITRA Associate General Manager

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S.CHITRA [Aug 25, 2025 17:26:10 GMT+5:5]

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TCAS INTERFACE CIRCUIT

TITLE: RELAY WIRING DIAGRAM

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IPU_2059/4C WR.DRG.NO.TCAS 2059/4C

शीट SHEET कुल शीट SHEETS

39 43

सिगनल व द्रव संचार

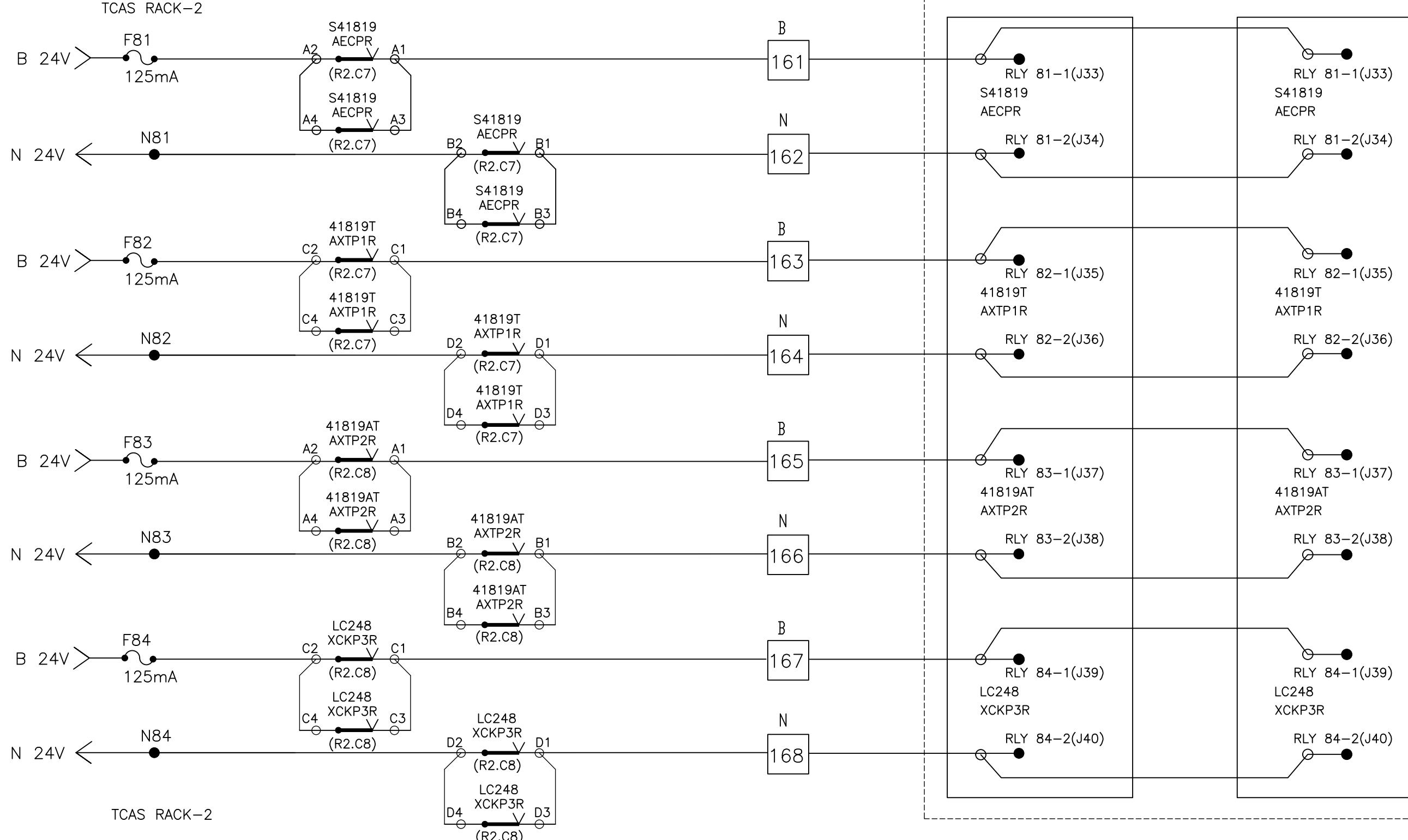
SIGNAL & TELECOMMUNICATIONS

ADASROAD (ADD)

WESTERN RAILWAY

DIVISION: VADODARA

STATION TCAS



HBL

DESIGN

Prepared By SHREYANSH Engineer

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Approved By MVH.PRASAD Design Director

Version V 2.1.5

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TCAS INTERFACE CIRCUIT

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IPU_2059/4C

WR.DRG.NO.TCAS 2059/4C

शीट SHEET

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सिग्नल व द्रव संचार

SIGNAL & TELECOMMUNICATIONS

ADASROAD (ADD)

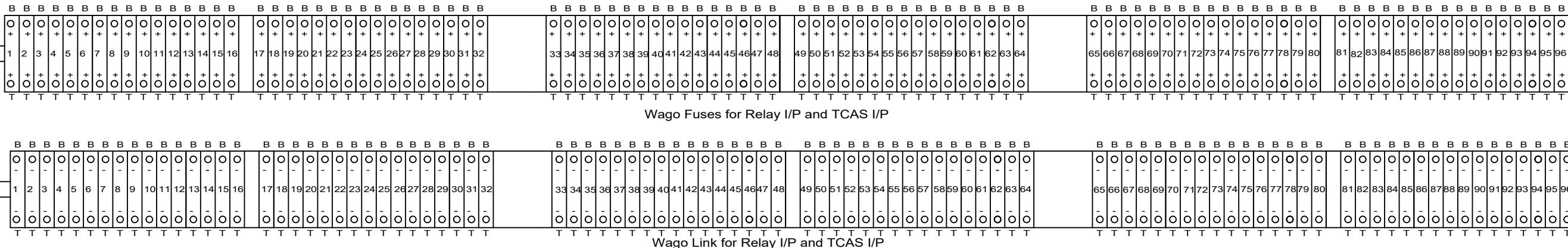
WESTERN RAILWAY

DIVISION: VADODARA

40

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TCAS RELAY RACK-1 LAYOUT IN RELAY ROOM



1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32

Wago Fuses for Relay I/P and TCAS I/P

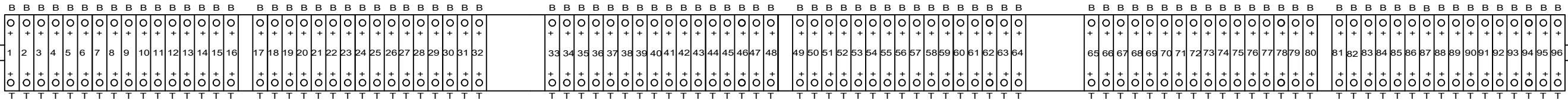
Wago Link for Relay I/P and TCAS I/P

| | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 |
|---|-------------------|------------------|-------------------|-------------------|-------------------|-------------------|-------------------|-------------------|
| A | A41830 HECP2R | A41830 RECP1R | A41830 HHECP2R | A41830 DECP4R | S5 HECP2R | S5 RECP2R | S5 HHECP2R | S5 DECP2R |
| B | S12 DECP2R | S12 HECP3R | S12 A/AGECP1R | S12 HHECP1R | S37 HECP1R | S37 RECP1R | S39 HECP2R | S39 RECP2R |
| C | S46 DECP2R | S46 HECP2R | C046 HECP1R | S46 HHECP3R | S46 A/AGECP2R | S46 UHECP2R | S14 HECP1R | S14 RECP1R |
| D | 101/102 NWKP5R | S2/300 AECPR | 103/104 NWKP2R | 101/102 RWKP2R | 105/106 NWKP2R | 103/104 RWKP2R | 111/112 NWKP2R | 105/106 RWKP1R |

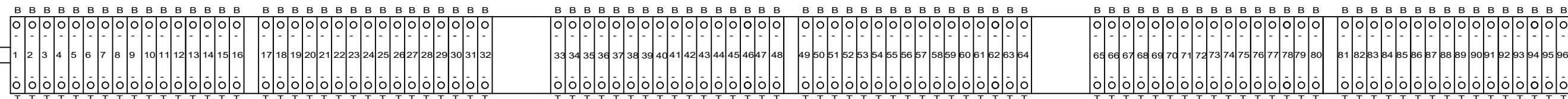
NOTE: ALL THE ABOVE RELAYS ARE QNN1 6F/2B x 2 TYPE

| HBL | DESIGN | | | | VERIFIED BY DIVISION | | | | TCAS INTERFACE CIRCUIT | | સિગનલ વાદ્ય સંચાર SIGNAL & TELECOMMUNICATIONS ADASROAD (ADD) | | |
|------------------|-----------------------------|---------------------------|-----------------|---|----------------------|--|--|--|---|--|--|--|--|
| | TITLE: RELAY RACK LAYOUT | | | | | | | | | | | | |
| | Prepared By | SHREYANSH | Engineer | <i>Shreyansh</i> Shreyansh (Aug 24, 2025 15:02:54 GMT+5.5) | SSE/Sig | | | | REF: SIG PLAN NO. P. રે આરોખ. સં. IPU_2059/4C WR.DRG.NO.TCAS 2059/4C | | | | |
| | Verified By | Y.AKHIL | Sr Engineer | <i>Y.Akhil</i> Y.Akhil (Aug 24, 2025 15:39:29 GMT+5.5) | ADSTE | | | | શીટ SHEET કુલ શીટ SHEETS | | | | |
| | Approved By | MVH.PRASAD | Design Director | <i>MVH Prasad</i> MVH Prasad (Aug 24, 2025 15:39:29 GMT+5.5) | DSTE | | | | 41 43 | | | | |
| | Version | V_2.1.5 | | | Sr.DSTE/N/BRC | | | | DIVISION: VADODARA | | | | |
| Proof Consultant | S.CHITRA | Associate General Manager | | <i>S.Chitra</i> S.CHITRA (Aug 25, 2025 17:26:10 GMT+5.5) | DSTE | | | | WESTERN RAILWAY | | | | |

TCAS RELAY RACK-2 LAYOUT IN RELAY ROOM



Wago Fuses for Relay I/P and TCAS I/P



Wago Link for Relay I/P and TCAS I/P

NOTE: ALL THE ABOVE RELAYS ARE QNN1 6F/2B x 2 TYPE

| HBL® | DESIGN | | | | VERIFIED BY DIVISION | | TCAS INTERFACE CIRCUIT | | SIGNAL & TELECOMMUNICATIONS ADASROAD (ADD) WESTERN RAILWAY DIVISION: VADODARA | |
|------|------------------|------------|---------------------------|---|----------------------|---------------|-----------------------------|---------------------------|--|--|
| | Prepared By | SHREYANSH | Engineer | <small>Shreyansh (Aug 24, 2025 15:02:54 GMT+5.5)</small> | | | TITLE: RELAY RACK LAYOUT | | | |
| | Verified By | Y.AKHIL | Sr Engineer | <small>Y.Akhil</small> | | | REF:SIG PLAN NO. | प. रे आरेख सं. | | |
| | Approved By | MVH.PRASAD | Design Director | <small>MVH Prasad (Aug 24, 2025 15:39:29 GMT+5.5)</small> | | | IPU. 2059/4C | WR.DRG.NO.TCAS 2059/4C | | |
| | Version | V | 2.1.5 | | | शीट SHEET | कल शीट SHEETS | | | |
| | Proof Consultant | S.CHITRA | Associate General Manager | <small>S.CHITRA (Aug 25, 2025 17:26:10 GMT+5.5)</small> | DSTE | Sr.DSTE/N/BRC | 42 | 43 | | |

REVISION HISTORY SHEET

Station Name/Location:- ADASROAD (ADD)

| SL.No | Date | Version | Reason for Change | Remarks |
|-------|------------|---------|---|---------|
| 1 | 10.10.2023 | 2.0.0 | Initial Version | |
| 2 | 28.02.2024 | 2.0.1 | 5T and 46T tracks are added for updating to V4.0 | |
| 3 | 01.08.2024 | 2.1.2 | Updated Relay wiring as per latest SIP 2059/4C 1.LC-253 is removed on site and the signals A42218 & S42120/304 which were connected to LC-253 are now connected to ADAS ROAD. 2.Added Tracks relays 42218 AXT /AXT2,42218A AXT1/AXT2,42120 AXT1/AXT2 & 42120A AXT1/AXT2 3.S42120/304 and S41921/300 and their respective tracks nomenclature changed S49 and S2/300 respectively. 4.Block Section tracks updated to MSDAC 5.Added Point relays of point P201KL/202KL | |
| 4 | 21.10.2024 | 2.1.3 | 1. Updated as per Bell test copy 2. Removed point relays of point P201KL/202KL | |
| 5 | 25.12.2024 | 2.1.4 | 1. Due to removal LC-248 AH, the following relays are added: 1. S41819 RECP1R 2. S41819 HECP1R 3. S41819 DECP1R 4. S41819 HHECP1R 5. S41819 AECP1R 6. 41819T AXTP1R 7. 41819AT AXTP1R 8. LC248 XCKP2R 2. Updated as per comments from SSE/ND | |
| 6 | 20.08.2025 | 2.1.5 | Fuse are bifurcated as one fuse per coil | |

| HBL  | DESIGN | | | | VERIFIED BY DIVISION | | TCAS INTERFACE CIRCUIT | | सिग्नल व द्रव संचार SIGNAL & TELECOMMUNICATIONS | | | |
|--|------------------|------------|---------------------------|--|----------------------|---------------|----------------------------|---------------------------|--|--|--|--|
| | Prepared By | SHREYANSH | Engineer | <small>Shreyansh Shreyansh [Aug 24, 2025 15:02:54 GMT+5:5]</small> | | | TITLE: REVISION HISTORY | | | | | |
| | Verified By | Y.AKHIL | Sr Engineer | <small>Y.Akhil Y.Akhil [Aug 24, 2025 15:02:54 GMT+5:5]</small> | SSE/Sig | ADSTE | REF:SIG PLAN NO. | प. रे आरेख. सं. | | | | |
| | Approved By | MVH.PRASAD | Design Director | <small>MVH Prasad MVH Prasad [Aug 24, 2025 15:39:29 GMT+5:5]</small> | | | IPU. 2059/4C | WR.DRG.NO.TCAS 2059/4C | | | | |
| | Version | V_ | 2.1.5 | | DSTE | Sr.DSTE/N/BRC | शीट SHEET | कुल शीट SHEETS | | | | |
| | Proof Consultant | S.CHITRA | Associate General Manager | <small>S.Chitra S.Chitra [Aug 25, 2025 17:26:10 GMT+5:5]</small> | | | 43 | 43 | | | | |

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