

Shift-Reduce Parsers

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- 2 a data structure for detecting handles (a stack happens to be adequate)
- 3 a data structure for storing and accessing the lhs and rhs of rules.

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- Reduce:** When a handle appears on the stack, it is popped and replaced by the left hand side of the corresponding production.
- Accept:** When the stack contains only the start symbol and input buffer is empty, the parser halts announcing a *successful* parse.
- Error:** When the parser can neither shift nor reduce nor accept. Halts announcing an error.

Properties of shift-reduce parsers

Is the following situation possible?

- $\alpha \beta \gamma$ is the stack contents and $A \rightarrow \gamma$ is the handle.
- The stack contents reduces to $\alpha \beta A$
- Now $B \rightarrow \beta$ is the next handle.

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Assume that this is true. Then, by the definition of a handle, there is a sequence of rightmost derivations:

$$S \xRightarrow{*rm} \alpha B A x y z \xRightarrow{rm} \alpha \beta A x y z \xRightarrow{rm} \alpha \beta \gamma x y z$$

But in the right sentential form $\alpha B A x y z$, B is not the rightmost non-terminal, and thus \xRightarrow{rm} is not a rightmost derivation. Therefore the above scenario is not possible.

Properties of shift-reduce parsers

So what scenarios are possible after a reduction?

$$\alpha\beta\gamma xyz$$

Properties of shift-reduce parsers

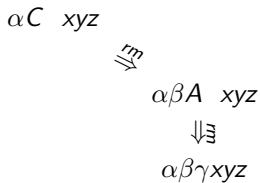
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$$\alpha\beta A \quad xyz$$
$$\Downarrow \exists$$
$$\alpha\beta\gamma xyz$$

Production used is $A \rightarrow \gamma$

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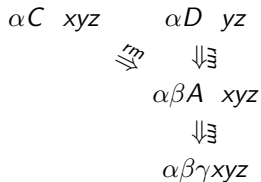
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Production used is $C \rightarrow \beta A$

Properties of shift-reduce parsers

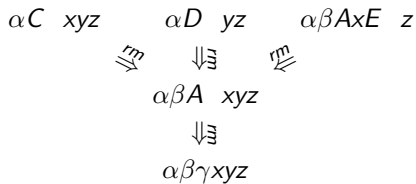
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Production used is $D \rightarrow \beta A x$

Properties of shift-reduce parsers

So what scenarios are possible after a reduction?



Production used is $E \rightarrow y$

Example of Shift-Reduce Parsing

Ambiguous grammar of expressions

Conflicts in a Shift-Reduce Parser

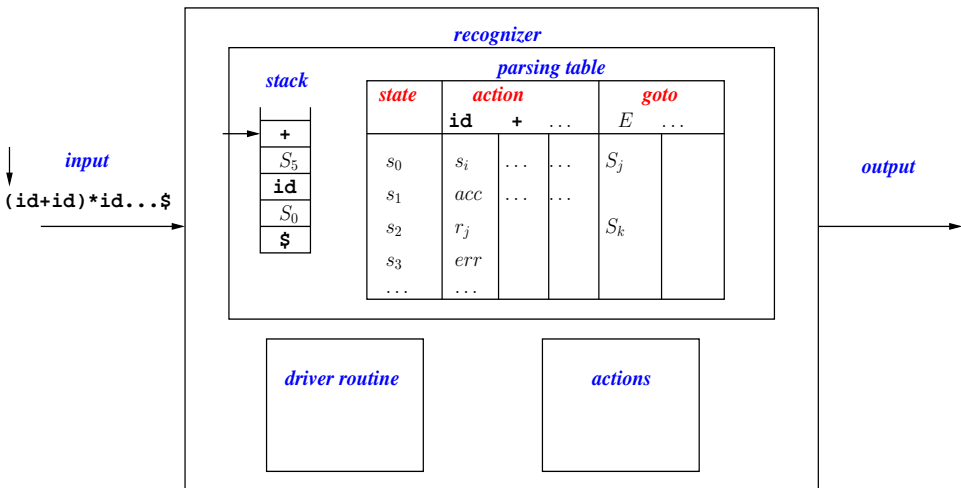
For some grammars, the shift-reduce parser may get into the following conflicting situations.

- *Shift-reduce conflict* A handle β occurs at *tos*; the *nexttoken* a is such that $\beta a \gamma$ happens to be another handle. The parser has two options
 - reduce the handle using $A \rightarrow \beta$
 - ignore the handle; shift a and continue parsing and eventually reduce using some rule $B \rightarrow \beta a \gamma$.
- *Reduce-reduce conflict* the stack contents are $\alpha \beta \gamma$ and both $\beta \gamma$ and γ are handles with $A \rightarrow \beta \gamma$ and $B \rightarrow \gamma$ as the corresponding rules. Then the parser has two reduce possibilities.

To handle such conflicts, the *nexttoken* could be used to prefer one move over the other.

- choose shift (or reduce) in a shift-reduce conflict
- prefer one reduce (over others) in a reduce-reduce conflict.

LR Parser Model



LR Parsers

Consist of

- a stack which contains strings of the form $s_0X_1s_1X_2\ldots X_ms_m$, where X_i is a grammar symbol and s_i is a special symbol called a *state*.
- a parsing table which comprises two parts, usually named as *Action* and *Goto*.

The entries in the Action part are:

- s_i which means shift to state i
- r_j which stands for reduce by the j^{th} rule,
- *accept*
- *error*

The Goto part contains blank entries or state symbols.

The Driver Routine

- Initializes stack with *start* state. Calls scanner to get next token.
- Consults the parsing table and performs the action specified there.
- Parsing continues till either an error or accept entry is encountered.

<i>top of stack</i>	<i>nexttoken</i>	<i>action</i>	<i>parsing action</i>
state j	a	si	push a ; push state i
	a	rj	$rj : A \rightarrow \alpha$; $length(\alpha) = r$; pop $2r$ symbols from stack; top of stack contains state k ; $goto[k, a] = cl$; push A ; push state l ;
state j	$\$$	acc	successful parse; halt
state j	a	err	error handling

SLR(1) Parser

1. $E \rightarrow E + T$
2. $E \rightarrow T$
3. $T \rightarrow T * F$
4. $T \rightarrow F$
5. $F \rightarrow (E)$
6. $F \rightarrow \text{id}$

state	action						goto		
	id	+	*	()	\$	E	T	F
0	s5			s4			c1	c2	c3
1		s6				acc			
2		r2	s7		r2	r2			
3		r4	r4		r4	r4			
4	s5			s4			c8	c2	c3
5		r6	r6		r6	r6			
6	s5			s4				c9	c3
7	s5			s4					c10
8		s6			s11				
9		r1	s7		r1	r1			
10		r3	r3		r3	r3			
11		r5	r5		r5	r5			