Lexical Analysis

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January 2024



Topic: Scanning

Section:

Introduction

Specifying Scanners

Tokenizing the Input

Constructing DFAs

Representing DFA

Minimizing DFA

Outline

- Introduction
- Specifying scanners
- Tokenizing input using DFAs
- Constructing DFAs
- Representing DFAs using four-arrays
- Minimizing DFAs



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Prof. Sanyal's slides (scanning-slides-sanyal-part1.pdf)



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Prof. Sanyal's slides (scanning-slides-sanyal-part2.pdf)



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An Example for Scanning: Specifications

Let L and D denote the set of all letters and digits, respectively

Pattern	Token
int	INT
L(L D)*	ID
D^+	NUM
	=
;	;

We will scan the input string int int32=5;←



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Example for Scanning: DFA for the Patterns

Formally, a Deterministic Finite Automaton (DFA) is a five tuple

$$(\Sigma, S, s_0, \delta, F)$$

where

- \bullet Σ is the input alphabet
- *S* is the set of states
- $s_o \in S$ is a unique start state
- $\delta: S \times \Sigma \to S$ is a transition function
- $F \subseteq S$ is a set of final states



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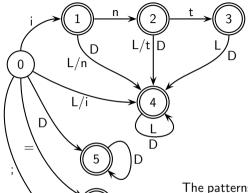
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Example for Scanning: DFA for the Patterns



States	Action
3	Found INT
1, 2, 4	Found ID
5	Found NUM
6	Found =
7	Found:

The patterns for INT precedes the pattern for ID Hence although state 3 could accept both INT

and ID, it is made to accept only INT



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A Format to Show A Trace of Scanning

Step No	State	${\sf MatchedString}$	Buffer	NextChar	LastFinalState	${\sf MarkedPos}$	Action

- State (S). Current State
- MatchedString (MS). Prefix of the buffer matched to identify a lexeme
- Buffer.
- NextChar (NC). The next character in the input; it will be shifted to the buffer if there is a valid transition in the DFA
- LastFinalState (LFS). The last final state seen
- MarkedPos (MP). The position of the character (in the buffer) just after the last seen lexeme
- Action.



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A Format to Show A Trace of Scanning

Step No State MatchedString Buffer NextChar LastFinalState MarkedPos Action

- State (S). Current State
- M When there is no transition on Nextchar,
- if MarkedPos is -1, no final state is seen, the first character in the buffer is discarded, and the second character becomes
 NextChar.
 - otherwise, the lexeme upto MarkedPos (excluding it) is returned, the character at MarkedPos becomes NextChar
 - In either case, the LastFinalState is set to -1 and the state is set to 0
- Al-Hon

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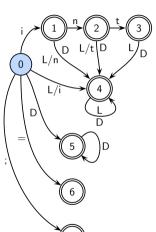
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SNo	S	MS	Buffer	NC	LFS	MP	Action
1	0		int _ int32=5;←	i			



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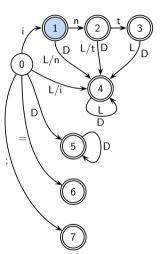
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SNo	S	MS	Buffer	NC	LFS	MP	Action
1	0		int_int32=5;←	i			
2	1	i	int_int32=5;←	n	1	1	



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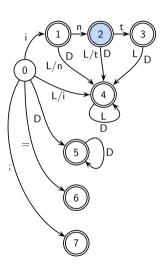
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SNo	S	MS	Buffer	NC	LFS	MP	Action
1	0		int_int32=5;←	i			
2	1	i	int∟int32=5;←	n	1	1	
3	2	in	int_int32=5;←	t	2	2	



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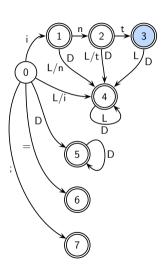
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SNo	S	MS	Buffer	NC	LFS	MP	Action
1	0		int_int32=5;←	i			
2	1	i	int∟int32=5;←	n	1	1	
3	2	in	int_int32=5;←	t	2	2	
4	3	int	int_int32=5;←	1	3	3	Found INT



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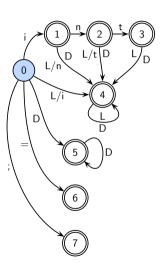
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SNo	S	MS	Buffer	NC	LFS	MP	Action
1	0		int_int32=5;←	i			
2	1	i	int∟int32=5;←	n	1	1	
3	2	in	int_int32=5;←	t	2	2	
4	3	int	int_int32=5;←	1	3	3	Found INT
5	0		ـint32=5;←	1			Discard 🗅



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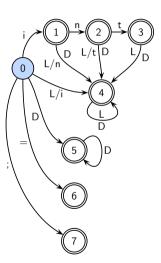
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SNo	S	MS	Buffer	NC	LFS	MP	Action
1	0		int_int32=5;←	i			
2	1	i	int∟int32=5;←	n	1	1	
3	2	in	int_int32=5;←		2	2	
4	3	int	int∟int32=5;←	1	3	3	Found INT
5	0		∟int32=5;←	1			Discard 🗕
6	0		int32=5;←	i	,		



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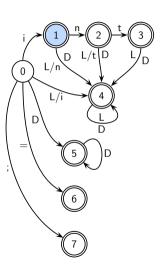
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SNo	S	MS	Buffer	NC	LFS	MP	Action
1	0		int_int32=5;←	i			
2	1	i	int∟int32=5;←	n	1	1	
3	2	in	int_int32=5;←	t	2	2	
4	3	int	int_int32=5;←		3	3	Found INT
5	0		ـint32=5;←	1			Discard 🗕
6	0		int32=5;←	i			
7	1	i	int32=5;←	n	1	1	



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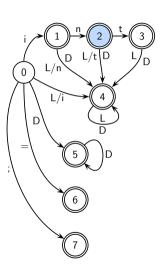
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SNo	S	MS	Buffer	NC	LFS	MP	Action
1	0		int_int32=5;←	i			
2	1	i	int∟int32=5;←	n	1	1	
3	2	in	int_int32=5;←	t	2	2	
4	3	int	int_int32=5;←	1	3	3	Found INT
5	0		ـint32=5;←	1			Discard 🗅
6	0		int32=5;←	-			
7	1	i	int32=5;←	n	1	1	
8	2	in	int32=5;←	t	2	2	



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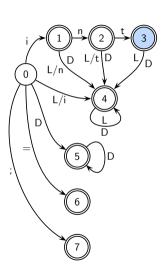
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SNo	S	MS	Buffer	NC	LFS	MP	Action
1	0		int_int32=5;←	i			
2	1	i	int∟int32=5;←	n	1	1	
3	2	in	int_int32=5;←	t	2	2	
4	3	int	int_int32=5;←	1	3	3	Found INT
5	0		ـint32=5;←	1			Discard 🗅
6	0		int32=5;←				
7	1	i	int32=5;←	n	1	1	
8	2	in	int32=5;←	t	2	2	
9	3	int	int32=5;←	3	3	3	



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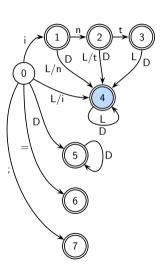
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I	SNo	S	MS	Buffer	NC	LFS	MP	Action
١	1	0		int_int32=5;←	i			
ı	2	1	i	int∟int32=5;←	n	1	1	
ı	3	2	in	int_int32=5;←		2	2	
ı	4	3	int	int_int32=5;←	1	3	3	Found INT
ı	5	0		ـint32=5;←	1			Discard 🗅
	6	0		int32=5;←	i			
ı	7	1	i	int32=5;←	n	1	1	
	8	2	in	int32=5;←	t	2	2	
ı	9	3	int	int32=5;←	3	3	3	
	10	4	int3	int32=5;←	2	4	4	



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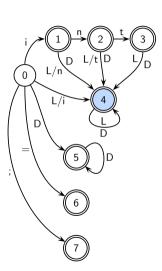
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SNo	S	MS	Buffer	NC	LFS	MP	Action
1	0		int_int32=5;←	i			
2	1	i	int∟int32=5;←	n	1	1	
3	2	in	int_int32=5;←	t	2	2	
4	3	int	int∟int32=5;←	1	3	3	Found INT
5	0		∟int32=5;←	1			Discard 🗅
6	0		int32=5;←	i			
7	1	i	int32=5;←	n	1	1	
8	2	in	int32=5;←	t	2	2	
9	3	int	int32=5;←	3	3	3	
10		int3	int32=5;←	2	4	4	
11	4	int32	int32=5;←	=	4	5	Found ID



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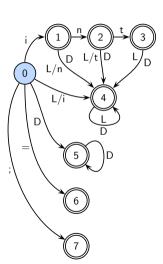
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SNo	S	MS	Buffer	NC	LFS	MP	Action
1	0		int_int32=5;←	i			
2	1	i	int∟int32=5;←	n	1	1	
3	2	in	int_int32=5;←	t	2	2	
4	3	int	int_int32=5;←	1	3	3	Found INT
5	0		ـint32=5;←	1			Discard 🗅
6	0		int32=5;←	-			
7	1	i	int32=5;←	n	1	1	
8	2	in	int32=5;←	t	2	2	
9	3	int	int32=5;←	3	3	3	
10	4	int3	int32=5;←	2	4	4	
11	4	int32	int32=5;←		4	5	Found ID
12	0		=5;←	=			



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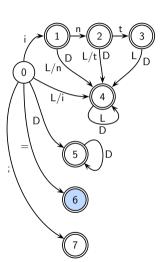
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SNo	S	MS	Buffer	NC	LFS	MP	Action
1	0		int _ int32=5;←	i			
2	1	i	int∟int32=5;←	n	1	1	
3	2	in	int_int32=5;←	t	2	2	
4	3	int	int∟int32=5;←	1	3	3	Found INT
5	0		∟int32=5;←	1			Discard 🗕
6	0		int32=5;←	-			
7	1	i	int32=5;←	n	1	1	
8	2	in	int32=5;←	t	2	2	
9	3	int	int32=5;←	3	3	3	
10	4		int32=5;←	2	4	4	
11	4	int32	int32=5;←	=	4	5	Found ID
12	0		=5;←	=			
13	6	=	=5;←	5	6	1	Found =



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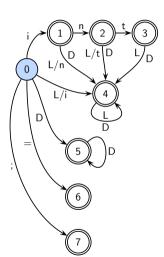
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SNo	S	MS	Buffer	NC	LFS	MP	Action
1	0		int_int32=5;←	i			
2	1	i	int∟int32=5;←	n	1	1	
3	2	in	int_int32=5;←	t	2	2	
4	3	int	int_int32=5;←	1	3	3	Found INT
5	0		ـint32=5;←	1			Discard _
6	0		int32=5;←	i			
7	1	i	int32=5;←	n	1	1	
8	2	in	int32=5;←	t	2	2	
9	3	int	int32=5;←	3	3	3	
10	4	int3	int32=5;←	2	4	4	
11	4	int32	int32=5;←	=	4	5	Found ID
12	0		=5;←	=			
13	6	=	=5;←	5	6	1	Found =
14	0		5;←	5			
•	•	-	•	-			•



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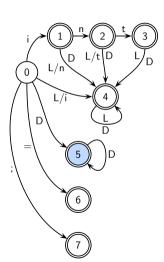
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SNo S MS Buffer NC LFS MP Action 1 0 $int_int32=5; \leftarrow$ i i 2 1 i $int_int32=5; \leftarrow$ n 1 3 2 in $int_int32=5; \leftarrow$ t 2 4 3 int $int_int32=5; \leftarrow$ $=$ 3 3 5 0 $int32=5; \leftarrow$ $=$ 1 Discard $=$ 1 6 0 $int32=5; \leftarrow$ $=$ 1 $=$ 1 7 1 i $int32=5; \leftarrow$ $=$ 1 $=$ 2 9 3 int $int32=5; \leftarrow$ $=$ 4 $=$ 2 9 3 int $int32=5; \leftarrow$ $=$ 4 $=$ 4 10 4 $int32=5; \leftarrow$ $=$ 4 $=$ 4 11 4 $int32=5; \leftarrow$ $=$ 4 $=$ 4 12 0 $=$ 5; \leftarrow $=$ 4 $=$ 4 13 6 $=$ 5; \leftarrow 5 $=$ 4 14 0 $=$ 5; \leftarrow 5 $=$ 5 15 5 $=$ 5; \leftarrow 5 $=$ 7								
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	SNo	S	MS	Buffer	NC	LFS	MP	Action
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	1	0		int_int32=5;←	i			
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	2	1	i	int∟int32=5;←	n	1	1	
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	3	2	in	'	-	2	2	
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	4	3	int		1	3	3	Found INT
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	5	0			1			Discard 🗅
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	6	0		'	i			
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	7	1	i	,	n	1	1	
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	8	2	in		t	2	2	
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	9	3			3	3	3	
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	10	4	int3	int32=5;←	2	4	4	
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	11	4	int32	int32=5;←	=	4	5	Found ID
14 0 5;← 5	12	0		,	=			
	13	6	=	=5;←	5	6	1	Found =
15 5 5 5;← ; 5 1 Found NUM	14	0		'	5			
	15	5	5	5;←	;	5	1	Found NUM



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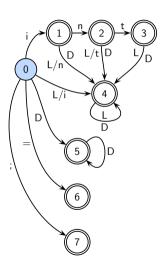
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SNo	S	MS	Buffer	NC	LFS	MP	Action
1	0		int_int32=5;←	i			
2	1	i	int∟int32=5;←	n	1	1	
3	2	in	int_int32=5;←	t	2	2	
4	3	int	int_int32=5;←	1	3	3	Found INT
5	0		ـint32=5;←	1			Discard 🗕
6	0		int32=5;←	-			
7	1	i	int32=5;←	n	1	1	
8	2	in	int32=5;←	t	2	2	
9		int	int32=5;←	3	3	3	
10		int3	int32=5;←	2	4	4	
11	4	int32	int32=5;←	=	4	5	Found ID
12	0		=5;←	=			
13	6	=	=5;←	5	6	1	Found =
14	0		5;←	5			
15	5	5	5;←	;	5	1	Found NUM
16	0		;←	;			



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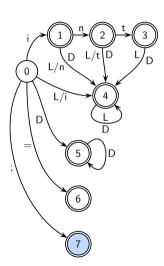
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SNo	S	MS	Buffer	NC	LFS	MP	Action
1	0		int_int32=5;←	i			
2	1	i	int _a int32=5;←	n	1	1	
3	2	in	int_int32=5;←	t	2	2	
4	3	int	int_int32=5;←		3	3	Found INT
5	0		∟int32=5;←	1			Discard 🗕
6	0		int32=5;←	i			
7	1	i	int32=5;←	n	1	1	
8	2	in	int32=5;←	t	2	2	
9	3	int	int32=5;←	3	3	3	
10		int3	int32=5;←	2	4	4	
11	4	int32	int32=5;←	=	4	5	Found ID
12	0		=5;←	=			
13	6	=	=5;←	5	6	1	Found =
14	0		5;←	5			
15	5	5	5;←	;	5	1	Found NUM
16	0		;←	;			
17	7	;	\leftarrow	\downarrow	7		Found ;



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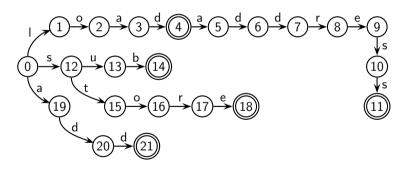
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Tutorial Problem On Scanning

 Find the occurrences of following substrings in a given input string load, loadaddress, add, sub, store

• Use the following automata



ullet Scan two input strings lacksquare loadsubadd \longleftrightarrow and lacksquare loadaddsub \longleftrightarrow



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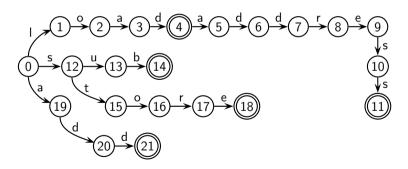
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The Role of MarkedPos

Observe the role of MarkedPos for the input | loadaddsub← |





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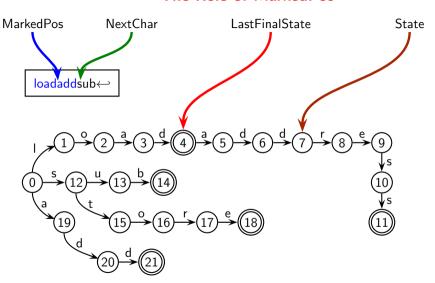
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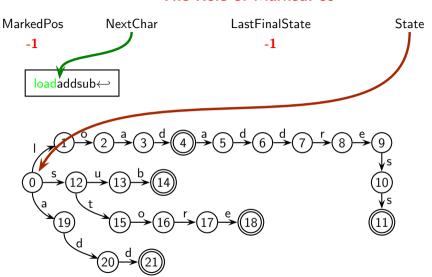
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Demo of Scan Trace



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Tokenizing the Input

Constructing DFAs

Representing DFAs

Minimizing DFAs



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Constructing DFA for Multiple Patterns

- Join multiple DFAs/NFAs using ϵ transition Transition without consuming any input symbol
- This creates an NFA (Non-deterministic Finite Automaton)
 - Possible transition without consuming any input symbol
 - Possibly multiple transitions on the same input symbol
- Make the NFA deterministic by subset construction
 - Each state in the resulting DFA is a set of "similar" states of the NFA
 - The start state of the DFA is a union of all original start states (of multiple patterns)
 - Subsequent states are identified by finding out the sets of states of the NFA for each possible input symbol



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Constructing NFA for a Regular Expression

Consider a regular expression R. Apply steps 1 to 4 to construct an NFA for R inductively:

- 1. If R is a letter in the alphabet Σ , create a two state NFA that accepts the letter (single transition from the start state to a single final state on the letter)
- 2. If R is $R_1 \cdot R_2$, create an NFA by joining the two NFAs N_1 and N_2 by adding an epsilon transition from every final state of N_1 to the start state of N_2 .
- 3. If the R is $R_1 \mid R_2$, create an NFA by joining the two NFAs N_1 and N_2 by creating a new start state s_0 and a new final state s_f . Add an epsilon transition from s_0 the start state of R_1 and similarly for R_2 . Add an epsilon transition from every final state of N_1 to s_f and similarly for N_2 .
- 4. If R is R_1^* , create an NFA by adding an epslion transition from every final state of R_1 to the start state of R_1

Alternatively, we can create a new start state s_0 with an epsilon transition to the start state of R_1 and a new final state s_f with epsilon transitions from the final states of R_1 , and then add an espilon transition from s_f to s_0 .



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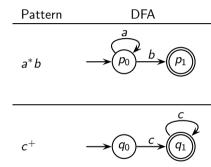
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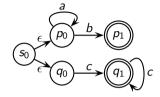
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State	7	Transition				
State	а	b	С			



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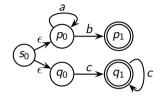
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Constructing DFA for Multiple Patterns: Example 1



State	Transition			
State	а	Ь	С	
$\{s_0, p_0, q_0\}$				

 $\left(\left\{s_0,p_0,q_0\right\}\right)$



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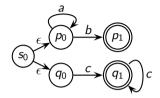
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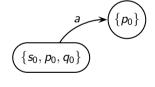
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State	Transition			
State	а	Ь	С	
$\{s_0, p_0, q_0\}$	$\{p_0\}$			
$\{p_0\}$				





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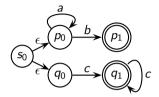
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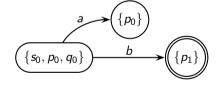
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State	Transition				
State	а	Ь	С		
$\{s_0, p_0, q_0\}$	$\{p_0\}$	$\{p_1\}$			
$\{p_0\}$					
$\{p_1\}$					





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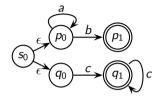
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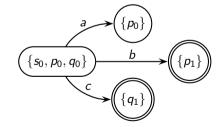
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State	Transition				
State	а	Ь	С		
$\{s_0, p_0, q_0\}$	$\{p_0\}$	$\{p_1\}$	$\{q_1\}$		
$\{p_0\}$					
$\{p_1\}$					
$\{q_1\}$					





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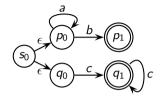
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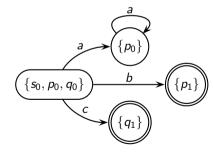
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State	Transition				
State	а	Ь	С		
$\{s_0, p_0, q_0\}$	$\{p_0\}$	$\{p_1\}$	$\{q_1\}$		
$\{p_0\}$	$\{p_0\}$				
$\{p_1\}$					
$\{q_1\}$					





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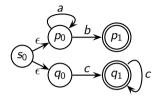
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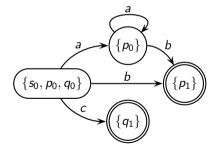
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State	Transition			
State	а	Ь	С	
$\{s_0, p_0, q_0\}$	$\{p_0\}$	$\{p_1\}$	$\{q_1\}$	
$\{p_0\}$	$\{p_0\}$	$\{p_1\}$		
$\{p_1\}$				
$\{q_1\}$				





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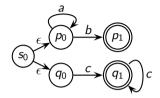
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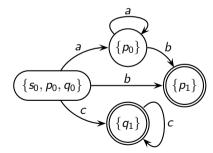
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State	Transition			
State	а	Ь	С	
$\{s_0, p_0, q_0\}$	$\{p_0\}$	$\{p_1\}$	$\{q_1\}$	
$\{p_0\}$	$\{p_0\}$	$\{p_1\}$		
$\{p_1\}$				
$\{q_1\}$			$\{q_1\}$	





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Constructing DFA for Multiple Patterns: Example 2

Let L and D denote the set of all letters and digits, respectively

Pattern	Token
int	INT
L(L D)*	ID
D^+	NUM
=	=
;	;

For convenience, we will ignore the last two patterns that are completely independent



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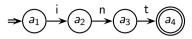
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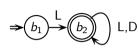
Tokenizing the Input

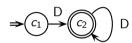
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State	i	n	t	$L-\{i, n, t\}$	D



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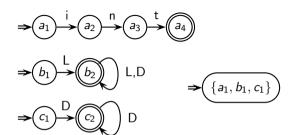
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State	i	n	t	$L-\{i,n,t\}$	D
$\{a_1,b_1,c_1\}$					



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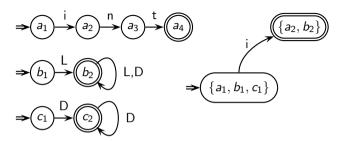
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State	i	n	t	$L-\{i,n,t\}$	D
$ \begin{cases} a_1, b_1, c_1 \\ a_2, b_2 \end{cases} $	$\{a_2,b_2\}$				
$\{a_2,b_2\}$					



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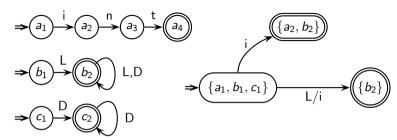
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State	i	n	t	$L-\{i,n,t\}$	D
$\{a_1,b_1,c_1\}$	$\{a_2,b_2\}$	{b ₂ }	{b ₂ }	{b ₂ }	
$\{a_2,b_2\}$					
$\{b_2\}$					



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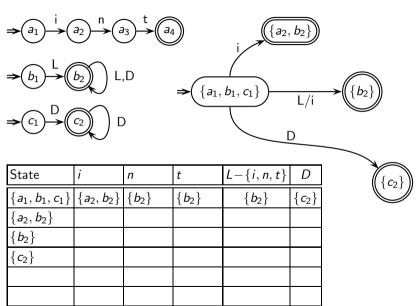
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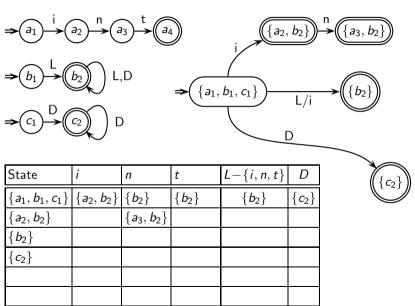
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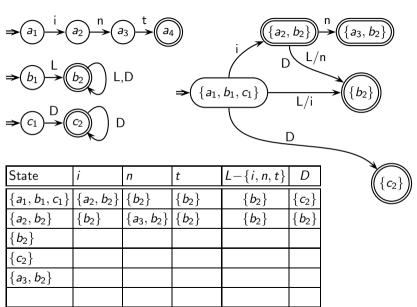
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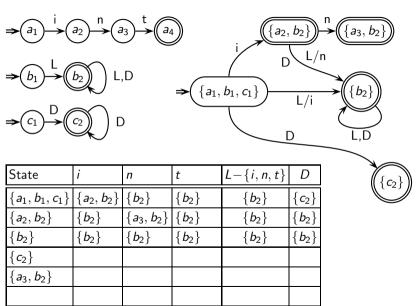
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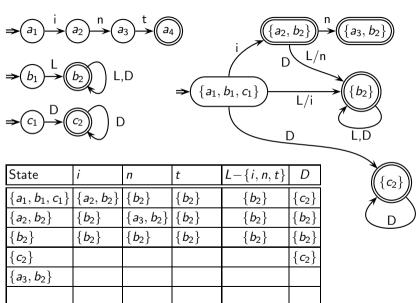
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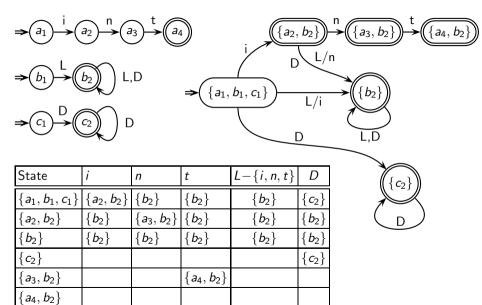
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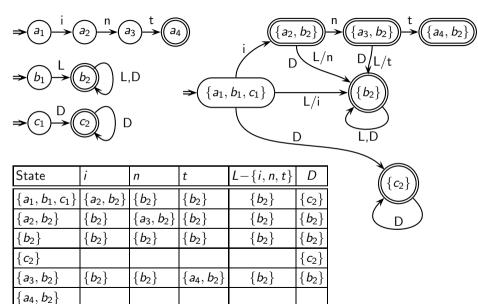
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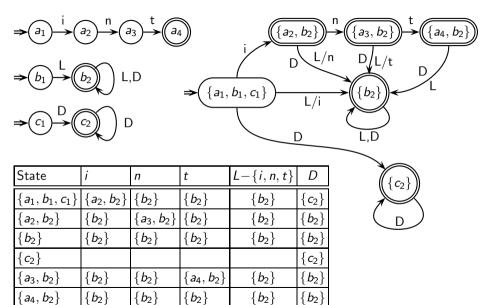
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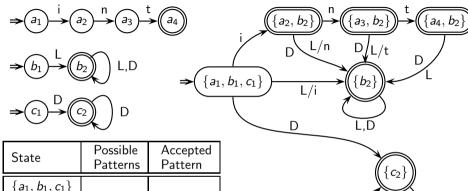
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State	Possible Patterns	Accepted Pattern
$\{a_1, b_1, c_1\}$		
$\{a_2,b_2\}$	ID	ID
$\{b_2\}$	ID	ID
$\{c_2\}$	NUM	NUM
$\{a_3,b_2\}$	ID	ID
$\{a_4,b_2\}$	INT, ID	INT



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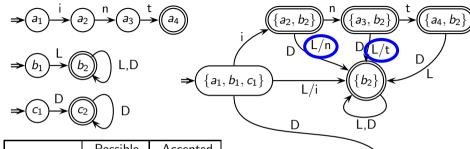
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Constructing DFA for Multiple Patterns: Example 2



State	Possible Patterns	Accepted Pattern
$\{a_1, b_1, c_1\}$		
$\{a_2,b_2\}$	ID	ID
$\{b_2\}$	ID	ID
$\{c_2\}$	NUM	NUM
$\{a_3, b_2\}$	ID	ID
$\{a_4, b_2\}$	INT. ID	INT

Longest match. Lexeme "int" reaches state $\{a_4, b_2\}$ whereas lexeme "integer" reaches the state $\{b_2\}$

First maching rule preferred. Transitions L/n and L/t to state $\{b_2\}$ ensure that INT is prefered over ID for the lexeme "int"



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Representing DFAs Using Four Arrays



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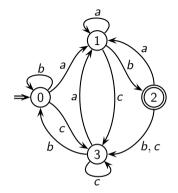
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DFA to be Represented Using Four Arrays: Example 1





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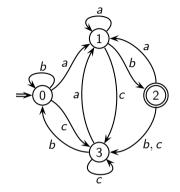
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DFA to be Represented Using Four Arrays: Example 1



	а	b	С
0	1	0	3
1	1	2	3
2	1	3	3
3	1	0	3



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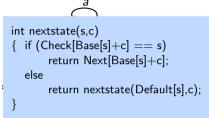
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DFA to be Represented Using Four Arrays: Example 1



States 0 and 3 have identical transitions. Transitions in states 1 and 2 differ from them only on b.

ı	Char	Code
	а	0
	b	1
	С	2
•		

	а	b	С
0	1	0	3
1	1	2	3
2	1	3	3
3	1	0	3

State	Default	Base
0		
1		
2		
3		

	Next	Check
0		
1		
2		
3		
4		
5		



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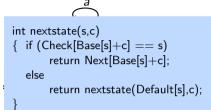
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DFA to be Represented Using Four Arrays: Example 1



We choose to fill the entries for state 0 first (state 3 could also have been used)

Char	Code
а	0
b	1
С	2

	а	b	U
0	1	0	3
1	1	2	3
2	1	3	3

State	Default	Base
0		
1		
2		
3		

	Next	Check
0		
1		
2		
3		
4		
5		



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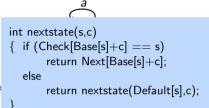
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Representing DFAs

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DFA to be Represented Using Four Arrays: Example 1



The Check array contains 0 to confirm that the corresponding entries in the next array are for state 0

Char	Code
а	0
b	1
С	2

b, c
3
C

	а	b	С
0	1	0	3
1	1	2	3
2	1	3	3
3	1	0	3

State	Default	Base	 0
0		•	1
1			2
2			3
3			4
			5

Next	Check
1	0
0	0
3	0



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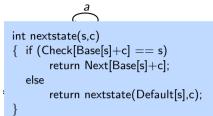
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DFA to be Represented Using Four Arrays: Example 1



The Check array contains 0 to confirm that the corresponding entries in the next array are for state 0

Char	Code
а	0
b	1
С	2

		C								N	JXS		Checl	k
					State	efault	Base		► 0		1		0	
	а	b	С		0		•		1)	0		0	
0 (1	0	3	> —	-				2	Ţ:	3		(0)	
1	1	2	3		2				3					
2	1	3	3		3				4			ı		
3	1	0	3				ı	1	5			Ī		



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DFA to be Represented Using Four Arrays: Example 1



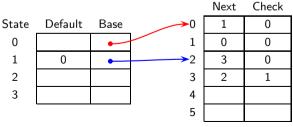
For state 1, we reuse the transitions on a and c from state 0 but need to enter transition on b explicitly. We do this using the next free entry (index 3) in the next array and back calculating the base of state 1.

return nextstate(Default[s],c);



Code
0
1
2

	а	b	С
0	1	0	3
1	1	2	3
2	1	3	3
3	1	0	3





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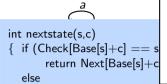
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DFA to be Represented Using Four Arrays: Example 1



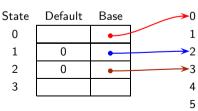
The variation in state 2 is similar to that for state 1. We reuse the transitions on a and c from state 0 but enter transition on b explicitly in the next free entry (index 4) in the next array and back-calculate the base of state 2.



return nextstate(Default[s].c);

	Char	Code
	а	0
ĺ	b	1
	С	2
l	С	2

	а	b	С
0	1	0	3
1	1	2	3
2	1	3	3
3	1	0	3



Next	Check	
1	0	
0	0	
3	0	
2	1	
3	2	



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DFA to be Represented Using Four Arrays: Example 1

int nextstate(s,c)
{ if (Check[Base[s]+c] == s
 return Next[Base[s]+c
 else

State 3 is identical to state 0. We have shown here its base as same as for state 0. (In practice, lex begins the entries from index 1

and keeps index 0 free for such entries. We have ignored it because it is a matter of details.)

return nextstate(Default[s],c);



Char	Code
а	0
b	1
С	2

	а	b	С
0	1	0	3
1	1	2	3
2	1	3	3
3	1	0	3

State	Default	Base	* (
0		•	/ 1
1	0	•	<u></u> / /2
2	0	•	_/3
3	0	•	4
			. 5

Next	Check
1	0
0	0
3	0
2	1
3	2



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C---:6:--- C-----

Tokenizing the Input

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The Intuition Behind Four Array Representation

How to find the appropriate space in Next array for a state?

- View the entries (in the row of the state) that are required to be stored as "pins" separated by the entries that are not required to be stored
- View the positions in the Next array that do not contain a transiton as "holes"
- Try to match the pattern (i.e. separation) of pins with that of the available holes



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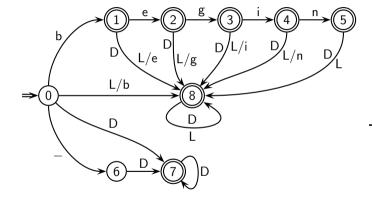
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DFA to be Represented Using Four Arrays: Example 2



Set	Characters
L	a to z
D	0 to 9

Pattern	Token
begin	BEGIN
$L(L D)^*$	ID
$(- \epsilon)D^+$	NUM



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Table Representation for Example 2

In the following, L denotes any letter from a to z other than b, e, g, i, n because these letters are listed seperately

	b	е	g	i	n	L	D	-
0	1	8	8	8	8	8	7	6
1	8	2	8	8	8	8	8	
2	8	8	3	8	8	8	8	
3	8	8	8	4	8	8	8	
4	8	8	8	8	5	8	8	
5	8	8	8	8	8	8	8	
6							7	
7							7	
8	8	8	8	8	8	8	8	

Character	Code
a - z	0 - 25
0 - 9	26 - 35
-	36



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Choice of Default States for Example 2

- States 8 and 6 are represented independently
- State 6 is the default state for state 7
- State 8 is the default state for all other states



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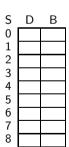
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Four Arrays Representation for Example 2

S: State D: Default B: Base N: Next C: Check



	N	С		N	С		N	С		N	С
0			20			40			60		
1			21			41			61		
2			22			42			62		
3			23			43			63		
4			24			44			64		
1 2 3 4 5 6 7 8			25			45			65		
6			26			46			66		
7			27			47			67		
8			28			48			68		
9			29			49			69		
10			30			50			70		
11			31			51			71		
12			32			52			72		
13			33			53			73		
14			34			54			74		
15			35			55			75		
16			36			56			76		
17			37			57			77		
18			38			58			78		
19			39			59			79		



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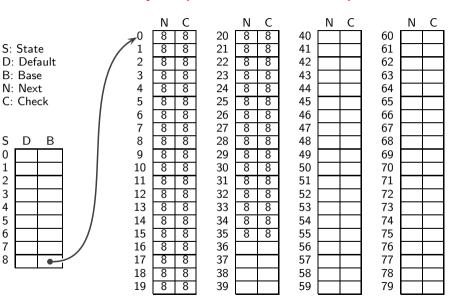
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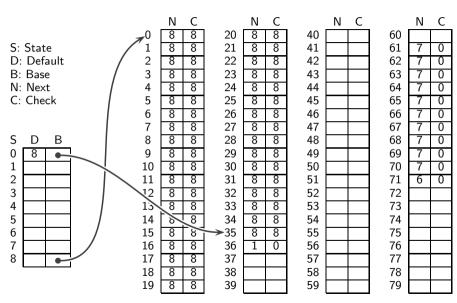
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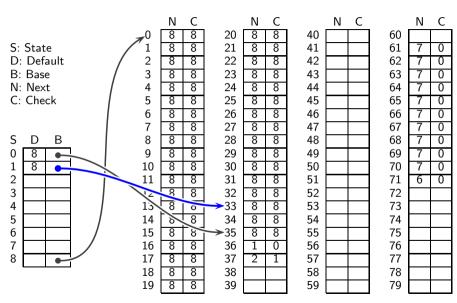
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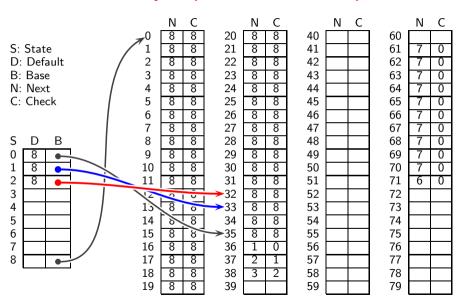
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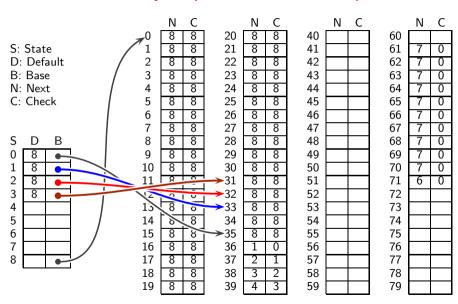




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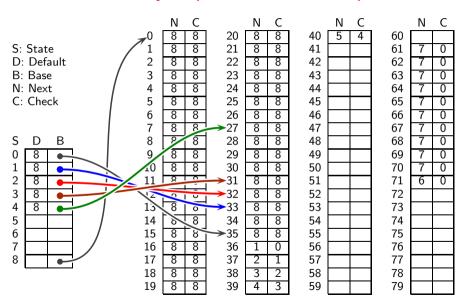
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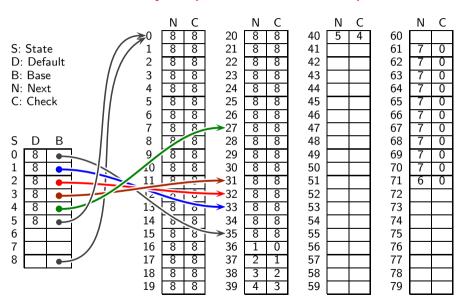
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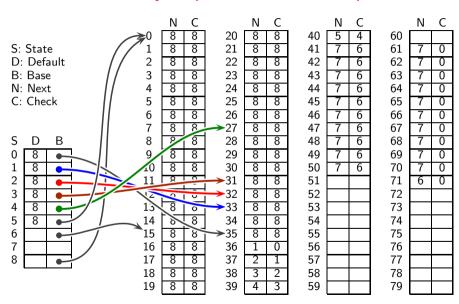
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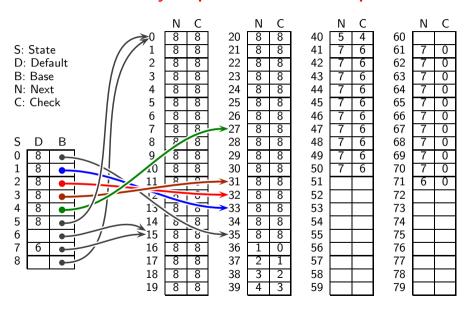
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Size Comparison for Example 2

• Space for a 2 dimensional table

rows
$$\times$$
 columns = $9 \times 36 = 324$

• Space for four arrays representation

Array	Size
Next	71
Check	71
Default	9
Base	9
Total	160

 If a large graph seen as adjacency matrix is stored using four arrays, it would have the need of pointers and dynamic memory allocation
 This would imply good cache behaviour



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Further Compression Using Equivalence Classes

- The four arrays handle similarity in the rows of the 2-D table
- Several columns could have a lot of similarity too
- We can define equivalence classes of characters that have identical transitions Identical columns are collapsed into a single column
- The equivalence classes are given contiguous codes thereby eliminating several "holes" in the Next and Check arrays



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Further Compression Using Equivalence Classes for Example 2

$EC \to$	0	1	2	3	4	5	6	7
	b	е	g	i	n	L	D	-
0	1	8	8	8	8	8	7	6
1	8	2	8	8	8	8	8	
2	8	8	3	8	8	8	8	
3	8	8	8	4	8	8	8	
4	8	8	8	8	5	8	8	
5	8	8	8	8	8	8	8	
6							7	
7							7	
8	8	8	8	8	8	8	8	

```
\label{eq:continuous_state} \begin{split} & \text{int nextstate(s,c)} \\ & \{ & \text{if (Check[Base[s]+c]} == s) \\ & \text{return Next[Base[s]+c];} \\ & \text{else} \\ & \text{return nextstate(Default[s],c);} \\ & \} \end{split}
```

Now c represents the class of a character instead of the character



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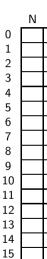
Four Arrays Representation Using Equivalence Classes for Example 2

$EC \to$	0	1	2	3	4	5	6	7
	b	е	g	i	n	L	D	-
0	1	8	8	8	8	8	7	6
1	8	2	8	8	8	8	8	
2	8	8	3	8	8	8	8	
3	8	8	8	4	8	8	8	
4	8	8	8	8	5	8	8	
5	8	8	8	8	8	8	8	
6							7	
7							7	
8	8	8	8	8	8	8	8	

:	Stat	:e	
):	Def	ault	
3:	Bas	e	
1:	Nex	t	
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8



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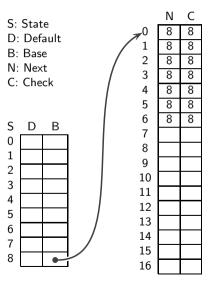
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$EC \to$	0	1	2	3	4	5	6	7
	b	е	g	i	n	L	D	-
0	1	8	8	8	8	8	7	6
1	8	2	8	8	8	8	8	
2	8	8	3	8	8	8	8	
3	8	8	8	4	8	8	8	
4	8	8	8	8	5	8	8	
5	8	8	8	8	8	8	8	
6							7	
7							7	
8	8	8	8	8	8	8	8	





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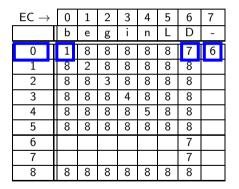
Specifying Scanners

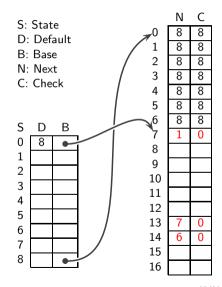
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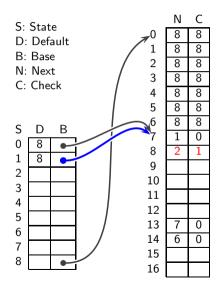
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$EC \to$	0	1	2	3	4	5	6	7
	b	е	g	i	n	L	D	-
0	1	8	8	8	8	8	7	6
1	8	2	8	8	8	8	8	
2	8	8	3	8	8	8	8	
3	8	8	8	4	8	8	8	
4	8	8	8	8	5	8	8	
5	8	8	8	8	8	8	8	
6							7	
7							7	
8	8	8	8	8	8	8	8	





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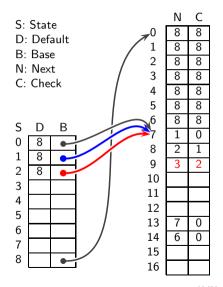
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$EC \to$	0	1	2	3	4	5	6	7
	b	е	g	i	n	L	D	-
0	1	8	8	8	8	8	7	6
1	8	2	8	8	8	8	8	
2	8	8	3	8	8	8	8	
3	8	8	8	4	8	8	8	
4	8	8	8	8	5	8	8	
5	8	8	8	8	8	8	8	
6							7	
7							7	
8	8	8	8	8	8	8	8	





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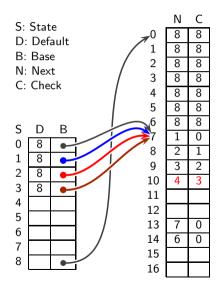
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$EC \to$	0	1	2	3	4	5	6	7
	b	е	g	i	n	L	D	-
0	1	8	8	8	8	8	7	6
1	8	2	8	8	8	8	8	
2	8	8	3	8	8	8	8	
3	8	8	8	4	8	8	8	
4	8	8	8	8	5	8	8	
5	8	8	8	8	8	8	8	
6							7	
7							7	
8	8	8	8	8	8	8	8	





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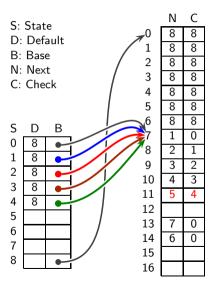
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$EC \to$	0	1	2	3	4	5	6	7
	b	е	g	i	n	L	D	-
0	1	8	8	8	8	8	7	6
1	8	2	8	8	8	8	8	
2	8	8	3	8	8	8	8	
3	8	8	8	4	8	8	8	
4	8	8	8	8	5	8	8	
5	8	8	8	8	8	8	8	
6							7	
7							7	
8	8	8	8	8	8	8	8	





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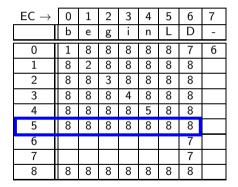
Later describer

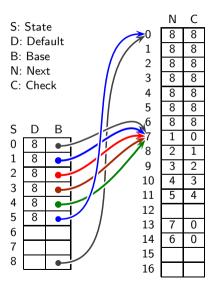
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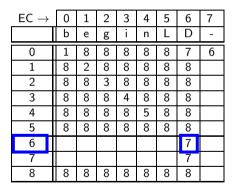
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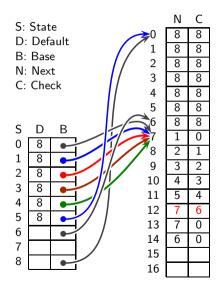
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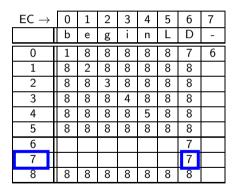
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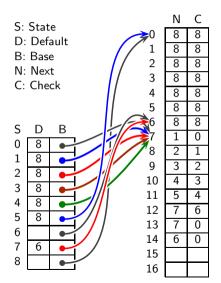
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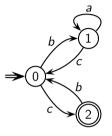
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Tutorial Problem

Represent the following DFA using 4-arrays notation as compactly as possible



Character	Code
а	0
Ь	1
C	2



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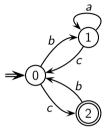
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Tutorial Problem

Represent the following DFA using 4-arrays notation as compactly as possible



Character	Code		
а	0		
Ь	1		
С	2		

State	Base	Default
0	2	
1	0	
2	0	

	Next	Check
0	1	1
1	0	2
2	0	1
3	1	0
4	2	0
5		



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Prof. Sanyal's slides (scanning-slides-sanyal-part3.pdf)