

CS614: Advanced Compilers

Control-Flow Graphs and IDFAs

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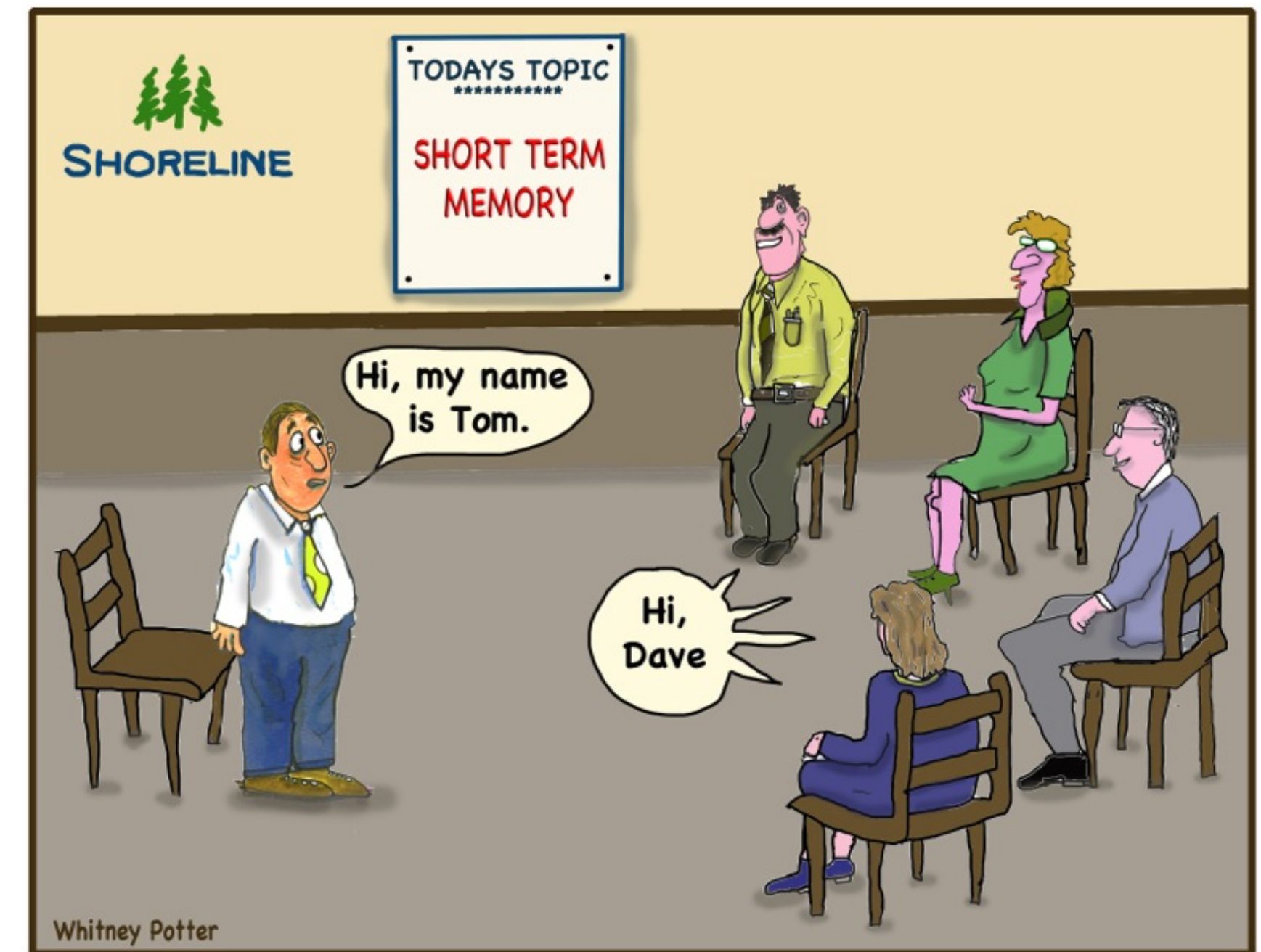
Things we learnt in the last class

- **Examples** of program optimizations

- Machine independent
- Machine dependent

- **Metrics** to measure optimization

- Time, memory, code size, energy

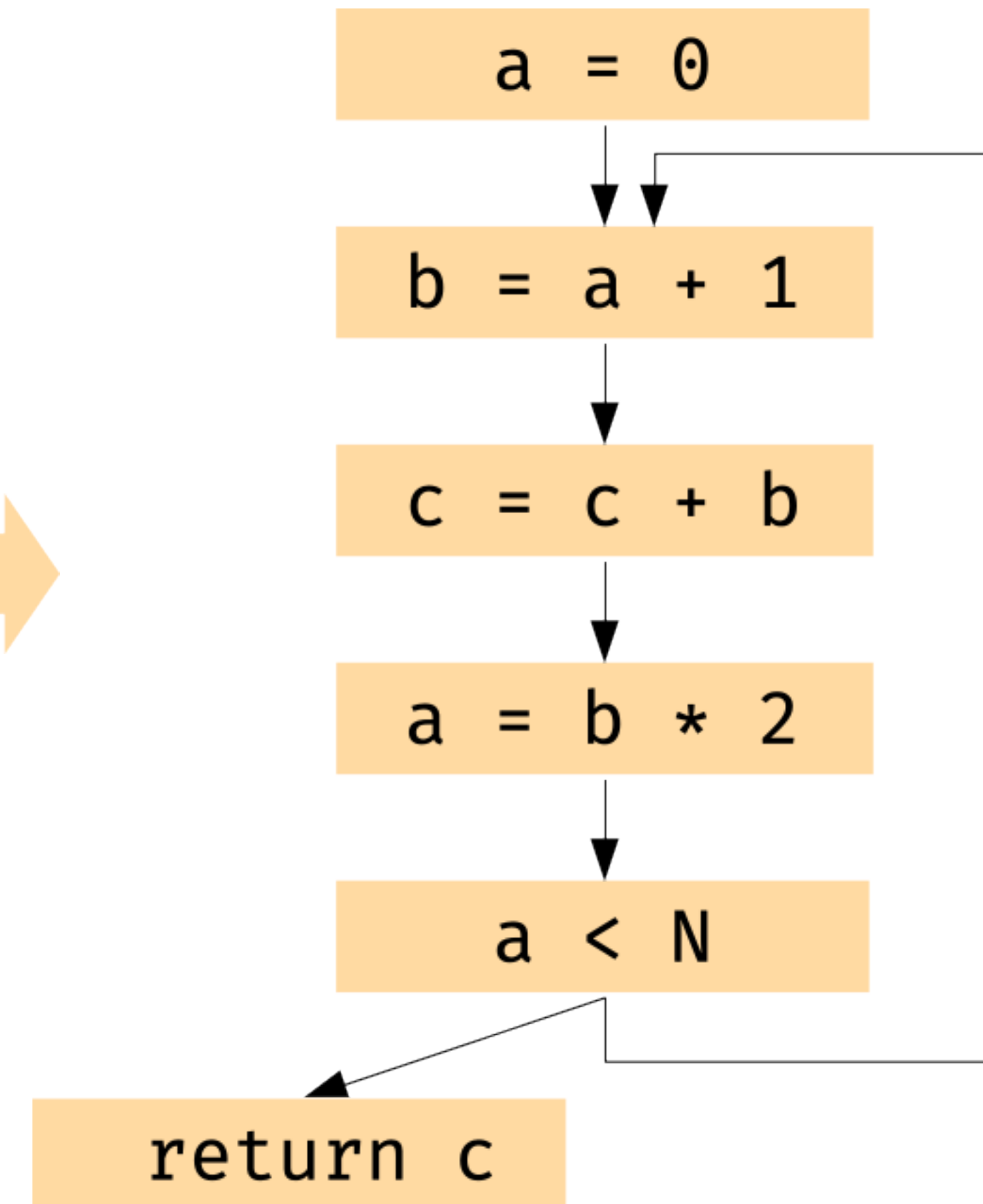


- Optimizations may **interact** with each other as well as themselves
- Optimizations are usually enabled by **program analysis**

Control-Flow Graphs

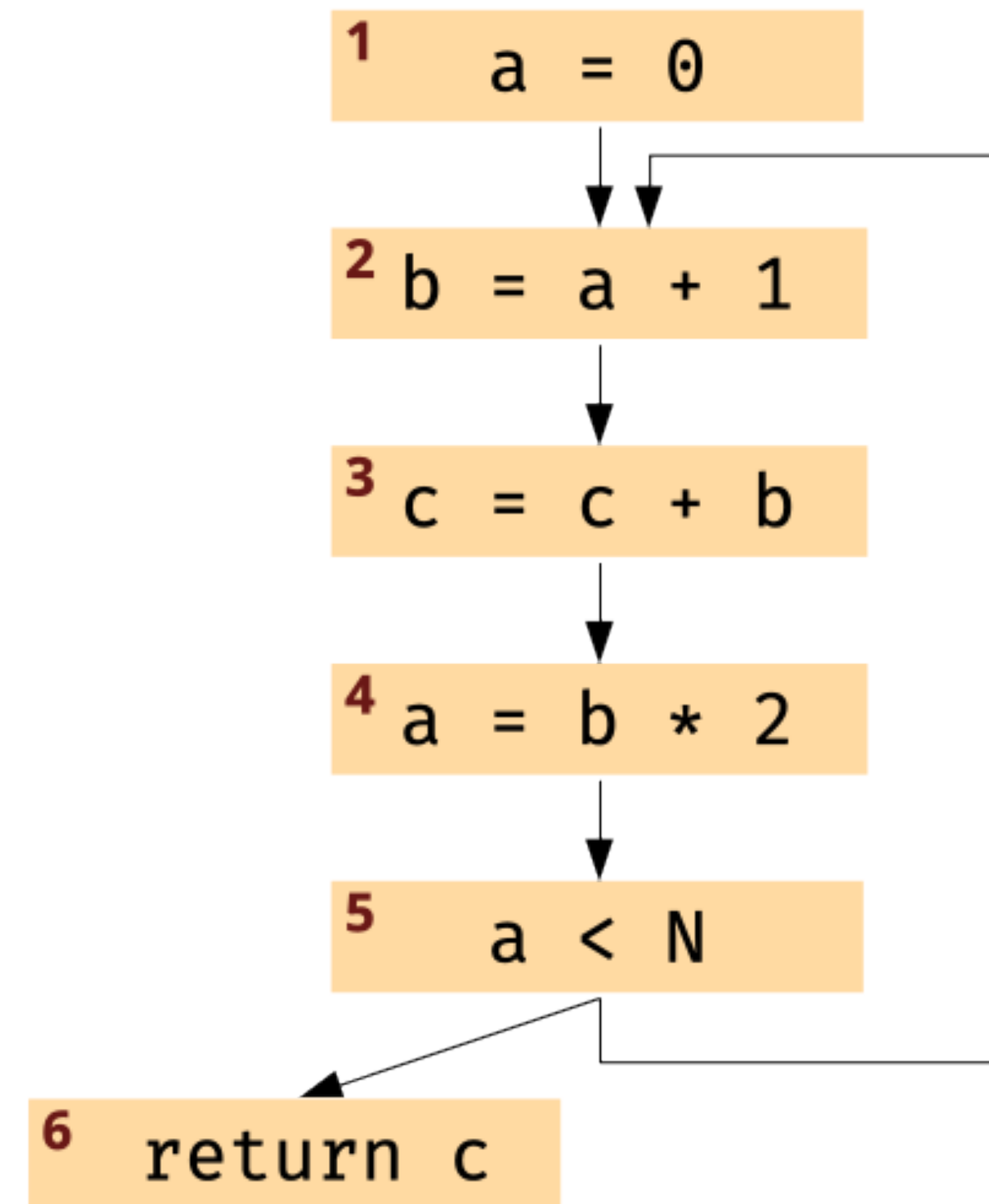
- Nodes represent instructions; edges represent flow of control

```
a = 0  
L1: b = a + 1  
   c = c + b  
   a = b * 2  
   if a < N goto L1  
   return c
```



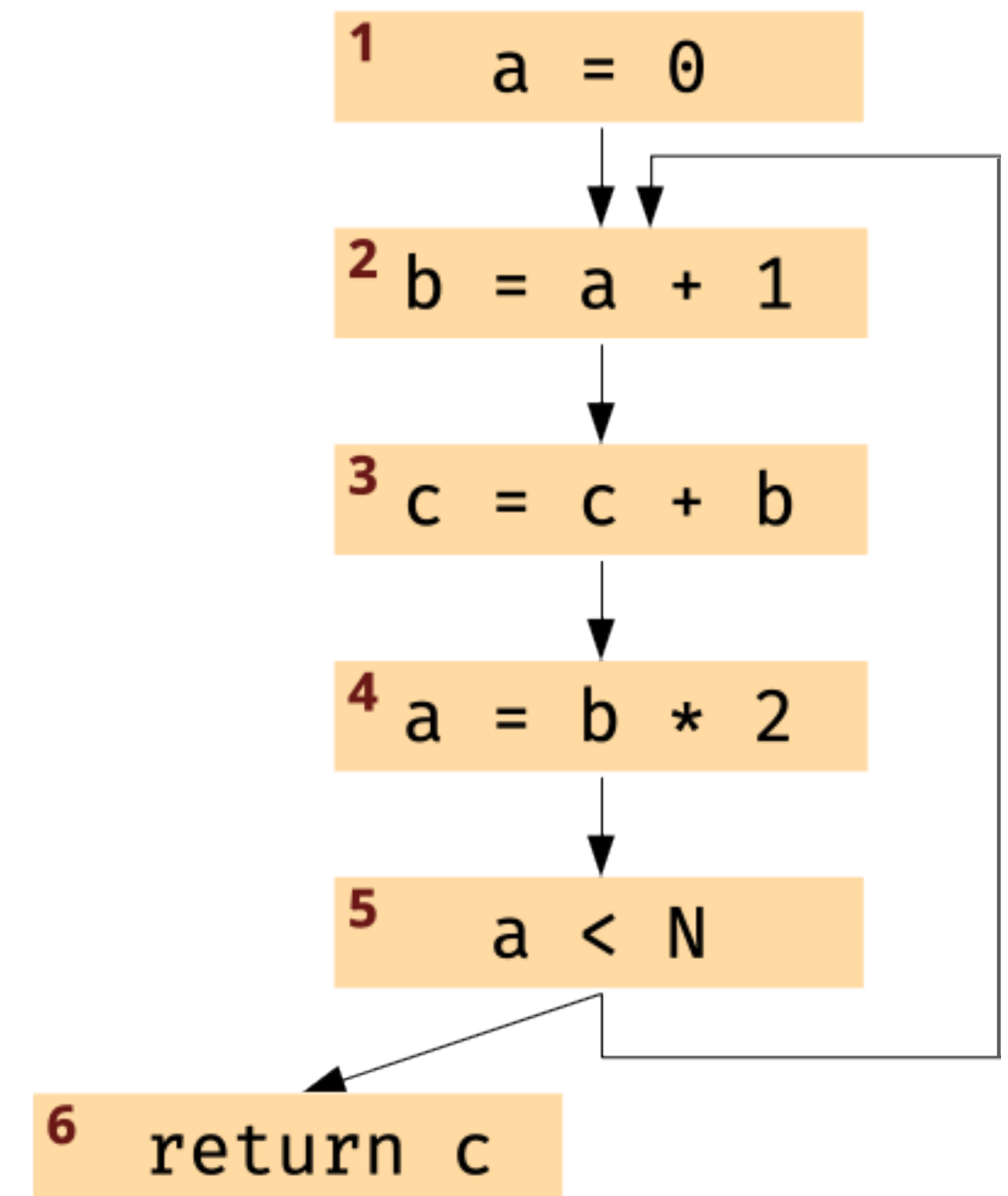
Some CFG Terminology

- $\text{pred}[n]$ gives predecessors of n
 - $\text{pred}[1]$? $\text{pred}[4]$? $\text{pred}[2]$?
- $\text{succ}[n]$ gives successors of n
 - $\text{succ}[2]$? $\text{succ}[5]$?
- $\text{def}(n)$ gives variables defined by n
 - $\text{def}(3) = \{c\}$
- $\text{use}(n)$ gives variables used by n
 - $\text{use}(3) = \{b, c\}$



Let's figure out *liveness* over CFGs

- A variable v is **live** on an edge if there is a directed path from that edge to a **use** of v that does not go through any **def** of v .
- A variable is **live-in** at a node if it is live on any of the in-edges of that node.
- A variable is **live-out** at a node if it is live on any of the out-edges of that node.
- **Check:**
 - a : $\{1 \rightarrow 2, 4 \rightarrow 5 \rightarrow 2\}$
 - b : $\{2 \rightarrow 4\}$



Computation of liveness

- Say *live-in* of n is $\text{in}[n]$, and *live-out* of n is $\text{out}[n]$.
- We can compute $\text{in}[n]$ and $\text{out}[n]$ for any n as follows:

$$\text{in}[n] = \text{use}[n] \cup (\text{out}[n] - \text{def}[n])$$

$$\text{out}[n] = \bigcup_{s \in \text{succ}[n]} \text{in}[s]$$

Called **transfer functions**.

Liveness as an **iterative dataflow analysis**

for each n

$in[n] = \{\}; out[n] = \{\}$

Initialize

IDFA

repeat

for each n

$in'[n] = in[n]; out'[n] = out[n]$

Save previous values

$in[n] = use[n] \cup (out[n] - def[n])$

$out[n] = \bigcup_{s \in succ[n]} in[s]$

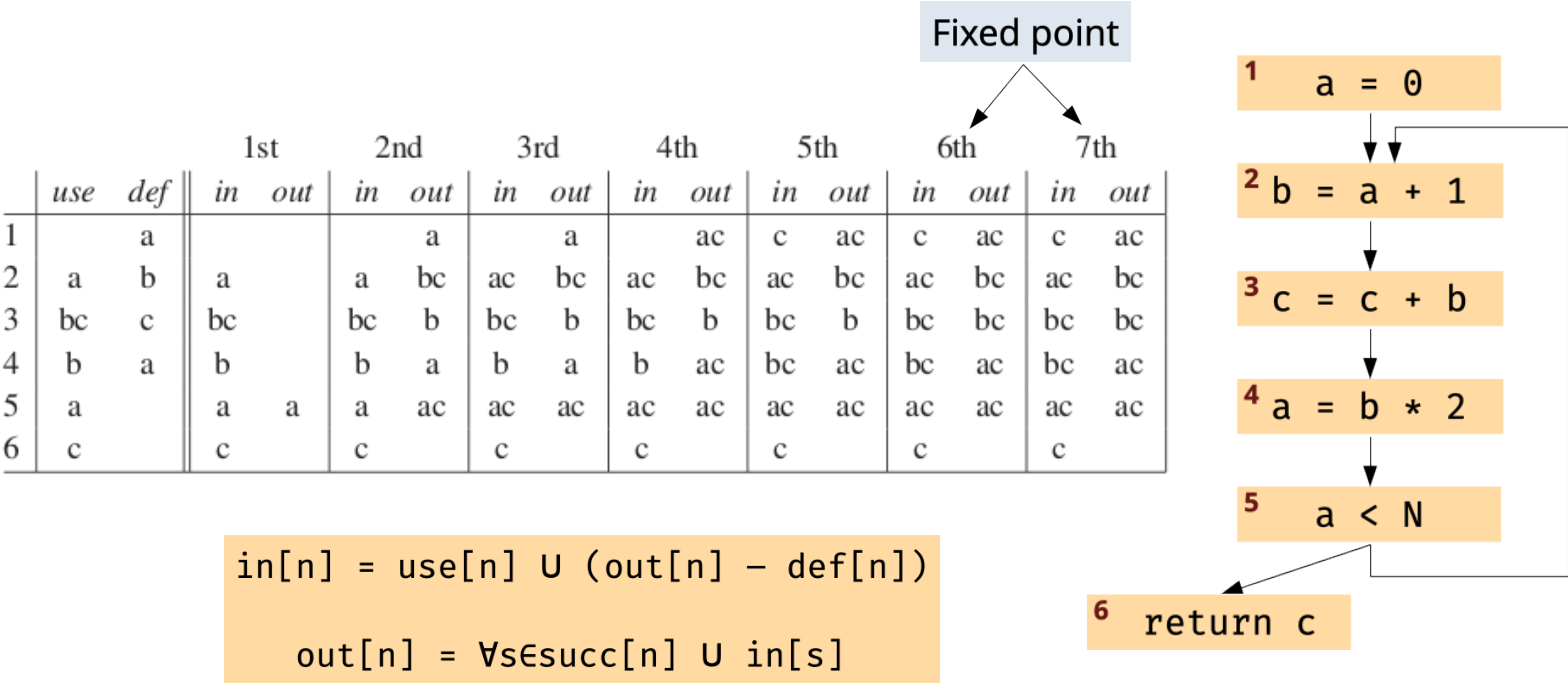
Compute
new values

until $in'[n] == in[n] \text{ and } out'[n] == out[n] \ \forall n$

Repeat till **fixed-point**



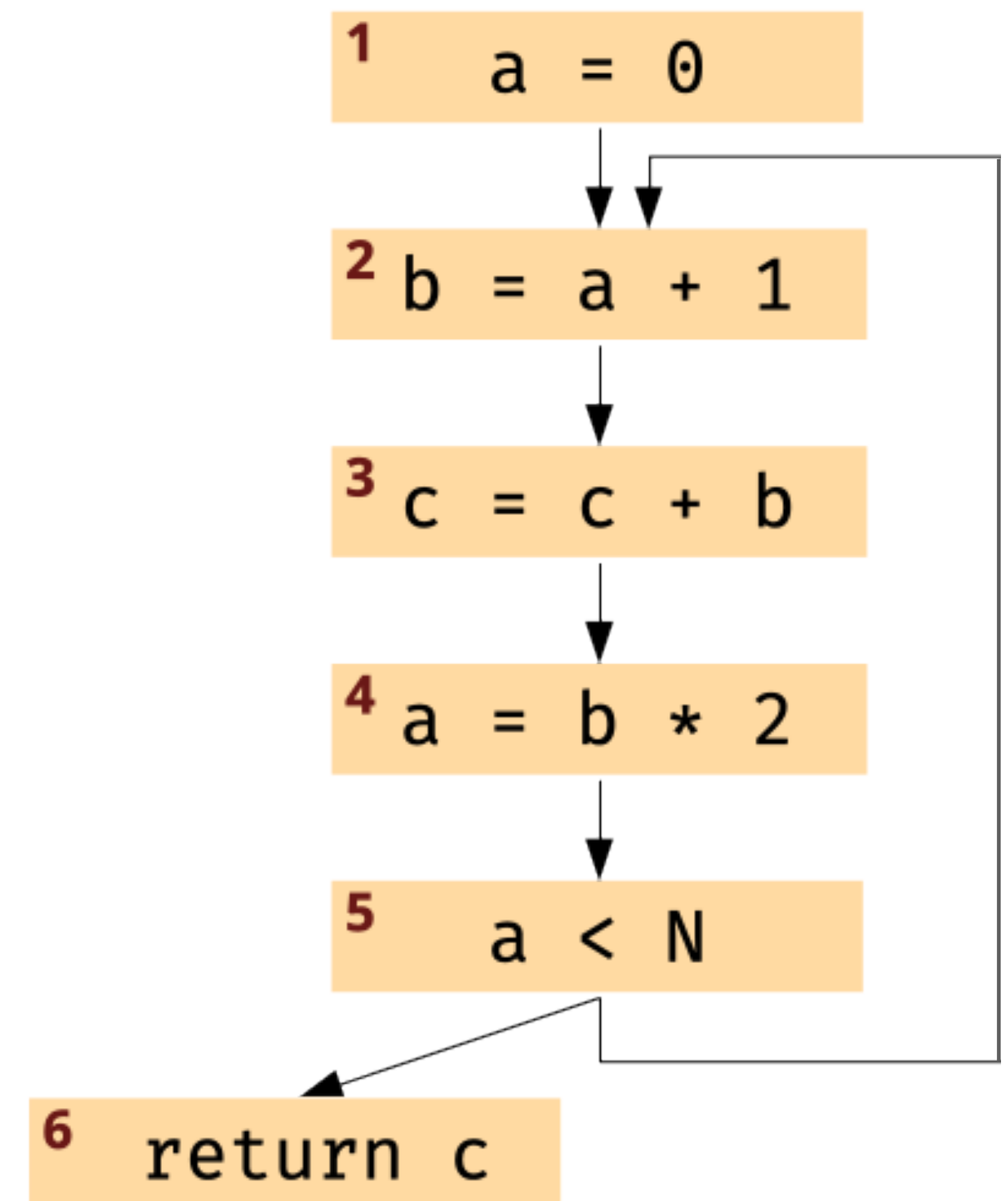
Liveness analysis example



In backward order

	<i>use</i>	<i>def</i>	1st		2nd		3rd	
			<i>out</i>	<i>in</i>	<i>out</i>	<i>in</i>	<i>out</i>	<i>in</i>
6	c			c		c		c
5	a		c	ac	ac	ac	ac	ac
4	b	a	ac	bc	ac	bc	ac	bc
3	bc	c	bc	bc	bc	bc	bc	bc
2	a	b	bc	ac	bc	ac	bc	ac
1		a	ac	c	ac	c	ac	c

- Fixed point in only 3 iterations!
- The order of processing statements matters for **efficiency** (not for correctness).

$$\text{in}[n] = \text{use}[n] \cup (\text{out}[n] - \text{def}[n])$$
$$\text{out}[n] = \bigcup_{s \in \text{succ}[n]} \text{in}[s]$$


Complexity of our liveness computation algorithm

- For **input program of size N**

- $\leq N$ nodes in CFG

- $\Rightarrow N$ variables

- $\Rightarrow N$ elements per in/out

- $\Rightarrow O(N)$ time per set union

```
for each n
    in[n] = {}; out[n] = {}
repeat
    for each n
        in'[n] = in[n]; out'[n] = out[n]
        in[n] = use[n]  $\cup$  (out[n] - def[n])
        out[n] =  $\forall s \in \text{succ}[n]$   $\cup$  in[s]
    until in'[n] == in[n] and out'[n] == out[n]  $\forall n$ 
```

- **for** loop performs **constant number of set operations per node**

- $\Rightarrow O(N^2)$ time for **for** loop

- Each iteration of **for** loop can only add to each set (monotonicity)

- Sizes of all in and out sets sum to $2N^2$, thus **bounding the number of iterations** of the **repeat** loop

- \Rightarrow worst-case complexity of $O(N^4)$

- Much less in practice (usually $O(N)$ or $O(N^2)$) if ordered properly.



Least fixed points

- There is often **more than one solution** for a given dataflow problem.
 - Any solution to dataflow equations is a **conservative approximation**.
- Conservatively assuming a variable is live does not break the program:
 - Just means more registers may be needed.
- Assuming a variable is dead when really live will break things.
- Many possible solutions; but we want the **smallest**: the **least fixed point**.
- The iterative algorithm computes this least fixed point.



Recall our IDFA algorithm

```
for each n
  in[n] = {}; out[n] = {}
  repeat
    for each n
      in'[n] = in[n]; out'[n] = out[n]
      in[n] = use[n] ∪ (out[n] - def[n])
      out[n] = ∪s ∈ succ[n] in[s]
    until in'[n] == in[n] and out'[n] == out[n] ∨ n
```

Initialize

Save previous values

Compute new values

Repeat till **fixed-point**

IDFA

➤ Do we need to process all the nodes in each iteration?



Worklist-based Implementation of IDFA

- Initialize a **worklist** of statements
- Forward analysis:
 - Prefer to start with the **entry** node
 - If $OUT(n)$ changes, then add $succ(n)$ to the worklist
- Backward analysis:
 - Prefer to start with the **exit** node
 - If $IN(n)$ changes, then add $pred(n)$ to the worklist
- In both the cases, iterate till **fixed point**.



Writing an IDFA (Cont.)

- **Confluence** (at control-flow merges):
 - union
 - intersection
- **Requirement for termination:**
 - finiteness of the set of possible dataflow values
 - unidirectional growth/shrinkage, called *monotonicity*
 - *for the dataflow values at each statement*



Liveness analysis revisited

- Direction:
 - Backward
- Confluence operation:
 - Union
- Flow functions:
 - $\text{out}[n] = \forall s \in \text{succ}[n] \cup \text{in}[s]$
 - $\text{in}[n] = \text{use}[n] \cup (\text{out}[n] - \text{def}[n])$



Common subexpressions revisited

- **Idea:** If a program computes the same value multiple times, reuse the value.



- Subexpressions can be reused until operands are redefined.
- Say given a node `n`, the expressions computed at `n` are denoted as `gen(n)` and the ones killed (operands redefined) at `n` are denoted as `kill(n)`.

Computing common subexpressions as an IDFA

- Direction:
 - Forward
- Confluence operation:
 - Intersection
- Flow functions:
 - $in[n] = \forall p \in pred[n] \cap out[p]$
 - $out[n] = gen[n] \cup (in[n] - kill[n])$



Are we efficient enough?

- When can IDFAs take a lot of time?
- Which operations could be **expensive**?
 - Confluence
 - Comparison (equality check)
- Compilers may have to perform several IDFAs.
- How can we make an IDFA more **efficient** (perhaps with some loss of **precision**)?

```
for each n
  in[n] = {}; out[n] = {}
repeat
  for each n
    in'[n] = in[n]; out'[n] = out[n]
    in[n] = use[n] u (out[n] - def[n])
    out[n] =  $\forall s \in \text{succ}[n] \cup \text{in}[s]$ 
  until in'[n] == in[n] and out'[n] == out[n]  $\forall n$ 
```

IDFA

Initialize

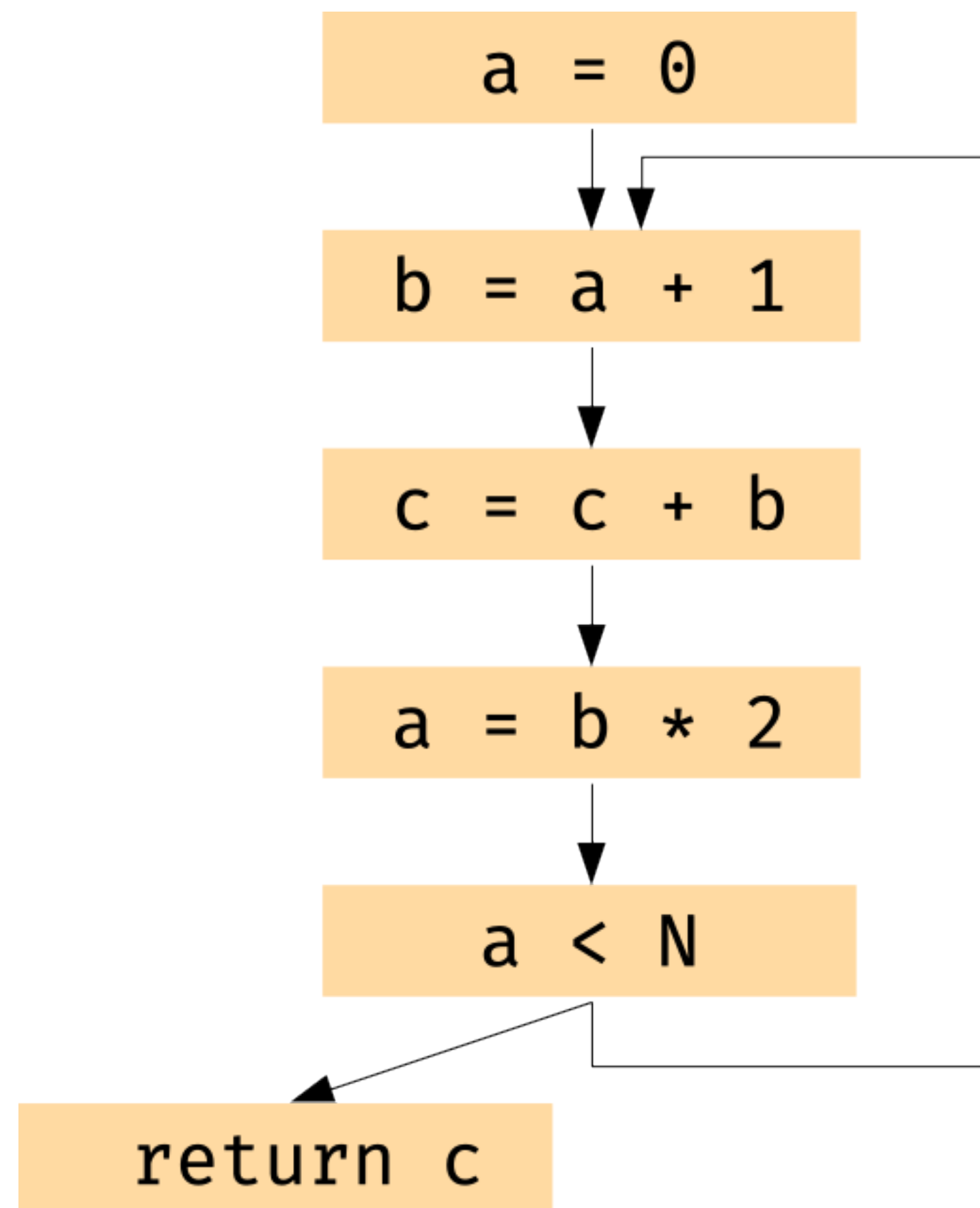
Save previous values

Compute new values

Repeat till **fixed-point**

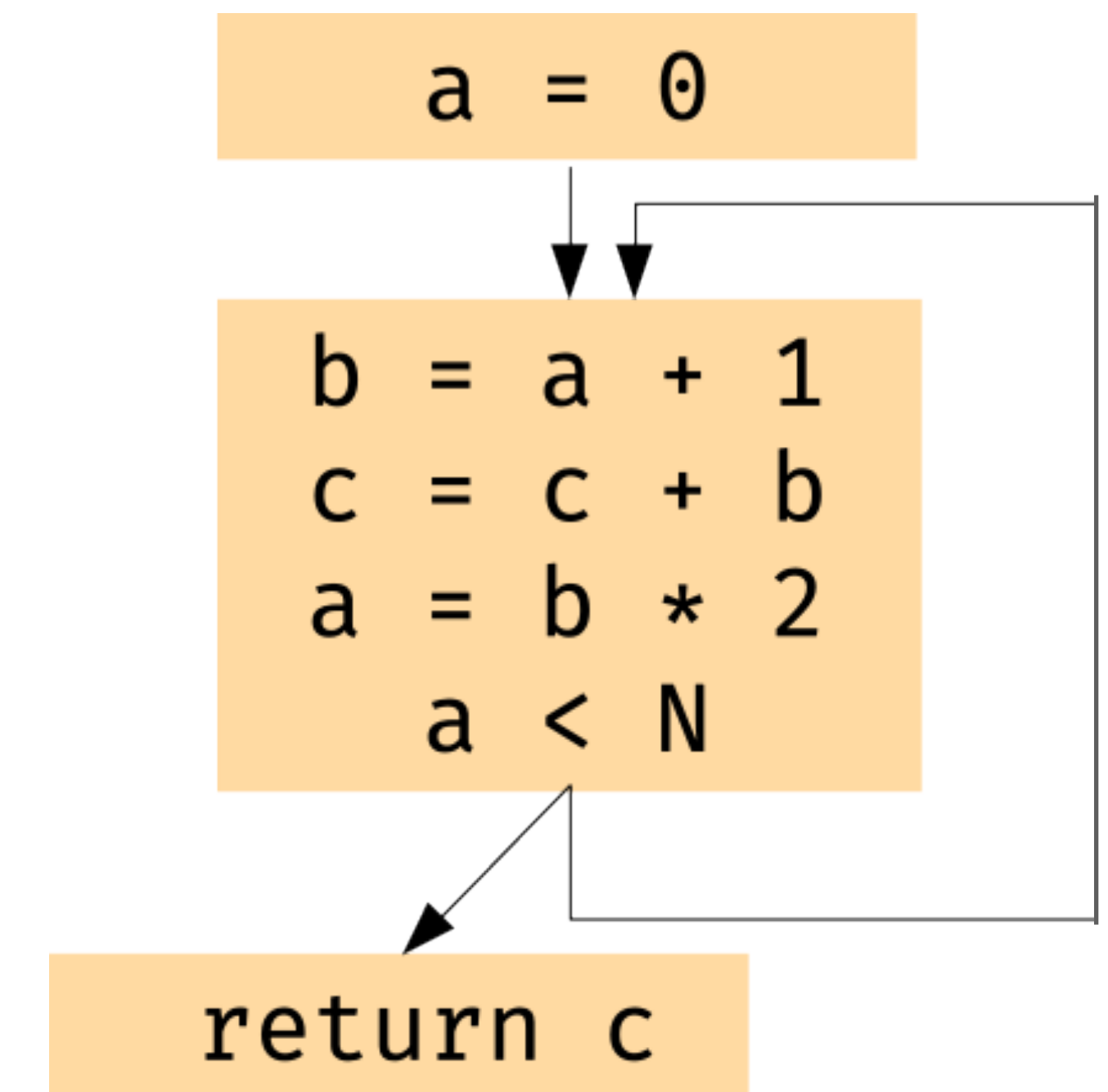


Basic Blocks



Each instruction as a node

```
a = 0
L1: b = a + 1
    c = c + b
    a = b * 2
    if a < N goto L1
    return c
```



Using basic blocks

Basic Blocks (Cont.)

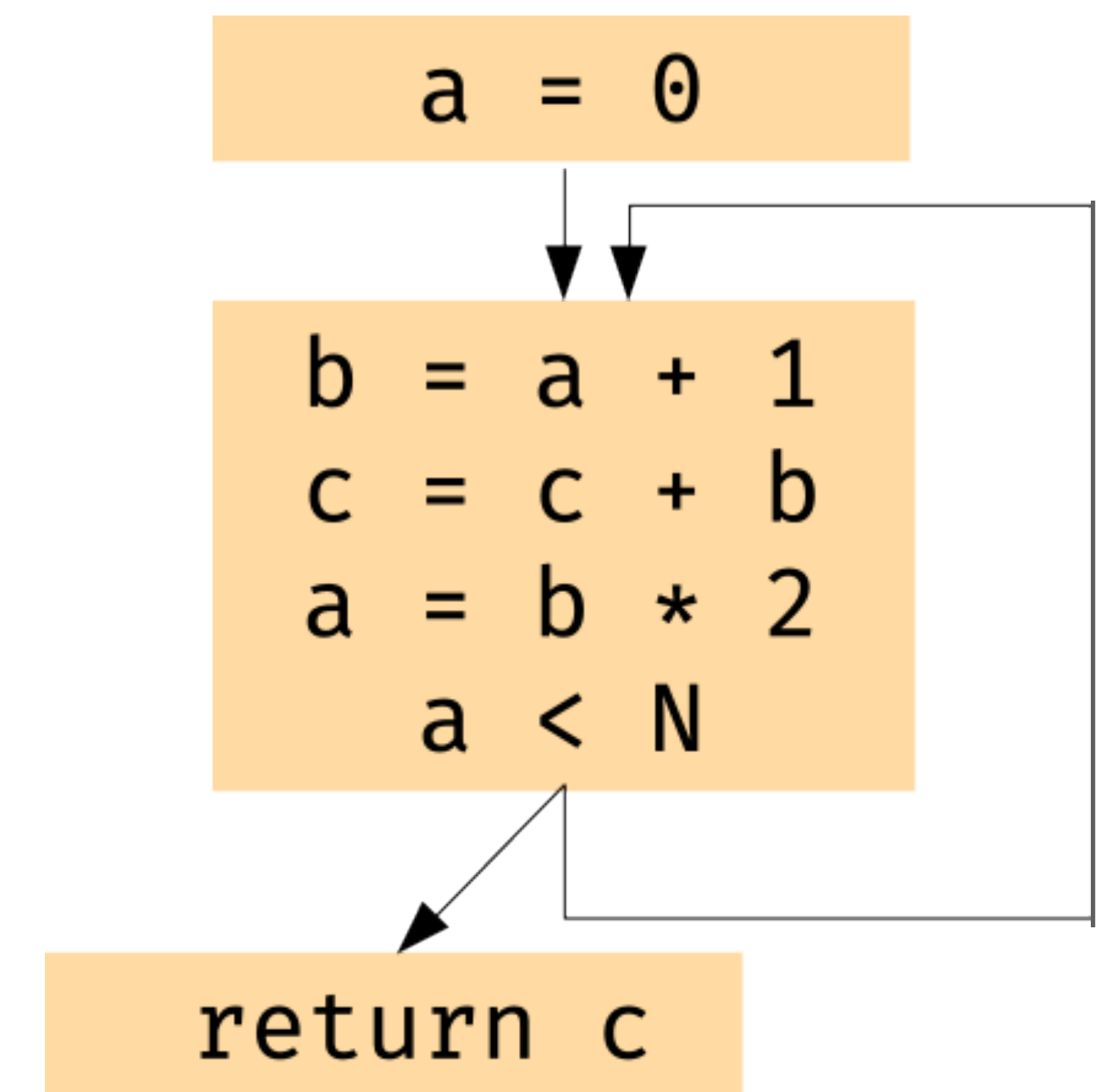
➤ Idea:

- Once execution enters a basic block, all statements are executed in sequence.
- Single-entry, single-exit region

➤ Details:

- Starts with a label
- Ends with one or more branches
- Edges may be labeled with predicates
 - True/false
 - Exceptions

- **Key:** Improve efficiency, with reasonable precision.



Using basic blocks



Have you got a compiler's eyes yet?

```
S1: y = 1;  
S2: y = 2;  
S3: x = y;
```

- What's the advantage if the above program is rewritten as follows?

```
S1: y1 = 1;  
S2: y2 = 2;  
S3: x = y2;
```

Next Class
**Static Single Assignment
(SSA) Form**

- **Def-use** becomes explicit; analysis can become faster.

