CS614: Advanced Compilers

Optimizations based on SSA

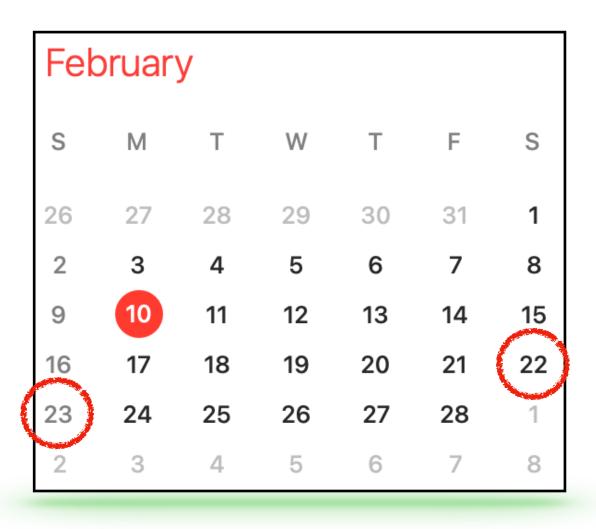
Manas Thakur

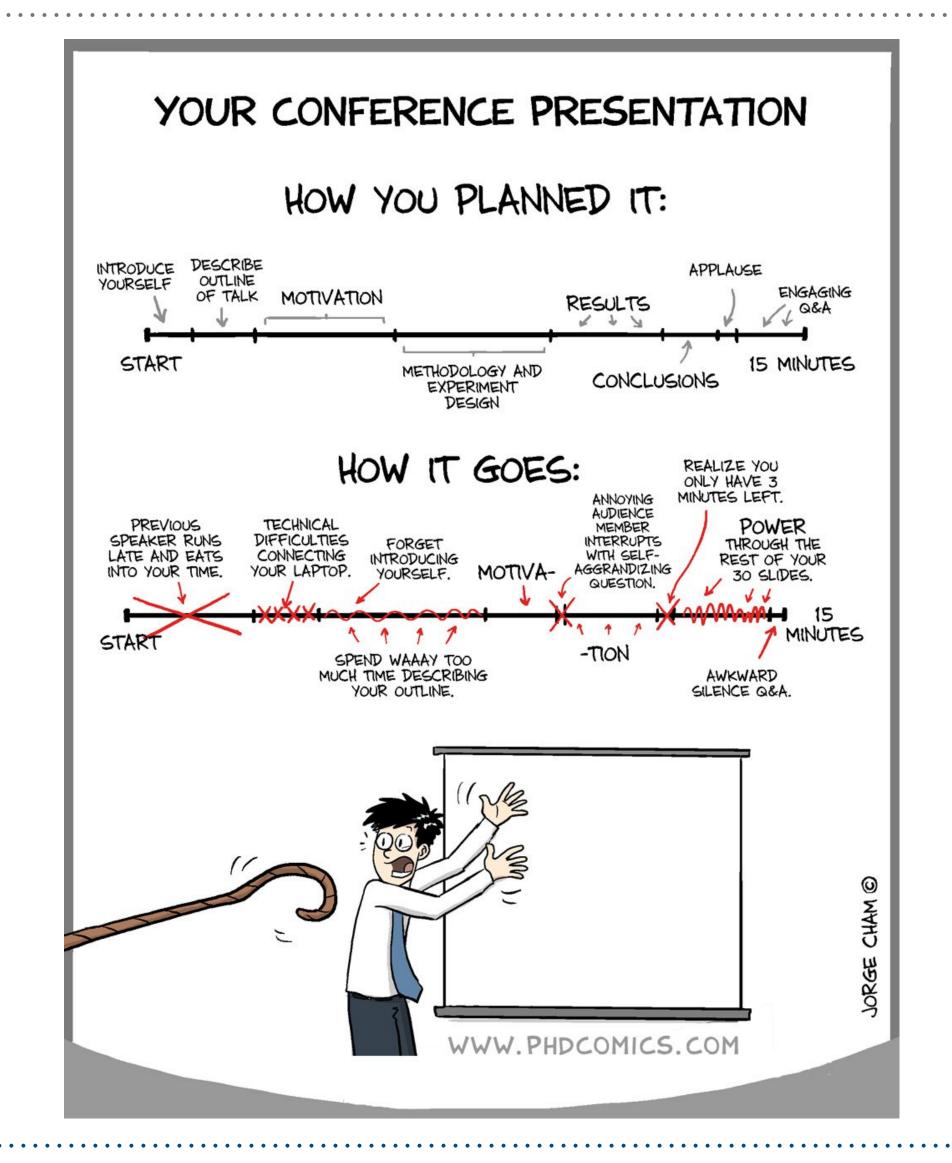
CSE, IIT Bombay



Outline of the recent weeks

- ➤ Intermediate code for OO languages
- ➤ CFGs and IDFAs
- > SSA form: construction, optimizations
- > SCP, SSCP, CCP, SCCP
- ➤ CSE, GVN, PRE
- ➤ Today:
 - ➤ PRE Practice
 - ➤ SSA Demo
 - ➤ SSA Goodbye







Clarification: Inserting computations to introduce full redundancy

- \triangleright Among the partially available expressions at n, we can place those at its entry that
 - ➤ are either anticipated locally, or can be placed at its exit (PpOut) and don't get killed; and
 - \succ for every predecessor p, are either available at p's exit or can be placed at p's exit.

Insertion happens only at the exit of a block;

hence PPIn has expressions that can be pushed to the exit of predecessors.

```
PpIn[n] = PavIn[n] ∩ (AntLoc[n] ∪ (PpOut[n] - Kill[n]))

∩ ∀p∈pred[n] (AvOut[p] ∪ PpOut[p])
```



Where do we insert new computations then?!

- ➤ We don't want to insert an expression at node *n* if it can be placed at a predecessor of *n*.
- \blacktriangleright Insert an expression *e* at the exit of a node *n* if
 - \triangleright Exit of *n* is a possible placement point for *e*;
 - > e is not already available at n; and
 - moving *e* further up does not work because either e cannot be placed at *n*'s entry or because *n* kills *e*.

```
Insert[n] = PpOut[n] \cap !AvOut[n] \cap (!PpIn[n] \cup Kill[n])
```



Finally, removing existing computations

- ➤ We have identified partial redundancies and added expressions to convert them to full redundancies.
- ➤ Now we can remove full redundancies!

➤ From a node *n*, we can remove the computation of expressions that are anticipated locally and can be placed at *n*'s beginning:

Remove[n] = AntLoc[n]
$$\cap$$
 PpIn[n]



PRE Practice

```
AvIn[n] = ∀p∈pred[n] ∩ AvOut[p]

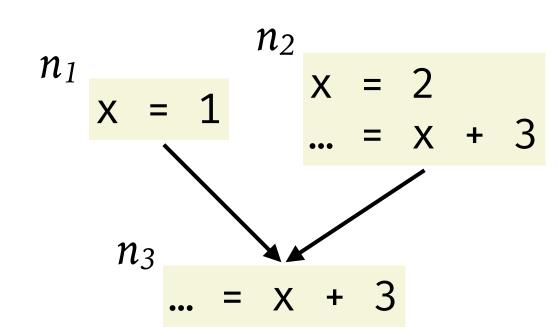
AvOut[n] = Gen[n] ∪ (AvIn[n] - Kill[n])

PavIn[n] = ∀p∈pred[n] ∪ PavOut[p]

PavOut[n] = Gen[n] ∪ (PavIn[n] - Kill[n])

AntOut[n] = ∀s∈succ[n] ∩ AntIn[s]

AntIn[n] = AntLoc[n] ∪ (AntOut[n] - Kill[n])
```



```
PPOut[n] = \forall s \in succ[n] \cap PPIn[s]
PpIn[n] = PavIn[n] \cap (AntLoc[n] \cup (PpOut[n] - Kill[n]))
\cap \forall p \in pred[n] (AvOut[p] \cup PpOut[p])
Insert[n] = PpOut[n] \cap !AvOut[n] \cap (!PpIn[n] \cup Kill[n])
Remove[n] = AntLoc[n] \cap PpIn[n]
```



PRE Notes

➤ Our PRE algorithm (1979) is bidirectional:

```
PPOut[n] = ∀s∈succ[n] ∩ PPIn[s]

PpIn[n] = PavIn[n] ∩ (AntLoc[n] ∪ (PpOut[n] - Kill[n]))

∩ ∀p∈pred[n] (AvOut[p] ∪ PpOut[p])
```

- This makes it complex as well as expensive.
- ➤ Researchers have later proposed other formulations of PRE that are combinations of multiple unidirectional analyses.
- ➤ A paper as recently as 2024 on reducing the number of required unidirectional analyses!
- ➤ Think how one could utilize SSA form to improve PRE.
 - ➤ Called the SSAPRE algorithm.

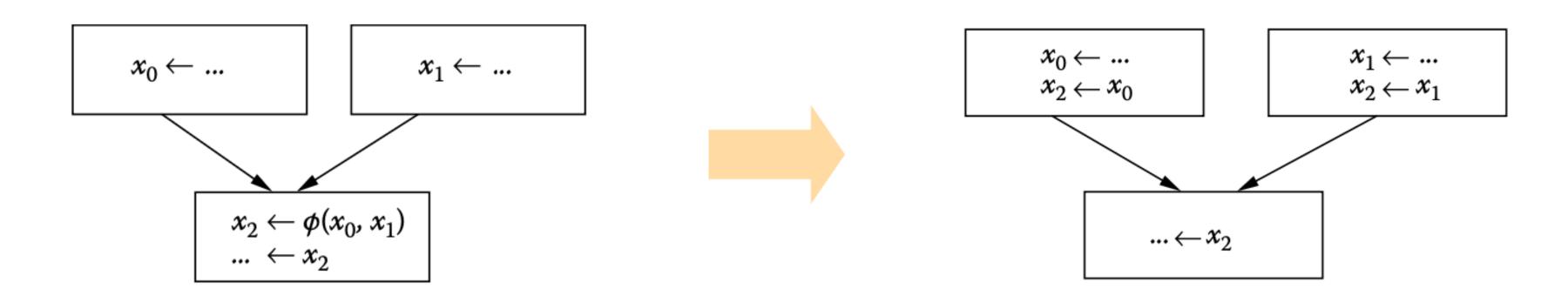


SSA Destruction



Removal of Ф Functions

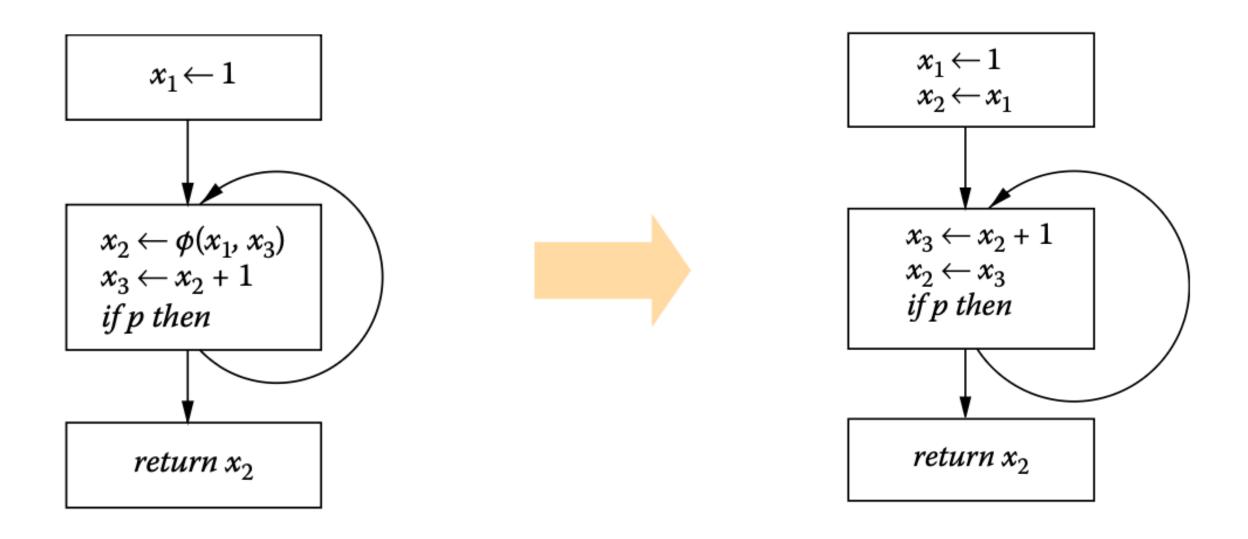
- > Φ functions have no equivalence in hardware.
- They need to be removed after performing enabled optimizations.
- ➤ Naive removal:
 - ➤ Insert a copy statement at the end of each predecessor node, corresponding to each argument:



Not so straightforward in multiple scenarios though.



The Lost Copy Problem

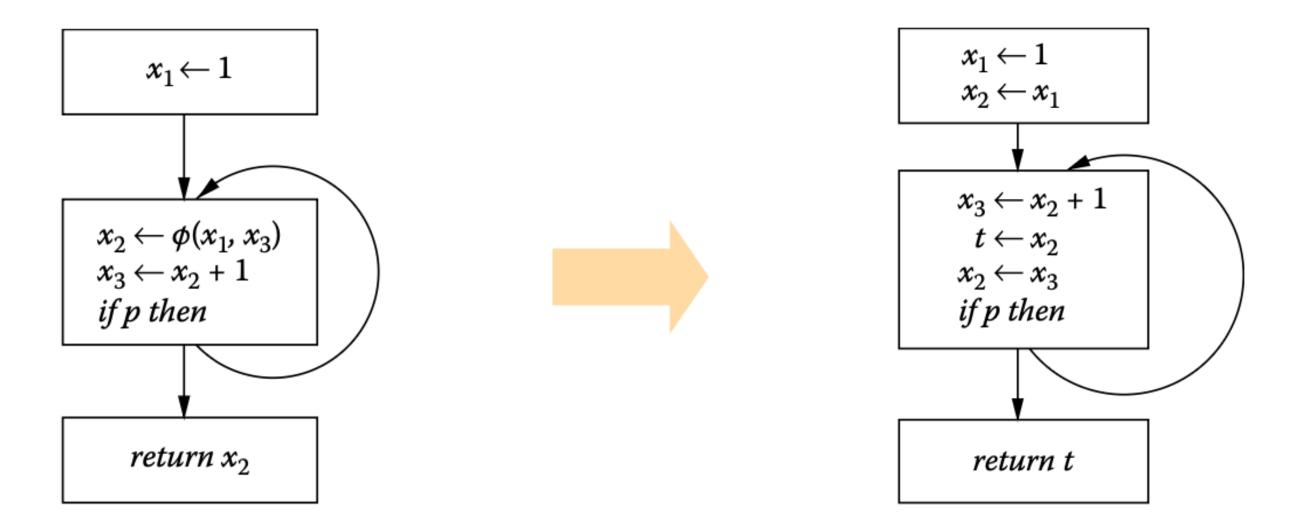


 \triangleright Wrong value of x2 will be returned by the enclosing function.



The Lost Copy Problem

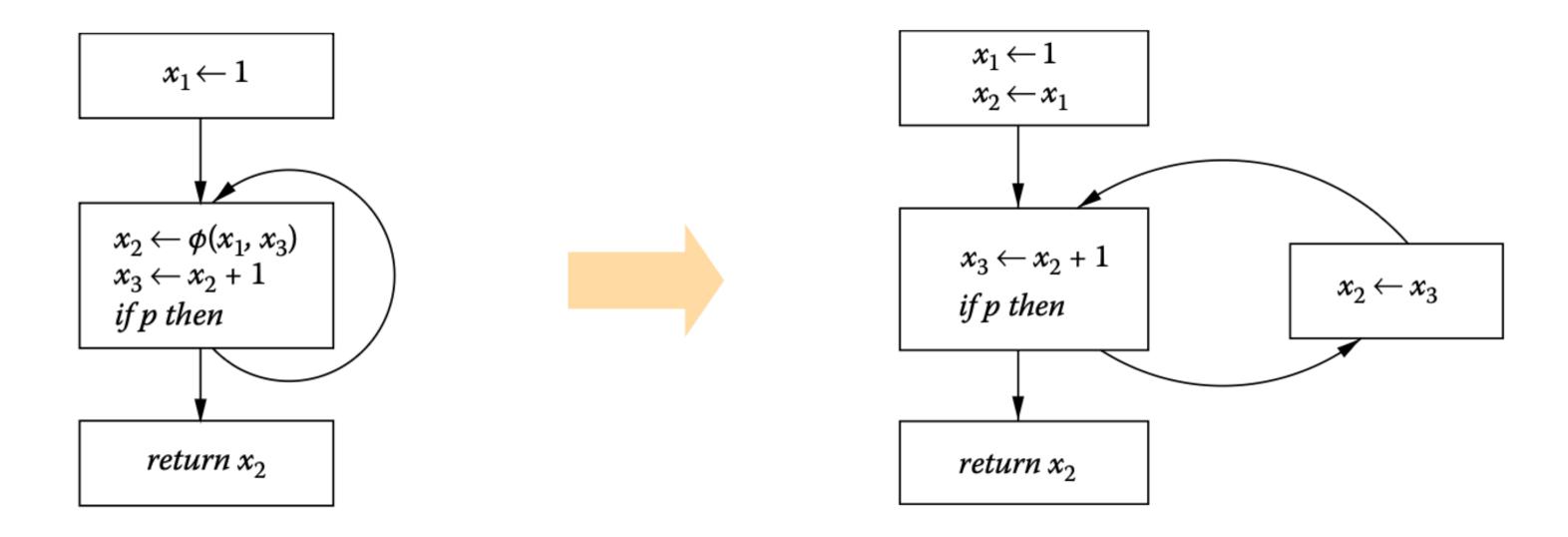
- \triangleright Cause: x2 was live beyond the scope of the Φ function that defined it.
- \triangleright Solution 1: Introduce a temporary variable to save the original value of x2:





The Lost Copy Problem

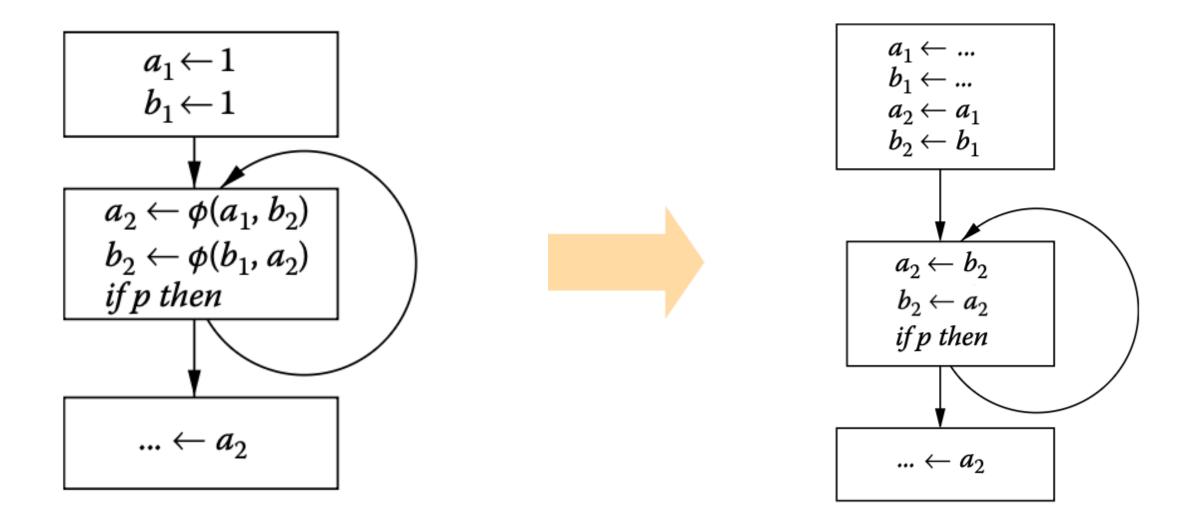
- \triangleright Cause: x2 was live beyond the scope of the Φ function that defined it.
- ➤ Solution 2: Split the *critical* edge and insert the overwrite in a new basic block:

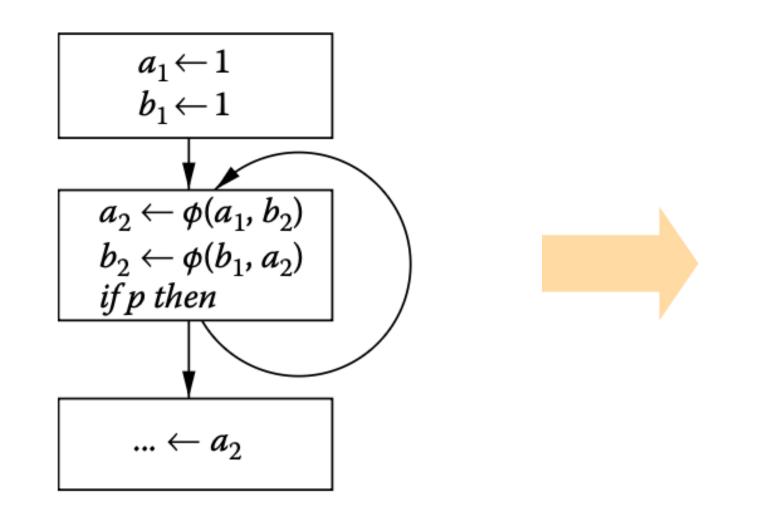


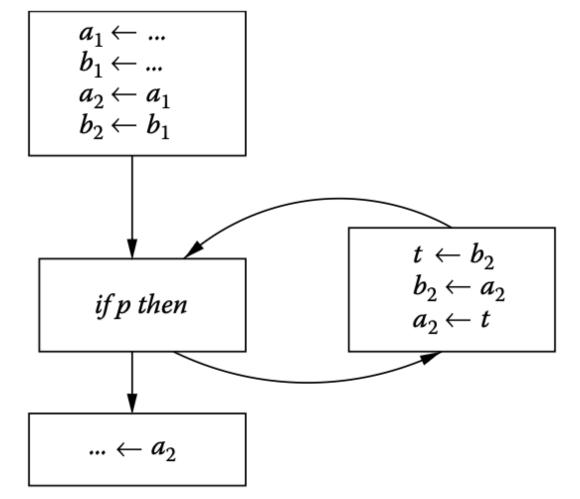


The Swap Problem

- ➤ a2 gets assigned the value of b2 instead of a1, in the first iteration.
- ➤ Can be again solved with edge splitting:







Insight: The Φ functions at a node have no inherent order.



SSA Demo

Bhavya Hirani NIT Surat Intern, CompL



A2 Discussion

