## Energy/Power and DVFS/DFS

Topics: Energy/Power and DVFS/DFS

Slide courtesy: Dr. Rajeev Balasubramonian

## **Power Consumption Trends**

- Dyn power  $\alpha$  activity x capacitance x voltage<sup>2</sup> x frequency
- Capacitance per transistor and voltage are decreasing, but number of transistors is increasing at a faster rate; hence clock frequency must be kept steady
- Leakage power is also rising; is a function of transistor count, leakage current, and supply voltage
- Power consumption is already between 100-150W in high-performance processors today
- Energy = power x time = (dynpower + lkgpower) x time

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Total power = dynamic power + leakage power
= 80W x 50% + 20W
= 60W
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- If processor A consumes 1.4x the power of processor B, but finishes the task in 20% less time, which processor would you pick:
  - (a) if you were constrained by power delivery constraints?
  - (b) if you were trying to minimize energy per operation?
  - (c) if you were trying to minimize response times?

- If processor A consumes 1.4x the power of processor B, but finishes the task in 20% less time, which processor would you pick:
  - (a) if you were constrained by power delivery constraints?Proc-B
  - (b) if you were trying to minimize energy per operation? Proc-A is 1.4x0.8 = 1.12 times the energy of Proc-B
  - (c) if you were trying to minimize response times?

    Proc-A is faster, but we could scale up the frequency
    (and power) of Proc-B and match Proc-A's response
    time (while still doing better in terms of power and
    energy)

# Relation b/w frequency and time

A processor's frequency is

- (a) increased by 30%
- (b) decreased by 40%

Find the percentage change in execution time.

Answer: (a) New time = 1/130% = 100/130 = 0.769 = 76.9%

Thus, execution time has reduced by 23.1%

(b) new time = 1/60% = 100/60 = 1.66 = 166.66%

Thus, execution time has increased by 66.66%.

Energy/Power Saving Techniques

## **DFS AND DVFS**

## Reducing Power and Energy

### DFS: Dynamic frequency scaling --

- Control knob: reduce frequency
- Result: reduces dynamic power, but increases energy

### DVFS: Dynamic voltage and frequency scaling

- Control knob: reduce frequency and voltage
- Result: reduces both dynamic and static power and energy
- voltage drop leads to slow transistors, so frequency of operation is also reduced

- Processor-A at 3 GHz consumes 80 W of dynamic power and 20 W of static power. It completes a program in 20 seconds.
- A. Find energy of this processor
- B. Find energy on scaling frequency down by 20%?
- C. Find energy on scaling freq and voltage down by 20%

- Processor-A at 3 GHz consumes 80 W of dynamic power and 20 W of static power. It completes a program in 20 seconds.
- **A.** Energy = 100 \* 20 = 2000 Joules
- B. Energy on scaling frequency down by 20%

  New dynamic power = 64W; New static power = 20W

  New execution time = 25 secs (assuming CPU-bound)

  Energy = 84 W x 25 secs = 2100 Joules
- C. Energy on scaling freq and voltage down by 20% New dynamic power = 41W; New static power = 16W; New exec time = 25 secs; Energy = 1425 Joules