Srijal Shekhar Poojari

Embedded Systems · Robotics · Controls

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EDUCATION

University of Maryland

M.S. in Systems Engineering (Control Systems) · GPA 3.97/4.0

College Park, USA

- Advisor: Dr. Derek A. Paley
- Thesis: ReZoom: A self-driving electric scooter

SARDAR PATEL INSTITUTE OF TECHNOLOGY | UNIVERSITY OF MUMBAI

Mumbai, India 2015–2019

B.E. in Electronics Engineering · GPA 8.49/10

- Capstone: Design of Tethered Multirotor System for High Payload Applications

APPOINTMENTS

ROBOTICS AND AUTONOMY LAB

College Park, USA

2021-

Graduate Research Assistant · 1 year 7 mos. (ongoing)

- Leading the development of autonomous e-scooters with Prof. Derek Paley.
- Developed state estimation fusing IMU, GPS and Wheel Odometry accurate within 2 meters upon travelling 350m in a dense urban environment.
- Developed planning + navigation of scooter and demonstrated autonomous travel for 230 meters along campus roads.

SP PRODUCT DEVELOPMENT CELL

Mumbai, India

Research Associate • 1 year

- Worked on industrial power electronics consultancy projects with Prof. R. R. Sawant and Prof. Y. S. Rao.
- Developed robust embedded software for Microchip dsPIC, TI C2000, and Microchip ATmega family of microcontrollers.
- Implemented the above with high power driver circuits for sensorless (no encoder) brushless and brushed DC motor control using back EMF sensing.
- Developed a 4.5 kW (135V, 35A) battery charger for the Indian Railways with run time short circuit recovery, earth fault detection and other protections.

DRISHTI WORKS

Mumbai, India

2018

Robotics Engineer Intern · 2 mos.

- Developed the sensing, power distribution and IMU system for AURUS, a beach cleaning robot.
- Implemented a fault-tolerant communication pipeline between the computing stack and hardware stack using ROS.

FRACTAL ANALYTICS

Mumbai, India

Project Intern · 1 mo.

2017

Developed applications using Unity (C#) on the Microsoft HoloLens Mixed Reality (MR) headsets for displaying statistical results in the form of interactive holograms.

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Indian Institute of Technology, Bombay

Summer Intern · 3 mos.

Mumbai, India

- Created self-reconfigurable robot modules inspired by the Dtto Modular Robot.
- Developed virtual simulations of the same modules on V-REP (now CoppeliaSim) with bluetooth control.

SELECT PROJECTS



PROJECT ATLAS

B.E. Capstone Project · 9 mos.

Mumbai, India 2018–2019

- Built a tethered multirotor with an All Up Weight (AUW) of 10 Kgs.
- Designed 140V to 32V@20A step-down switching converters (855kHz) after simulating the design for efficiency and two PCB revisions to optimize for heat, small form-factor, and low weight.
- **Second place** across all undergraduate departments for Technical Paper Presentation.

ROOM OCCUPANCY INDICATING SYSTEM

B.E. Semester VI Project · 3 mos.

Mumbai, India

2018

- Created a network of small (3"x3") wireless sensors to detect human presence in a office room, across multiple such rooms.
- Developed PCBs and 3D printed cases to integrate low power sensing and wireless communication across multiple devices placed in different rooms of an office space.

EYANTRA ROBOTICS COMPETITION

Mumbai, India 2016–2017

- All-India Robotics Competition 5 mos.First place amongst 162 teams across India.
- Led a team of 4 in implementing algorithms for localization and motion planning of a Firebird V robot.
- Used OpenCV with Python for image segmentation of the arena to detect and distinguish shapes, areas and colors of objects for the given challenge.

CYKLO

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Mumbai, India

Startup · 1 year 6 mos.

- Led hardware development at CYKLO, a point-to-point bicycle sharing service, started in 2015 at SPIT, Mumbai.
- Designed, built and programmed several hardware prototypes for the automated cycle locking system - including the locking mechanism, electronic controller and network interface.
- First place and award of INR 300,000 in InterThrone 2017 for the automated cycle locking prototypes.

OTHER AWARDS AND HONORS

University of Maryland's Outstanding Graduate Research Assistant Award — awarded to the top 2% (80 of over 4000) of campus Graduate Assistants each year.

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2022	First place (Mad Scientist) for best overall design in IEEE USA R2 Brown Bag - Analog and Digital Circuit Design.
2016	First place in department level Circuit Troubleshooting Competition at undergraduate institute.
2016	Second place out of 459 entries in the Arduino All-The-Things Contest on Instructables for my project called "The Companion IC" .
2015	First place in CodeChamps, a programming competition for all freshman students at my undergraduate institution. Used C++.
Теасн	HING EXPERIENCE

DYNAMICS OF AEROSPACE SYSTEMS

College Park, USA

Graduate Teaching Assistant

Instructor

- Teaching Assistant for ENAE 301: Dynamics of Aerospace Systems, taught by Prof. Derek Paley.
- Responsibilities included conducting weekly recitations, office hours and grading for 80 students.

SIGNAL PROCESSING ON DSPs

Mumbai, India

2019

 Invited for workshops to train faculty on incorporating embedded Digital Signal Processor (DSP) development boards in their curriculum at three institutes of the University of Mumbai: K.J. Somaiya, MPSTME and AIKTC.

ROBOTIC VISION Mumbai, India 2019

Undergraduate Teaching Assistant

- Teaching Assistant for ETRX OE2: Robotic Vision, taught by Prof. K.T. Talele.
- Responsible for designing, conducting and instructing lab sessions for a batch of about 20 students.

MULTIPLE WORKSHOPS ON EMBEDDED SYSTEMS DEVELOPMENT Instructor

Mumbai, India 2016-2019

- Conducted four workshops titled "Embedded Systems Design", "Microcontrollers, Sensors and Arduino", "ESP8266 and MQTT" and "PCB Making and Robotic Systems" throughout my undergraduate years.
- Instructed about 15 to 30 students in these workshops spanning 2 days each.

OTHER ACTIVITIES

2022	Facilitator for the Maryland LEAD Program for two semesters ♂.
202I	Graduate Innovation Fellow ♂.
2018	Student Technical Committee Member in Circuit Troubleshooting Event 2018 at Sardar Pate Institute of Technology.
2018	Certified on edX for course Robotics: Fundamentals &.
2016	Participated in workshop on MSP-FPGA Hardware and Software Co-design

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