# **Experiment 2: Error Detection using CRC-CCITT**

Aim: To apply CRC (CCITT Polynomial) for error detection

**Objective:** After carrying out this experiment, students will be able to:

- Apply CRC CCITT to develop codes for error detection
- Analyze how this CRC is able to detect bit errors irrespective of their length and position in the data

**Problem statement:** You are required to write a program that uses CRC to detect burst errors in transmitted data. Initially, write the program using the CRC example you studied in class. Your final program should ask the user to input data and choose a generator polynomial from the list given in the figure below. Your program is required to calculate the checksum and the transmitted data. Subsequently, the user enters the received data. Applying the same generator polynomial on the received data should result in a remainder of 0.

Name	Polynomial	Application
CRC-8	x8 + x2 + x + 1	ATM header
CRC-10	x <sup>10</sup> + x <sup>9</sup> + x <sup>5</sup> + x <sup>4</sup> + x <sup>2</sup> + 1	ATM AAL
CRC-16	x <sup>6</sup> + x <sup>12</sup> + x <sup>5</sup> +1	HDLC
CRC-32	x <sup>32</sup> + x <sup>26</sup> + x <sup>23</sup> + x <sup>22</sup> + x <sup>16</sup> + x <sup>12</sup> + x <sup>10</sup> + x <sup>8</sup> + x <sup>7</sup> + x <sup>5</sup> + x <sup>4</sup> + x <sup>2</sup> + x + 1	LANs

**Analysis:** While analyzing your program, you are required to address the following points:

- How is this method different from 2D parity scheme that you have implemented previously?
- What are the limitations of this method of error detection?

#### **MARKS DISTRIBUTION**

Component	Maximum Marks	Marks Obtained
Preparation of Document	7	
Results	7	
Viva	6	
Total	20	

**Submitted by:** K Srikanth

Register No: 17ETCS002124



# 1. Algorithm/Flowchart

- 1. Start
- 2. Declaring Variables
- 3. Input the choice
- 4. Case 1: Redundancy generation (Bitstream to be transmitted)
  - 1. Input the bitstream:
  - 2. N=size of bitstream
  - 3. If N>9 then choose generator polynomial for ATM header Else If N>11 then choose generator polynomial for ATM AAL Else If N>17 then choose generator polynomial for HDLC Else If N>33 then choose generator polynomial for LANs Else print "Invalid input"
  - 4. R=Selected polynomial
  - 5. L=length(R)
  - 6. Append L-1 zeroes to M and store it in M
  - 7. T=M
  - 8. I=0
  - 9. While(T[I+L] is not end of line)

```
If T[I] is '1' then T [ I: I+L] XOR (T [ I: I+L], R)
```

- 10. Output Remainder = T [ I: I+L]
- 11. Output Message = XOR (M, T)

# 5. Case 2: Redundancy Check (Bitstream received)

- 1. Input the received bitstream.
- 2. Select the generating polynomial
- 3. R=Selected polynomial
- 4. L=length(R)
- 5. I=0
- 6. While (T[I+L] is not end of line)
  - a. If T[I] is '1' then T [ I: I+L] XOR (T [ I: I+L], R)
- 7. Remainder = T[I:I+L]
- 8. If **Remainder** is 00000... then output "Error Free"
- 9. Else print **ERROR**= Remainder

# 6. Stop



### 2. Program

# **C Program**

```
#include <string.h>
int main(int argc, char** argv) {
 char m[50],r[50],t[100],c[100];
   int n,l;
   int i=0,j,count,choice;
   printf("******* MENU *******\n");
   printf("\n1.Redundancy Generation\n2.Redundancy Check\nEnter your choice:");
   scanf("%d",&choice);
   printf("\n");
   switch(choice){
        case 1:
            printf("Input the bitstream: ");
            scanf("%s",&m);
            n=strlen(m);
           if(n>33){strcpy(r,"100000100110000010001110110110111");}
            else if(n>17){strcpy(r,"10001000000100001");}
            else if(n>11){strcpy(r,"11000110101");}
else if(n>9){strcpy(r,"100000111");}
                printf("please enter a bit stream whose length is greater than generator polynomial\n");
                exit(0);
```

Figure 1 C Program Code for the given problem statement

```
strcpy(t,m);
l=strlen(r)-1;
while(i<l){
    t[n+i]='0';
    i++;
strcpy(c,t);
t[n+i]='\0';
i=0;
n=strlen(t);
while(t[i+l]!='\0'){
   if(t[i]=='1'){
        for(j=0;j<=l;j++){
            if(t[i+j]==r[j]){
                t[i+j]='0';
            }else{
                t[i+j]='1';
    }}}
        i++;
printf("Remainder: %s\n",t);
n=strlen(t);
for(j=0;j<n;j++){
            if(t[j]==c[j]){
                t[j]='0';
            }else{
                t[j]='1';
printf("Message: %s\n",t);
```

Figure 2 C Program Code for the given problem statement. Continued



```
case 2:
  scanf("%s",&m);
   printf("Generating Polynomial:\n");
  printf("1.100000111\n");
  printf("2.11000110101\n");
   printf("3.10001000000100001\n");
  printf("4.100000100110000010001110110110111\n");
   scanf("%d",&choice);
   n=strlen(m);
   switch(choice){
      case 1:strcpy(r,"100000111");break;
      case 2:strcpy(r,"11000110101");break;
case 3:strcpy(r,"10001000000100001");break;
case 4:strcpy(r,"1000001001100000110110110110111");break;
       default:printf("invalid choice\n");
   strcpy(t,m);
    n=strlen(t);
   i=0;
   l=strlen(r)-1;
       if(t[i]=='1'){
           for(j=0;j<=l;j++){
               if(t[i+j]==r[j]){
                    t[i+j]='0';
                     t[i+j]='1';
        if(t[i]=='1')count++;
   if(count>0)printf("Error: %s\n",t);
   else printf("Error free-> remainder:%s\n",t);
   break;
   printf("Invalid choice");
```

# 3. Results

Figure 3 C Program Code for the given problem statement... Continued

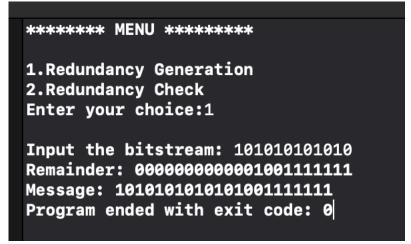


Figure 4 C Program output for the given problem statement.



```
****** MENU ******

1.Redundancy Generation
2.Redundancy Check
Enter your choice:2

Input the bitstream: 1010101010101010
Generating Polynomial:
1.100000111
2.11000110101
3.10001000000100001
4.10000010011000001110110110111
1
Error: 0000000011110101
Program ended with exit code: 0
```

Figure 5 4 C Program output for the given problem statement.

### 4. Analysis and Discussions

# **Redundancy Generator (Sender side):**

- The binary data (message) is first augmented by adding L-1 zeros in the end of the data
- Use modulo-2 binary division to divide binary data by the key and store remainder of division.
- In each step, a copy of the divisor R (or generating polynomial) is XORed with the L bits of the dividend (or message).
- The result of the XOR operation (remainder) is (L-1) bits, which is used for the next step after 1 extra bit is pulled down to make it L bits long.
- When there are no bits left to pull down, we have a result. The (L-1)-bit remainder which is appended at the sender side.

#### Redundancy check (Receiver side):

Perform modulo-2 division again and if remainder is 0, then there are no errors.

How is this method different from 2D parity scheme that you have implemented previously?



- In 2D parity in case of burst errors, there is possibility that the error data can be accepted by the receiver as correct data. Thus, 2D parity cannot fully detect burst errors, whereas CRC can successfully detect single bit errors as well as burst errors.
- In CRC, if error occurs, then the remainder at receiver will not be 0 at any case.
- The data is accepted at the receiver only if the remainder obtained after division is 0.

#### What are the limitations of this method of error detection?

- CRC uses redundant bits for error detection, which makes extra bits getting added hence process is complex for computation.
- Time consuming. As we can see execution time is much higher than that of 2d parity bit error detection's

#### 5. Conclusions

The user is given a choice to enter a bitstream thereby a generating polynomial is chosen by the program itself and producing message to be transmitted. Receiver end tries to decode the message and checks whether the remainder is zero. CRC is easy to detect single bit as well as multiple burst errors. They can easily be implemented on several hardware and software programs.

#### 6. Comments

#### a. Limitations of the experiment

At the receiver's end automatic generating polynomial selection is not implemented hence pushing the users to select polynomial by themselves.

# b. Limitations of the results obtained

The transmitted message has to be input separately instead of directly giving a choice to detect the error after encoding message.

#### c. Learning

CRC, Burst errors and advantages of CRC over 2d parity

